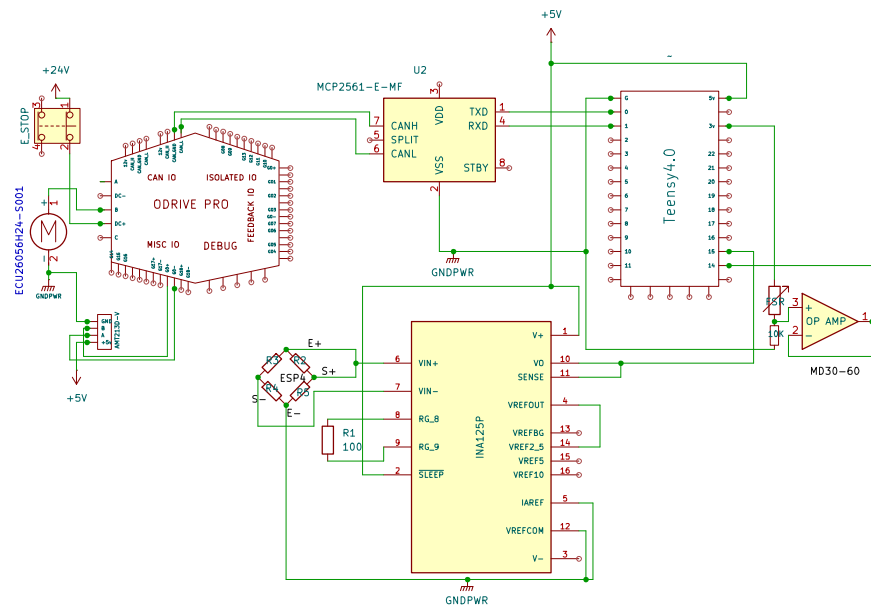


Circuit Diagram



Control Block Diagram

