NXP Semiconductors UM11029

Chapter 5: LPC84x ISP and IAP

## 5.5.18 Read/write FAIM page

This command is used to read/write FAIM page.

Table 35. USART ISP Read/Write FAIM Page command

Command	0	
Input	FAIM access:	
	0 = FAIM Page read	
	1 = FAIM Page write	
	FAIM page number: 0 - 7	
	FAIM configuration value: user defined (if FAIM access is page write)	
Return Code	CMD_SUCCESS	
	PARAM_ERROR	
	INVALID_COMMAND	
	INVALID_PAGE	
Description	This command is used to read/write FAIM page	
Example	"O 0 5 <cr><lf>" reads the FAIM page 5.</lf></cr>	
	"O 1 7 <cr><lf>" initiates a write to FAIM page 7. When the system is ready to receive the data it responds with 0<cr><lf> [CMD_SUCCESS]. The host should send a raw binary data of 4 bytes. FAIM programming starts when the fourth byte is received and the ISP command responds with 0<cr><lf> [CMD_SUCCESS] error code after the programming is completed successfully.</lf></cr></lf></cr></lf></cr>	

## 5.5.19 ISP/IAP Error codes

These error codes are located in the error.h file.

Table 36. ISP/IAP Error codes

Return Code	Error code	Description
0x0	CMD_SUCCESS	Command is executed successfully. Sent by ISP handler only when command given by the host has been completely and successfully executed.
0x1	INVALID_COMMAND	Invalid command.
0x2	SRC_ADDR_ERROR	Source address is not on word boundary.
0x3	DST_ADDR_ERROR	Destination address is not on a correct boundary.
0x4	SRC_ADDR_NOT_MAPPED	Source address is not mapped in the memory map. Count value is taken into consideration where applicable.
0x5	DST_ADDR_NOT_MAPPED	Destination address is not mapped in the memory map. Count value is taken into consideration where applicable.
0x6	COUNT_ERROR	Byte count is not multiple of 4 or is not a permitted value.
0x7	INVALID_SECTOR/INVALID_PAGE	Sector/page number is invalid or end sector number is greater than start sector number.
0x8	SECTOR_NOT_BLANK	Sector is not blank.
0x9	SECTOR_NOT_PREPARED_ FOR_WRITE_OPERATION	Command to prepare sector for write operation was not executed.
0xA	COMPARE_ERROR	Source and destination data not equal.
0xB	BUSY	Flash programming hardware interface is busy.
0xC	PARAM_ERROR	Insufficient number of parameters or invalid parameter.
0xD	ADDR_ERROR	Address is not on word boundary.

NXP Semiconductors UM11029

Chapter 5: LPC84x ISP and IAP

Table 36. ISP/IAP Error codes

Return Code	Error code	Description
0xE	ADDR_NOT_MAPPED	Address is not mapped in the memory map. Count value is taken into consideration where applicable.
0xF	CMD_LOCKED	Command is locked.
0x10	INVALID_CODE	Unlock code is invalid.
0x11	INVALID_BAUD_RATE	Invalid baud rate setting.
0x12	INVALID_STOP_BIT	Invalid stop bit setting.
0x13	CODE_READ_ PROTECTION_ENABLED	Code read protection enabled.
0x14	-	Reserved.
0x15	USER_CODE_CHECKSUM	User code checksum is invalid.
0x16	-	Reserved.
0x17	EFRO_NO_POWER	FRO not turned on in the PDRUNCFG register.
0x18	FLASH_NO_POWER	Flash not turned on in the PDRUNCFG register.
0x19	-	Reserved.
0x1A	-	Reserved.
0x1B	FLASH_NO_CLOCK	Flash clock disabled in the AHBCLKCTRL register.
0x1C	REINVOKE_ISP_CONFIG	Reinvoke ISP not successful.
0x1D	NO_VALID_IMAGE	Invalid image
0x1E	FAIM_NO_POWER	FAIM not turned on in the PDRUNCFG register.
0x1F	FAIM_NO_CLOCK	FAIM clock disabled in the AHBCLKCTRL register.