

Clock and VGA signals

File: vгатiming.kicad_sch

Clock signals

File: vg_memsignal.kicad_sch

Video address and counters

File: vga_vidmem.kicad_sch

RAM

File: ram.kicad_sch

cpu_rom

File: cpu_rom.kicad_sch

Devices, Serial, VIA

File: Communication_sch.kicad_sch

address_decoder

File: address_decoder.kicad_sch

SoftSwitches

File: softswitches.kicad_sch

Language Card and Sound

File: banking.kicad_sch

TextMode

File: text.kicad_sch

PLL

File: PLL.kicad_sch

GraphicsV2

File: graphicsv2.kicad_sch

romdisk

File: romdisk_sch.kicad_sch

Mockingboard

File: Mockingboard.kicad_sch

LowRes

File: LowRes.kicad_sch

Power

File: Power.kicad_sch

Power2

File: power2.kicad_sch

DiskEmulator

File: diskemu.kicad_sch

H1 MountingHole

H2 MountingHole

H3 MountingHole

H4 MountingHole

H5 MountingHole

H6 MountingHole

H7 MountingHole

H8 MountingHole

H9 MountingHole

H10 MountingHole

Sheet: /
File: 3ric.kicad_sch

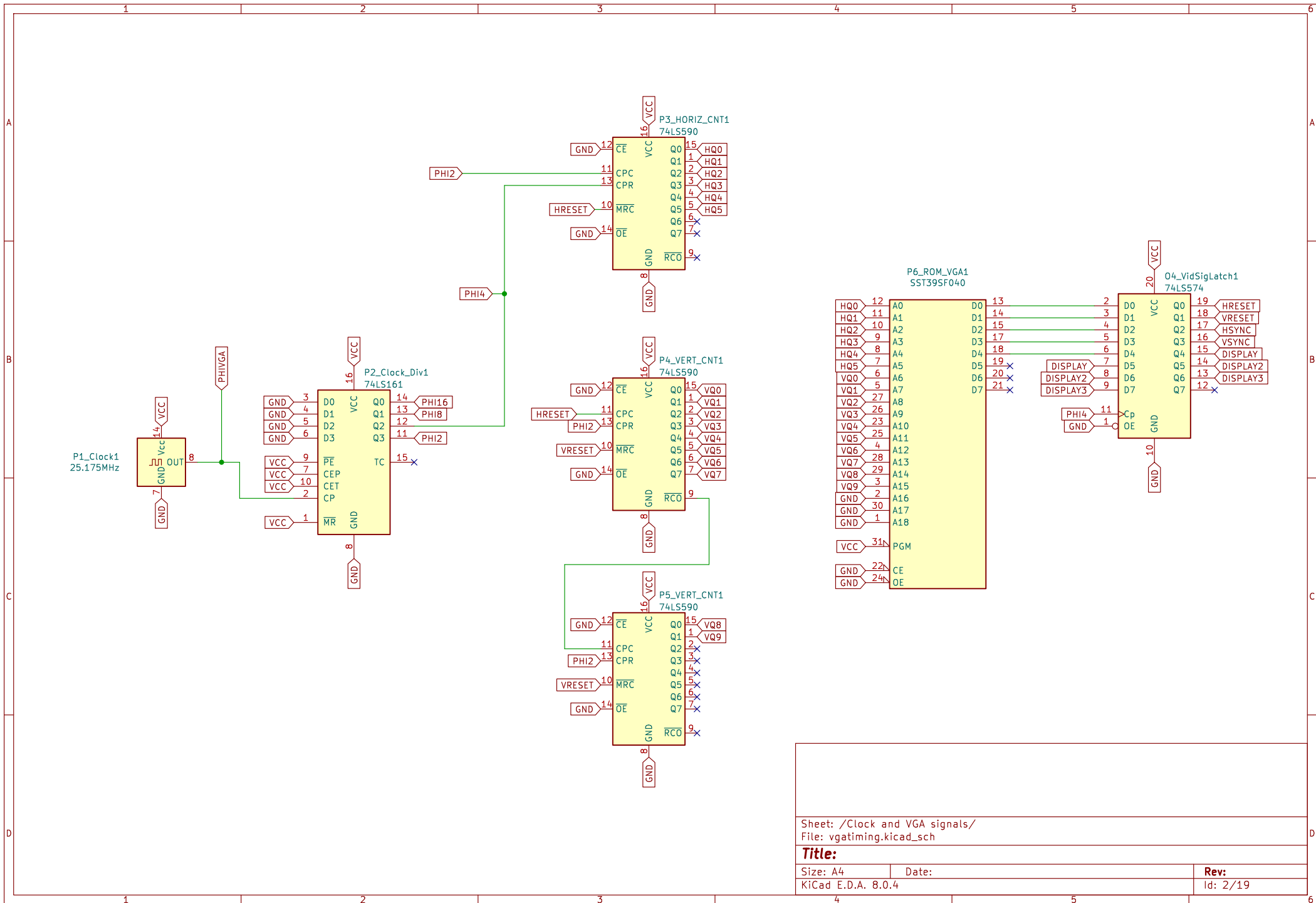
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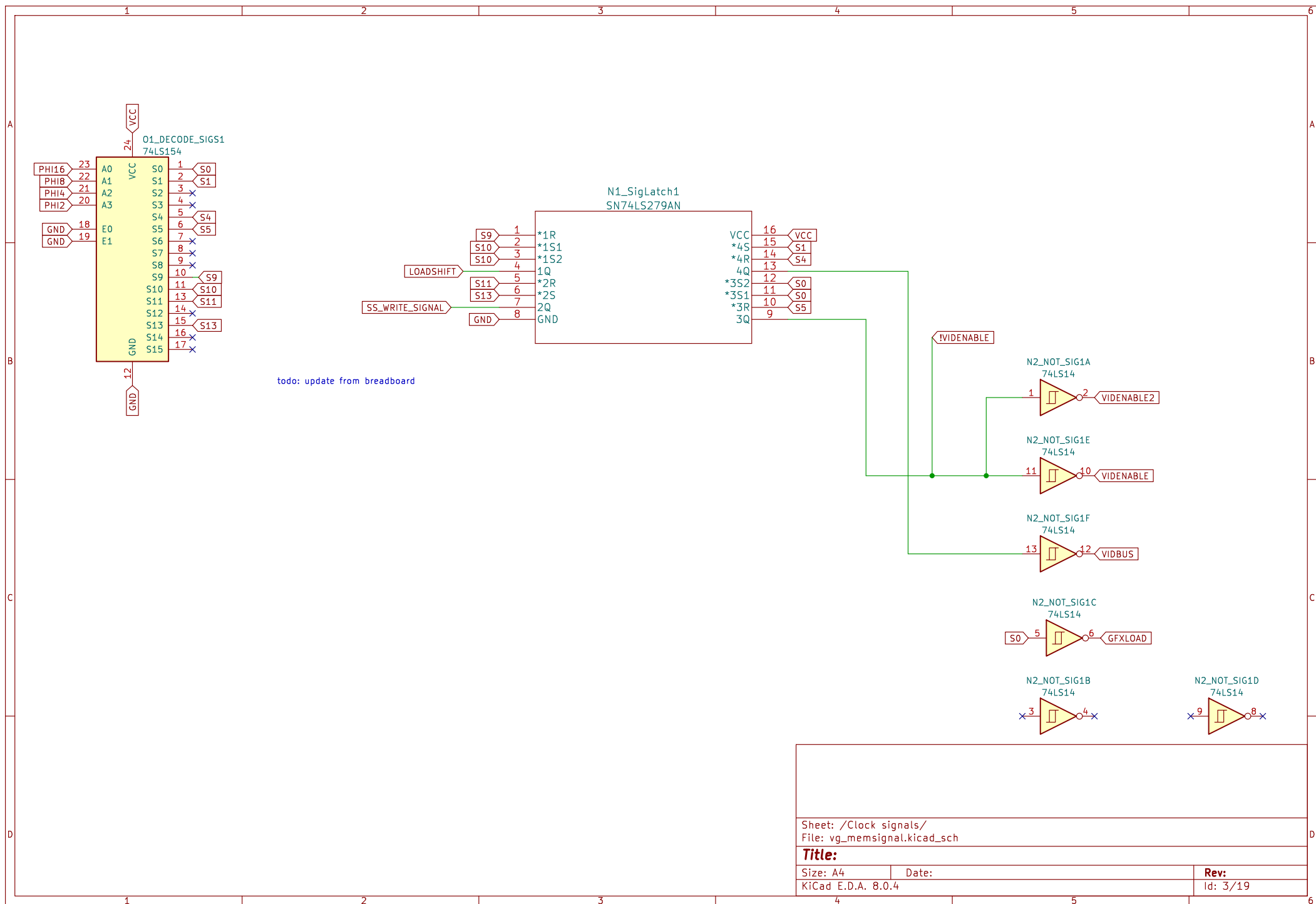
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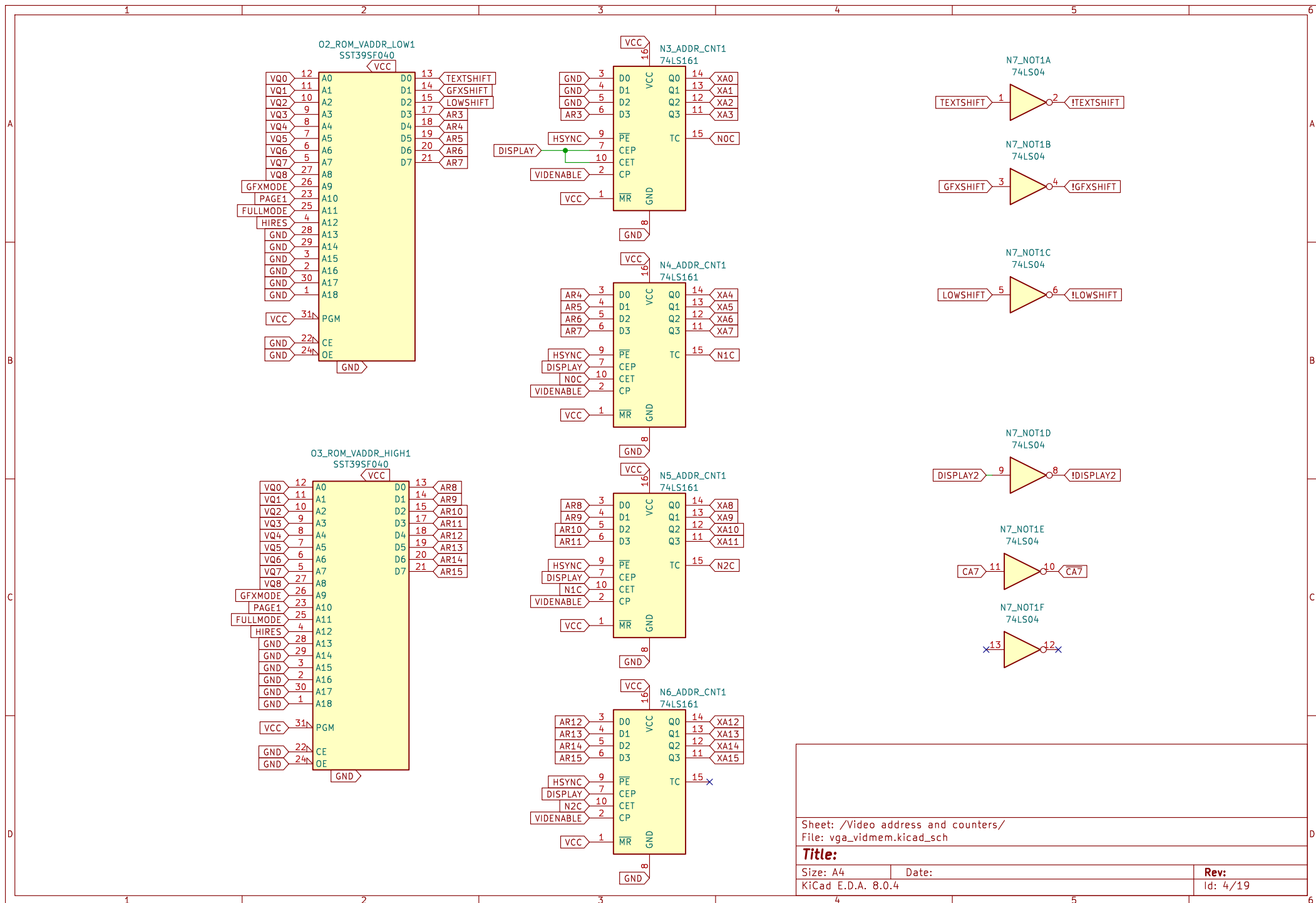
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Rev:
Id: 1/19







Sheet: /Video address and counters/
File: vga_vidmem.kicad_sch

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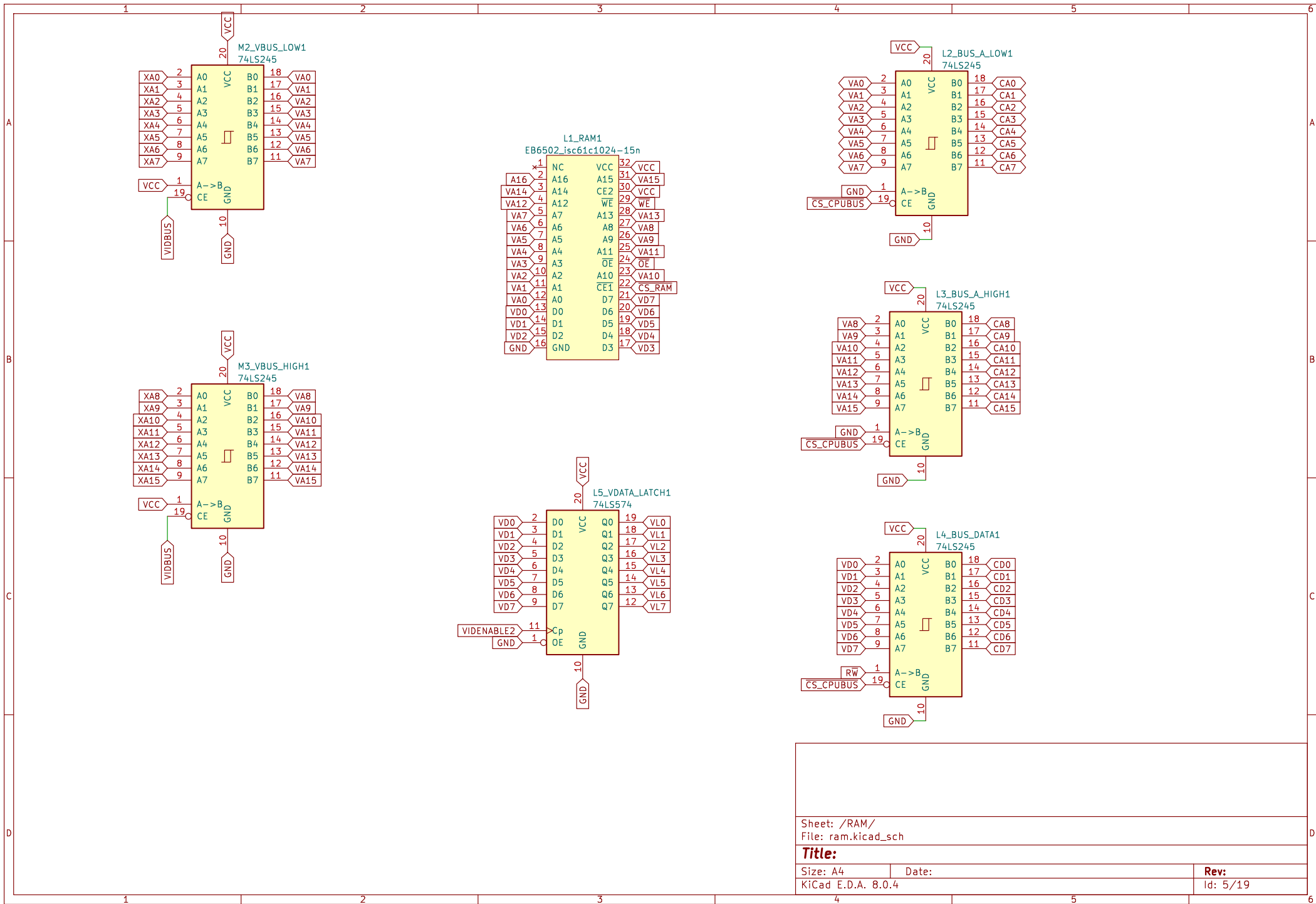
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Rev:

KiCad E.D.A. 8.0.4

Id: 4/19



Sheet: /RAM/
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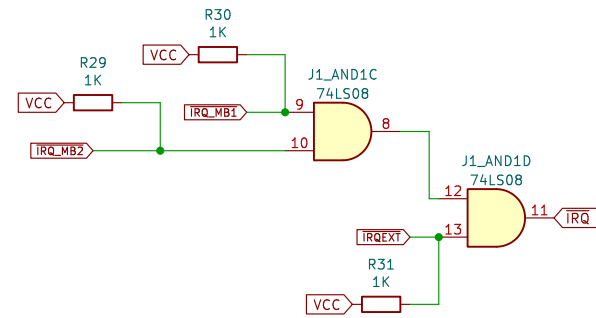
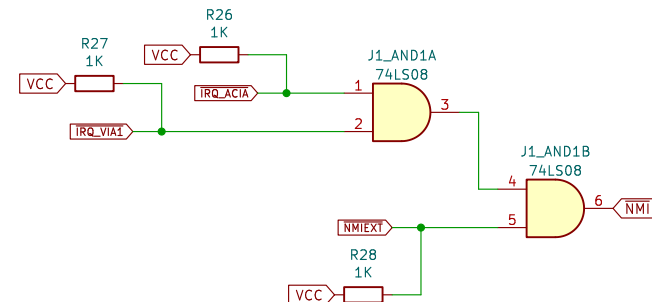
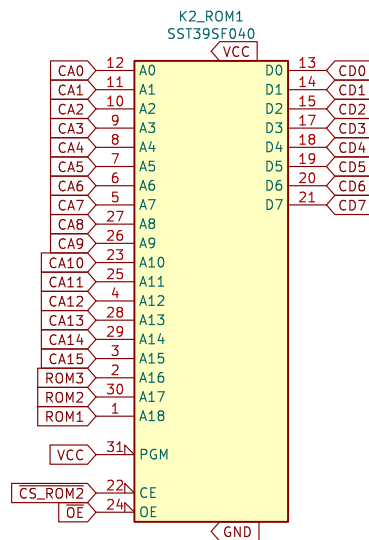
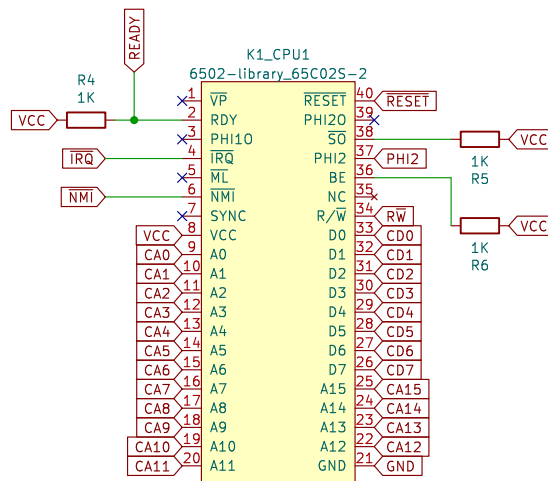
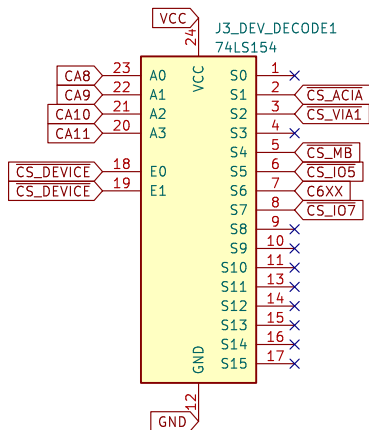
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Date:

Rev:

Id: 5/19



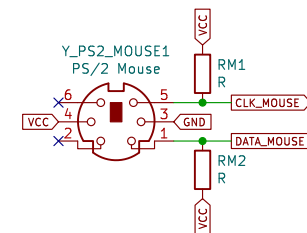
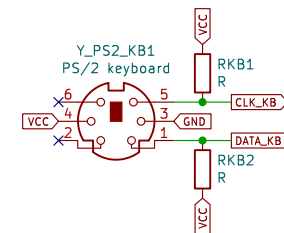
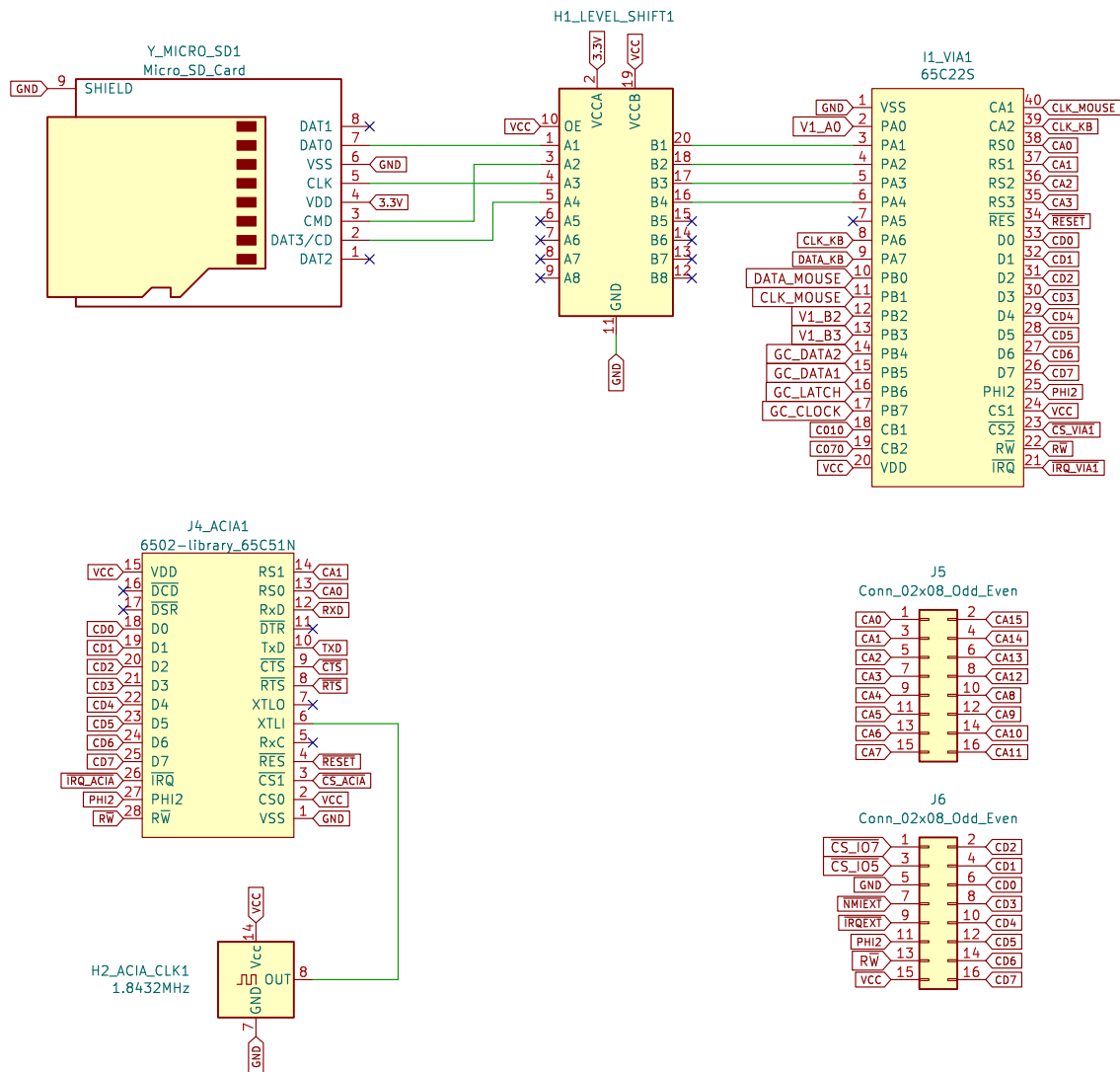
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File: cpu_rom.kicad_sch

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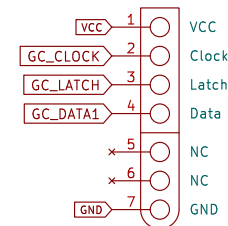
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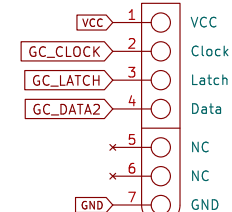
Rev:
Id: 6/19



Reference: Y_SNES_1
SNES_Controller_Socket



Reference: Y_SNES_2
SNES_Controller_Socket



Sheet: /Devices, Serial, VIA/
File: Communication_sch.kicad_sch

Title:

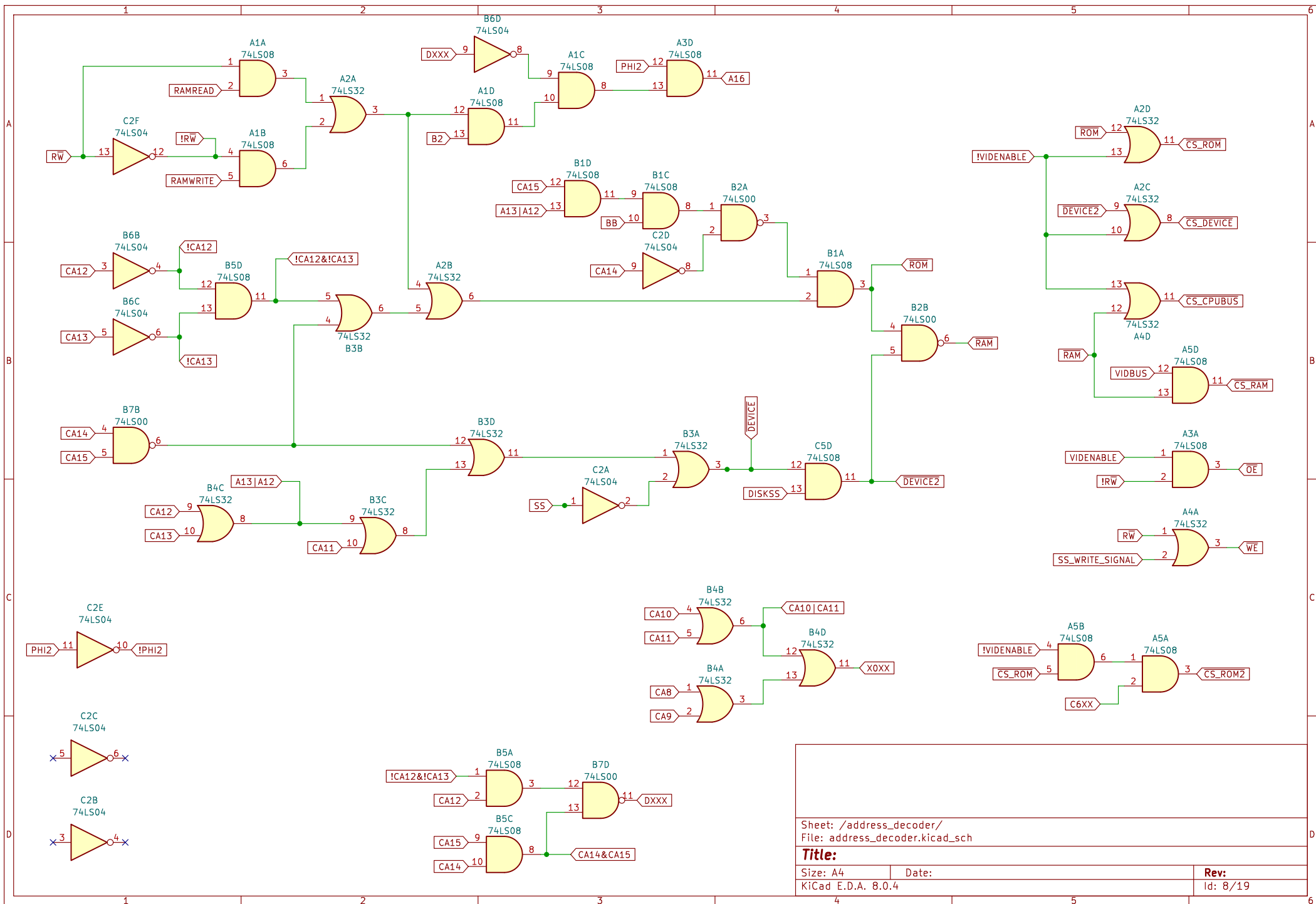
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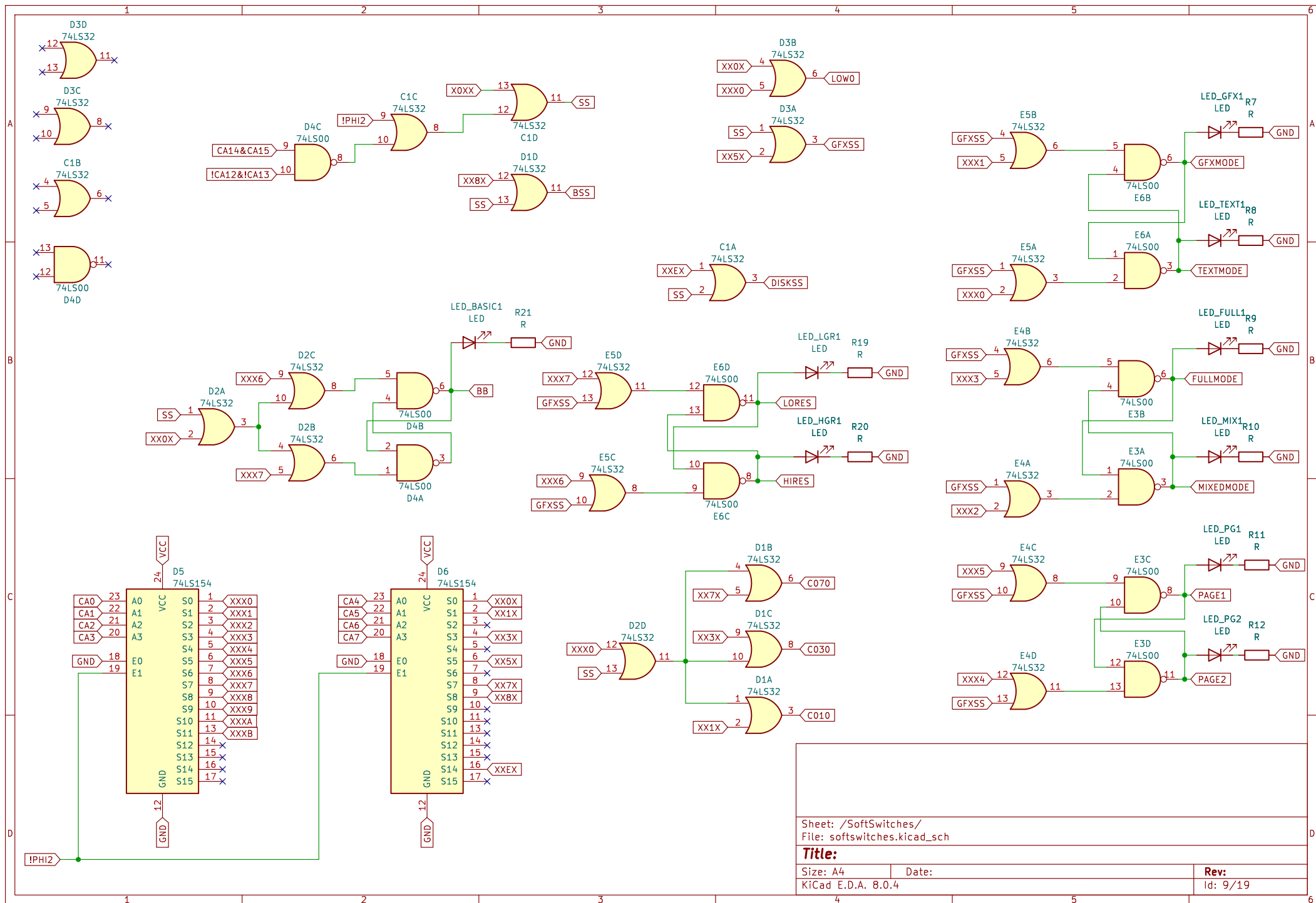
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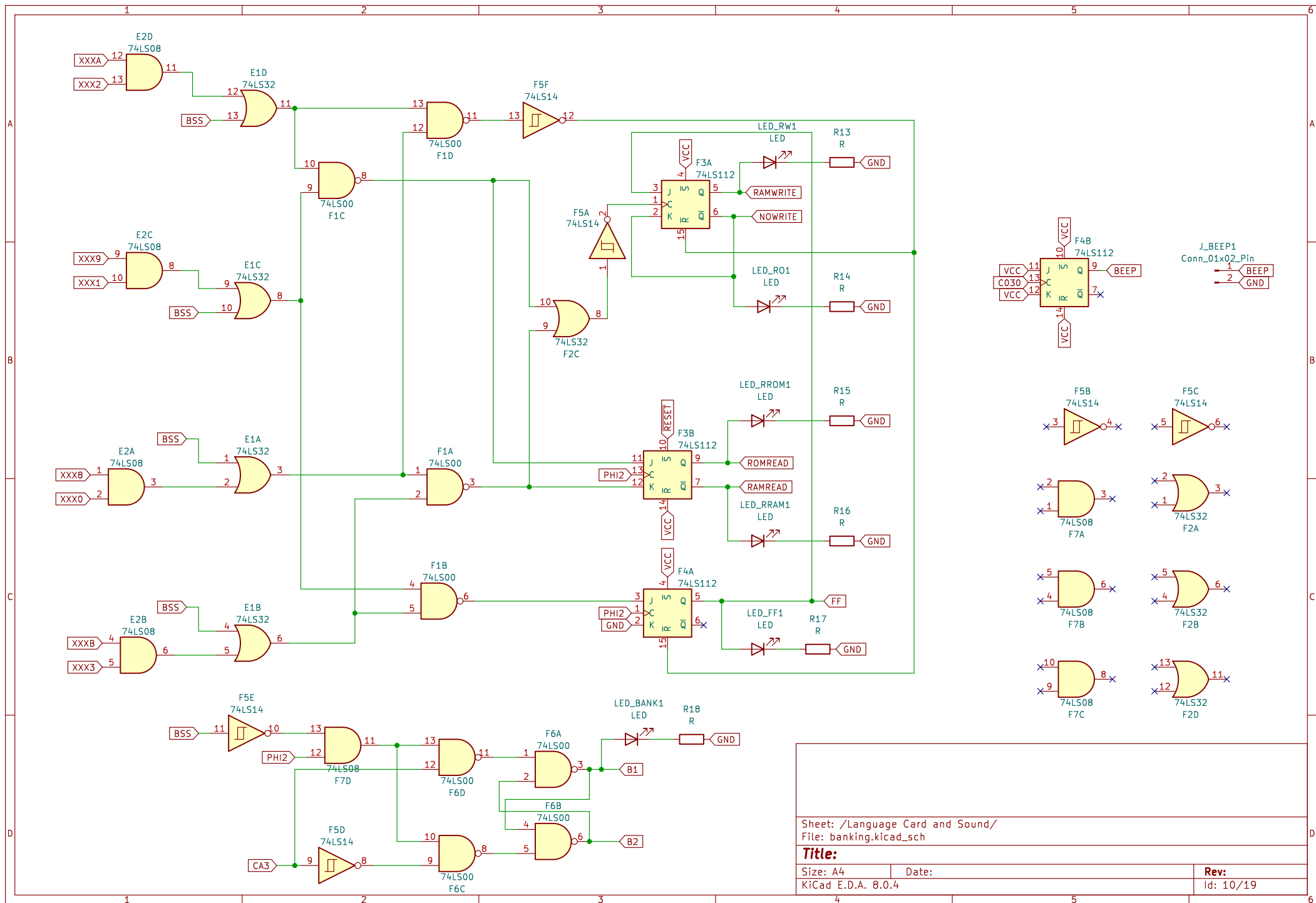
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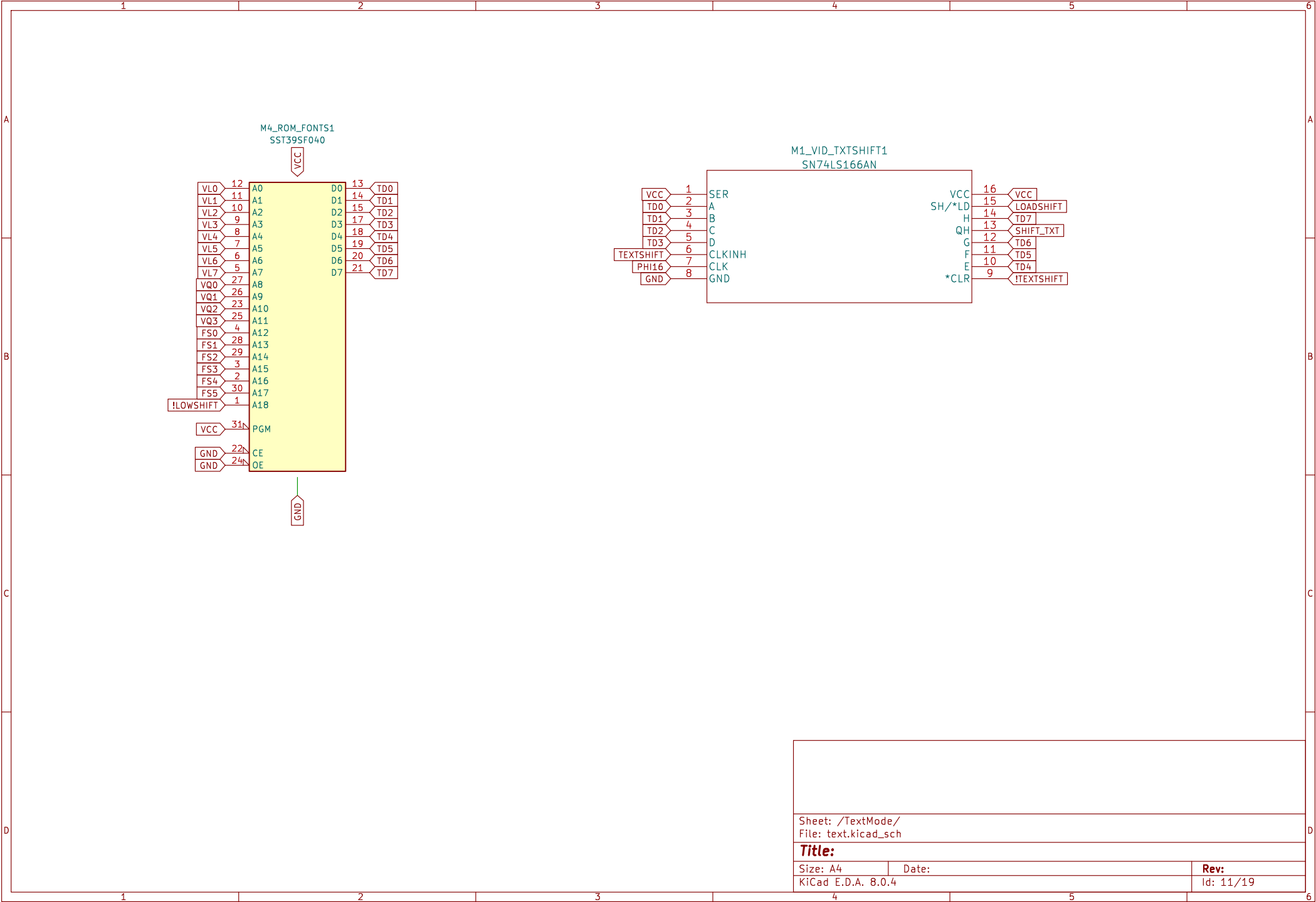
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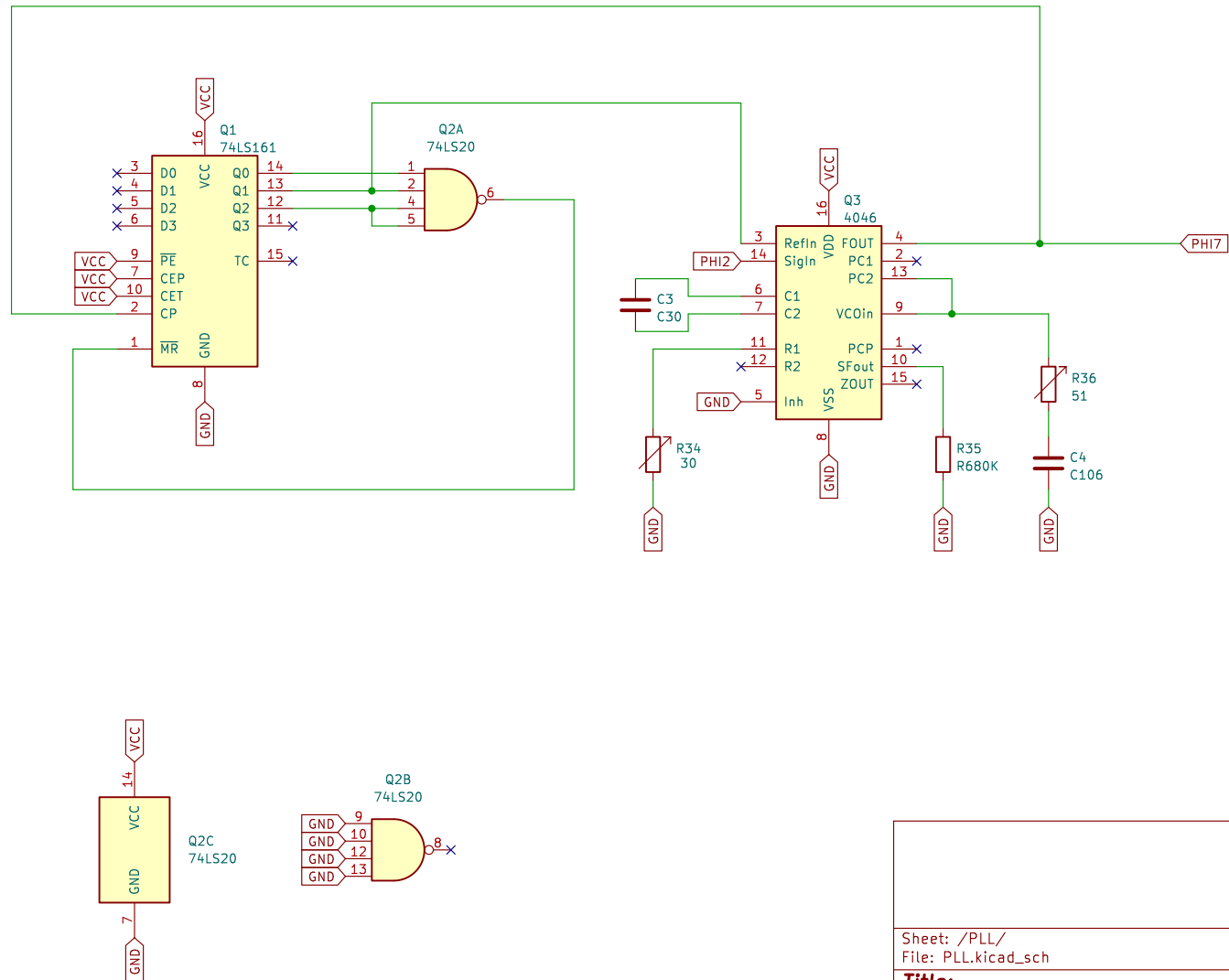
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Sheet: /PLL/
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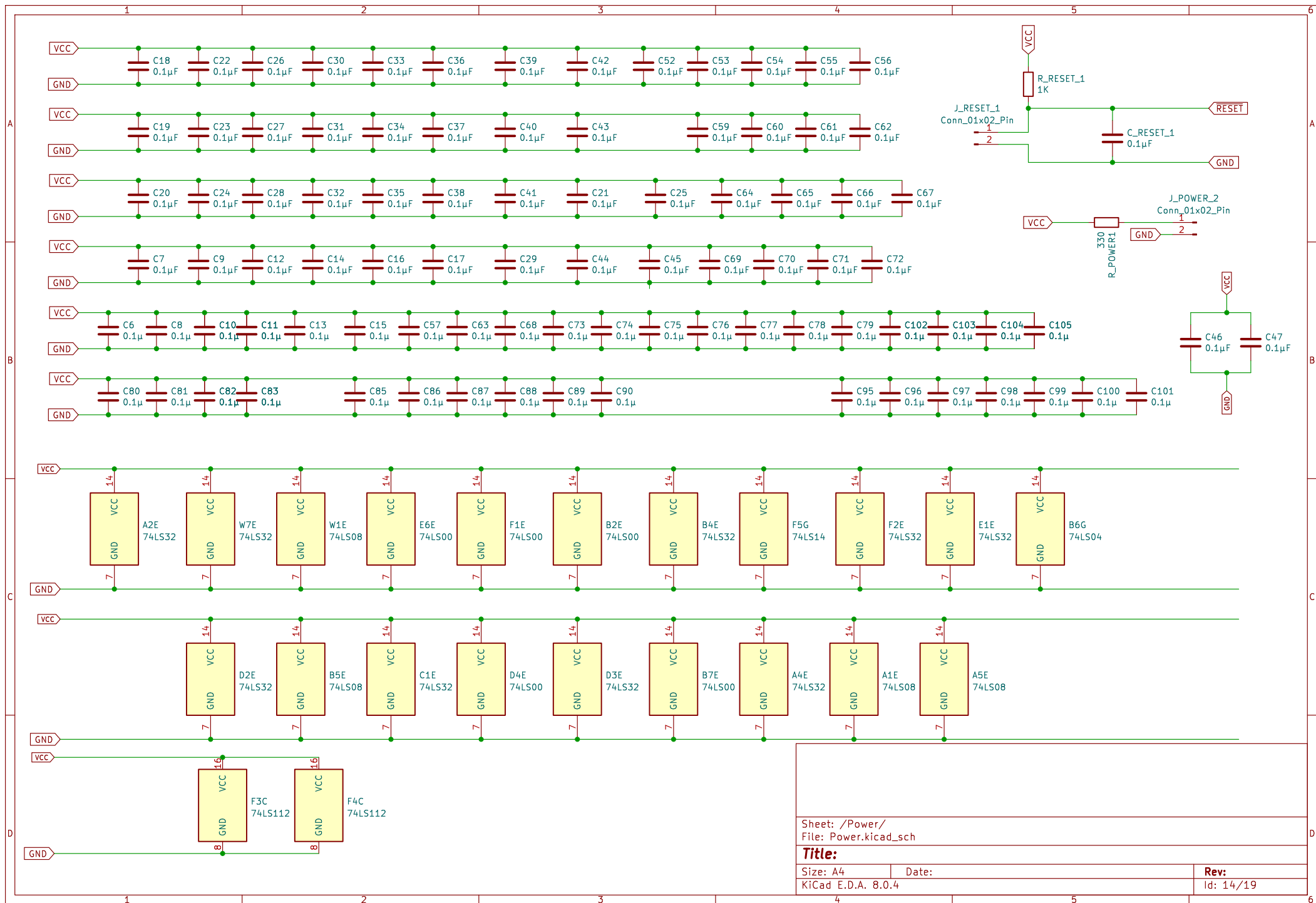
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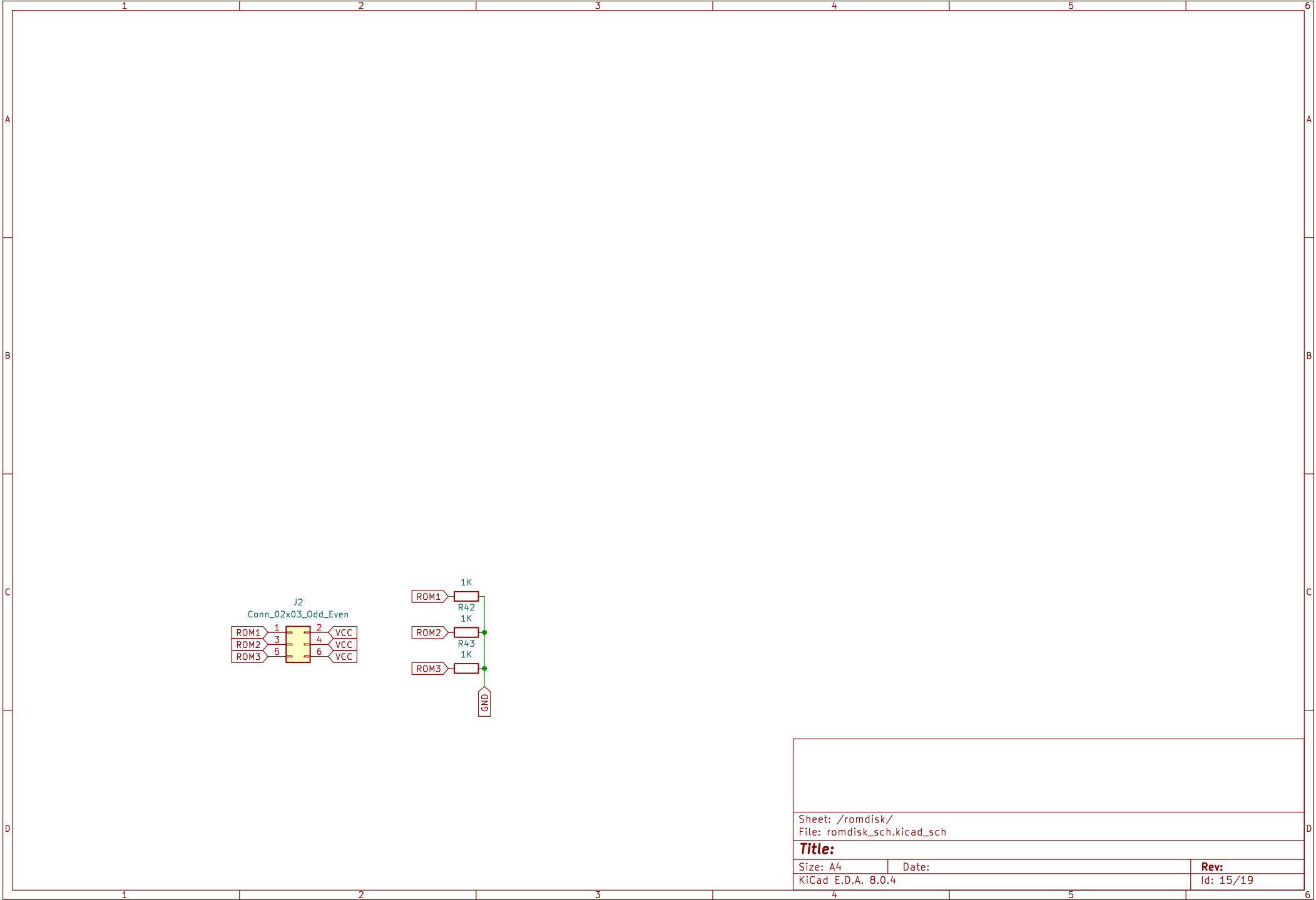
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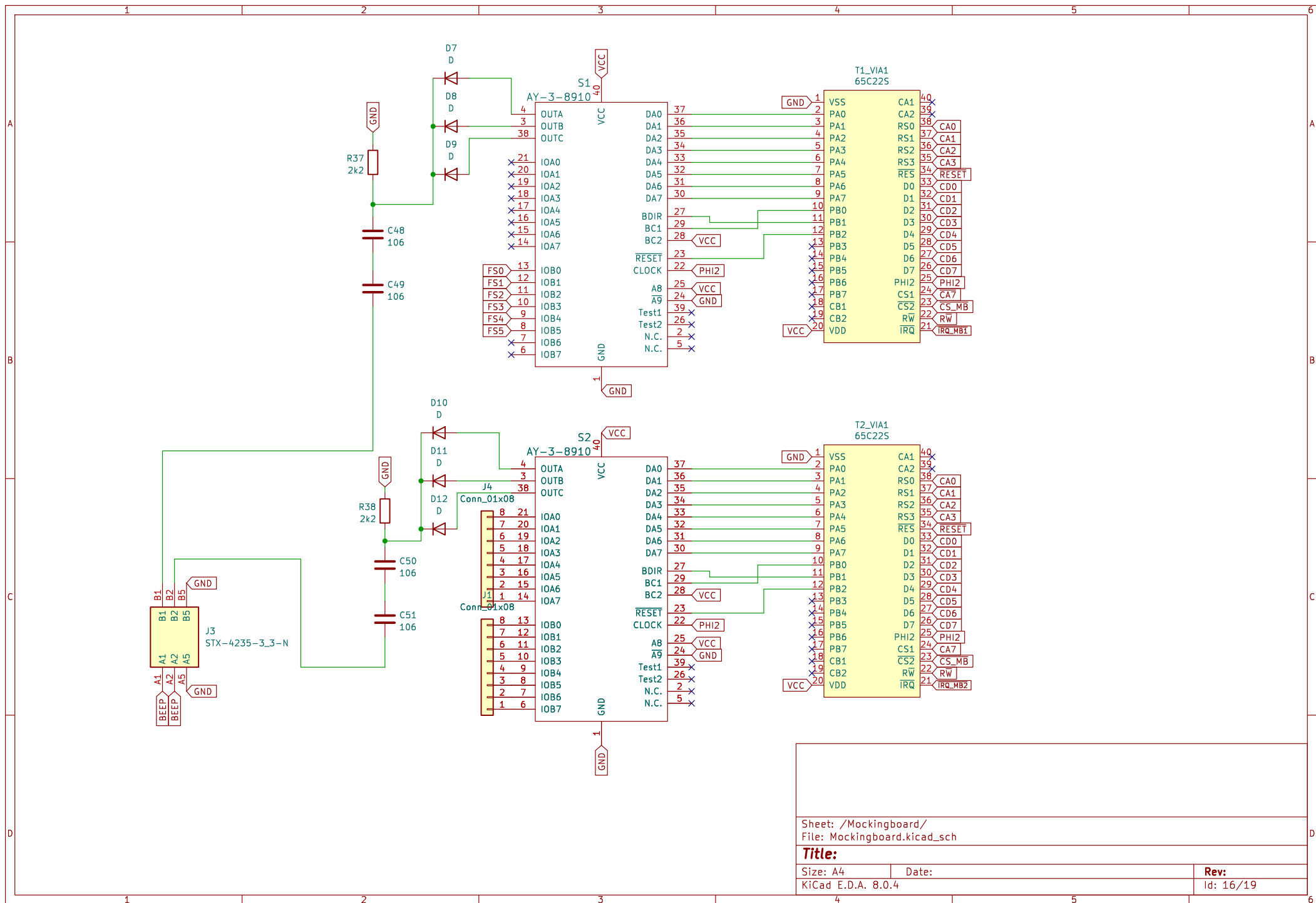
Id: 12/19





Sheet: /romdisk/
File: romdisk_sch.kicad_sch

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Size: A4	Date:	Rev:
KiCad E.D.A. 8.0.4	Id: 15/19	



Sheet: /Mockingboard/
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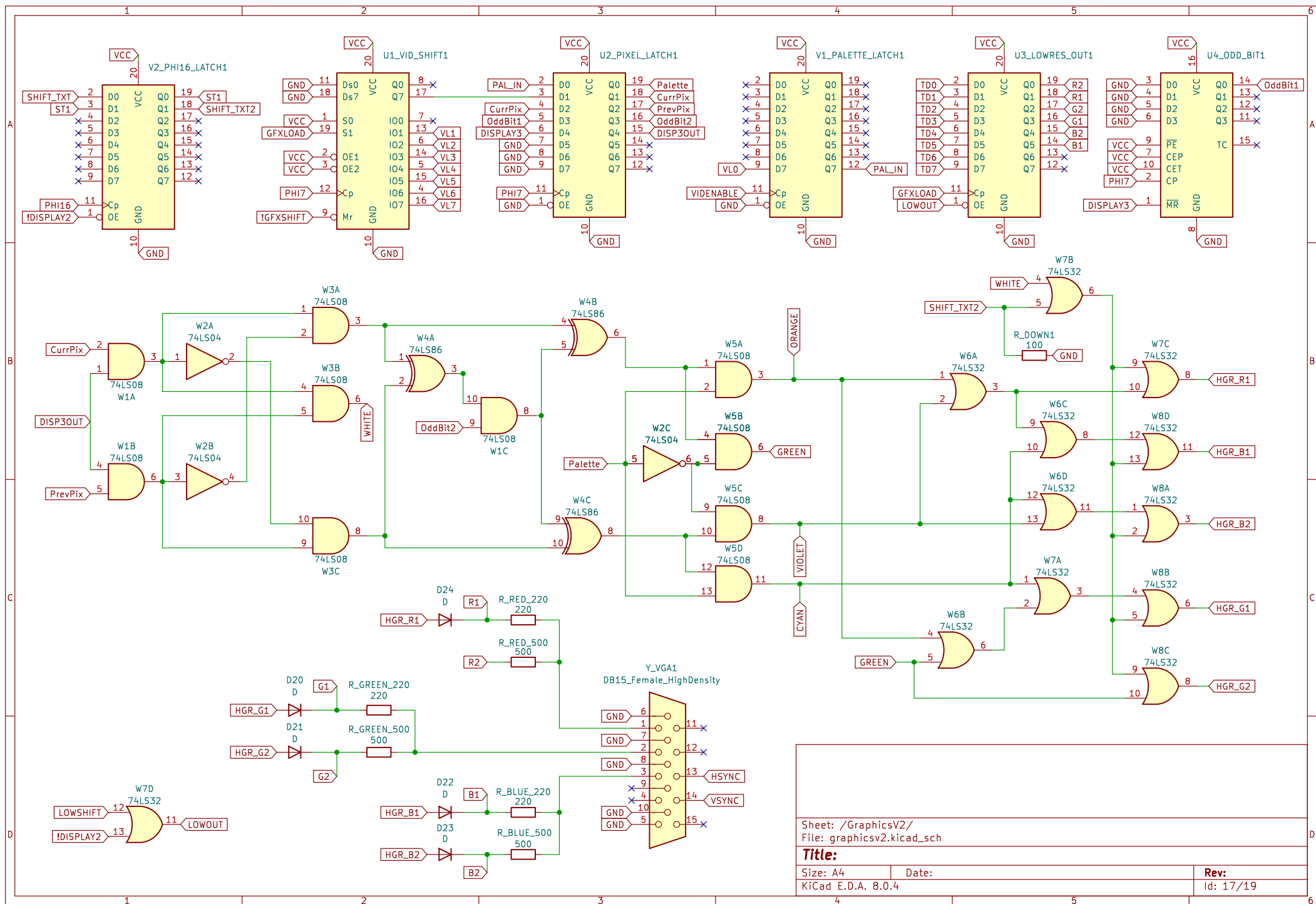
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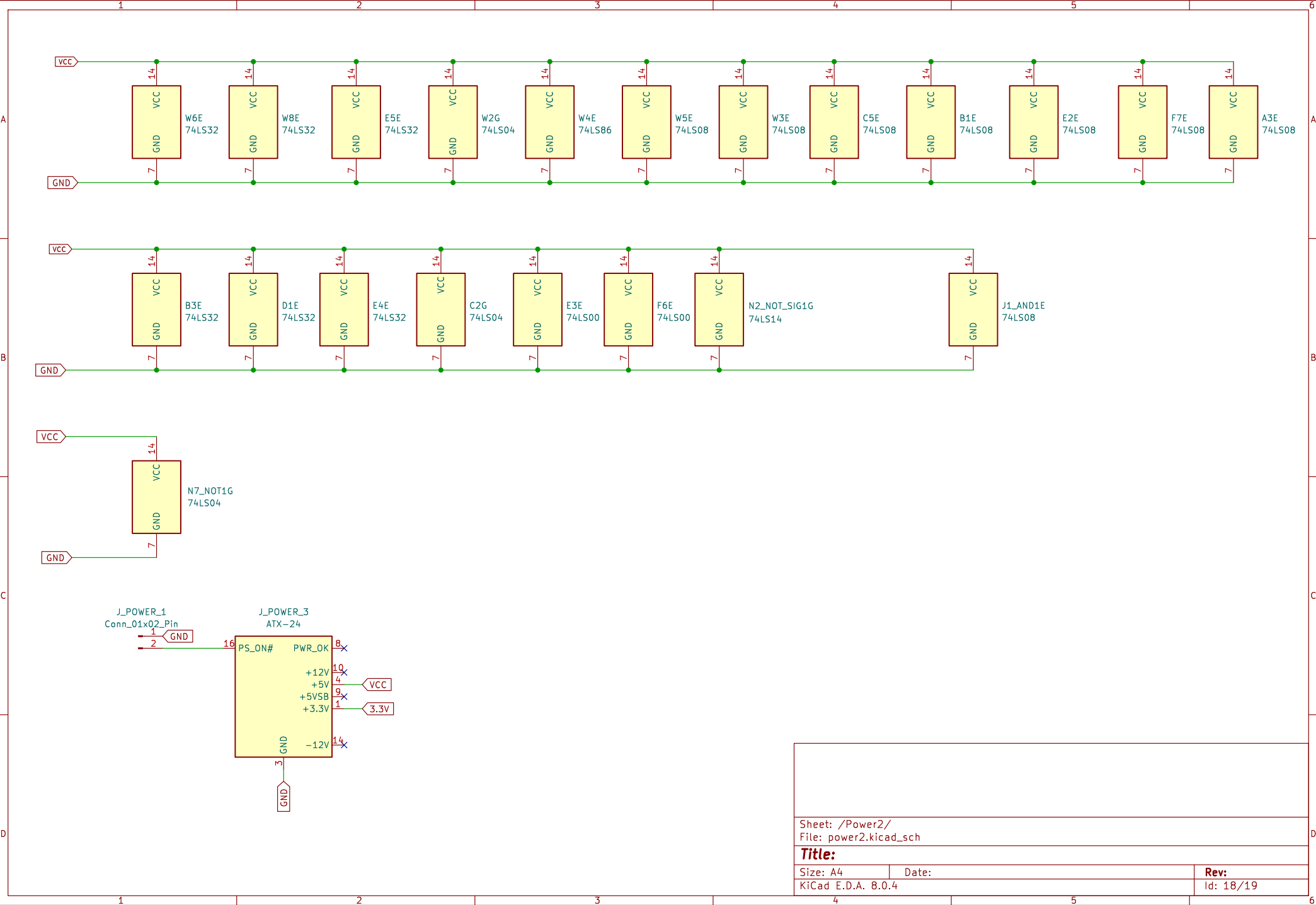
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Rev:

Id: 16/19





Sheet: /Power2/ File: power2.kicad_sch		
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KiCad E.D.A. 8.0.4	Id: 18/19	

1	2	3	4	5	6
A					A
B					B
C					C
D					D
1	2	3	4	5	6

Sheet: /LowRes/ File: LowRes.kicad_sch		
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Size: A4	Date:	Rev:
KiCad E.D.A. 8.0.4		Id: 19/19

