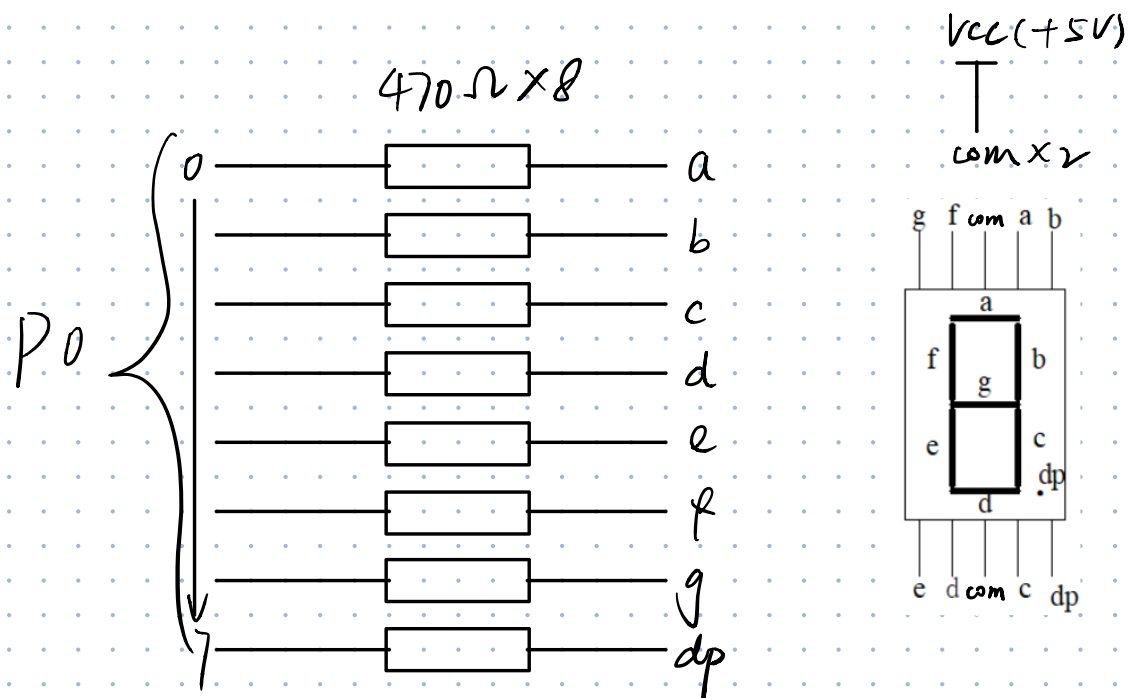


2.1 LED-1-Segment-Display

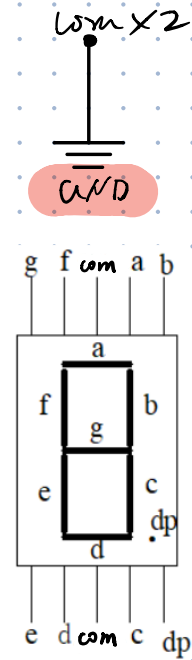
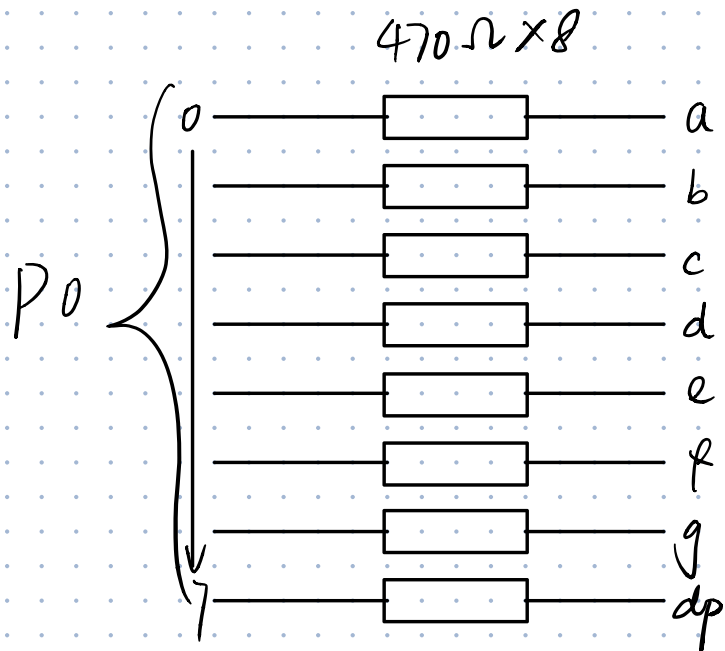
2.1.1 CA



$P_{2.7}$ ———— • Logic-Level-Input

2.1 LED-1-Segment-Display

2.1.2 CC



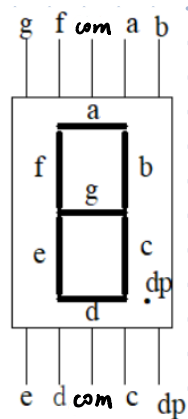
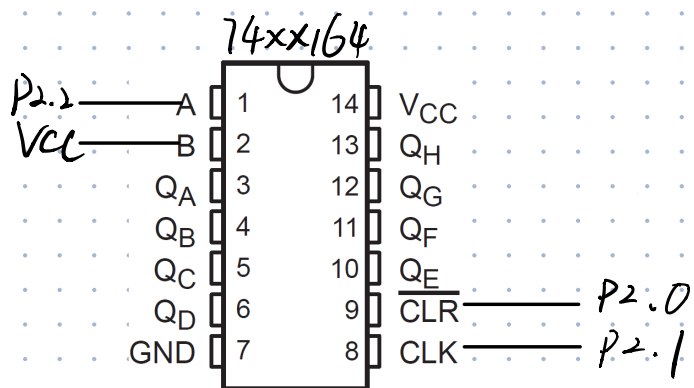
P2.7 •————• Logic-Level-Input

2.2 LED-1-Segment-Display-74xx164

2.2.1 CA

(74xx164) \bullet \longrightarrow \bullet a~g&dp
Q_A~Q_H

com x 2 \bullet \longrightarrow $\boxed{470\Omega \times 2}$ \longrightarrow VCC

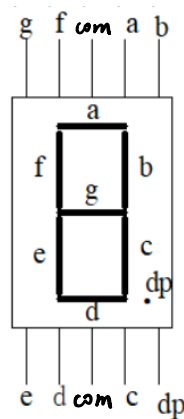
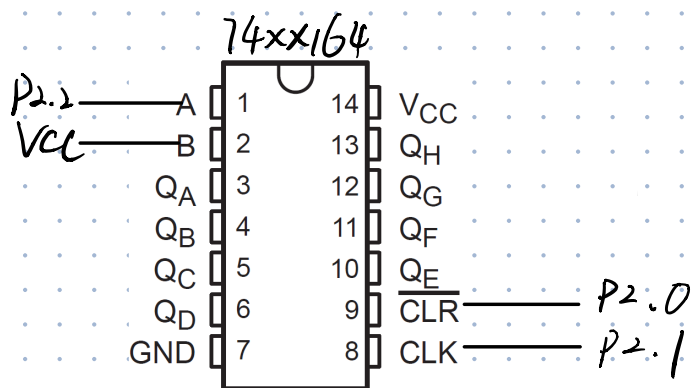


2.2 2ED-1-Segment-Display-74xx164

2.2.2 CC

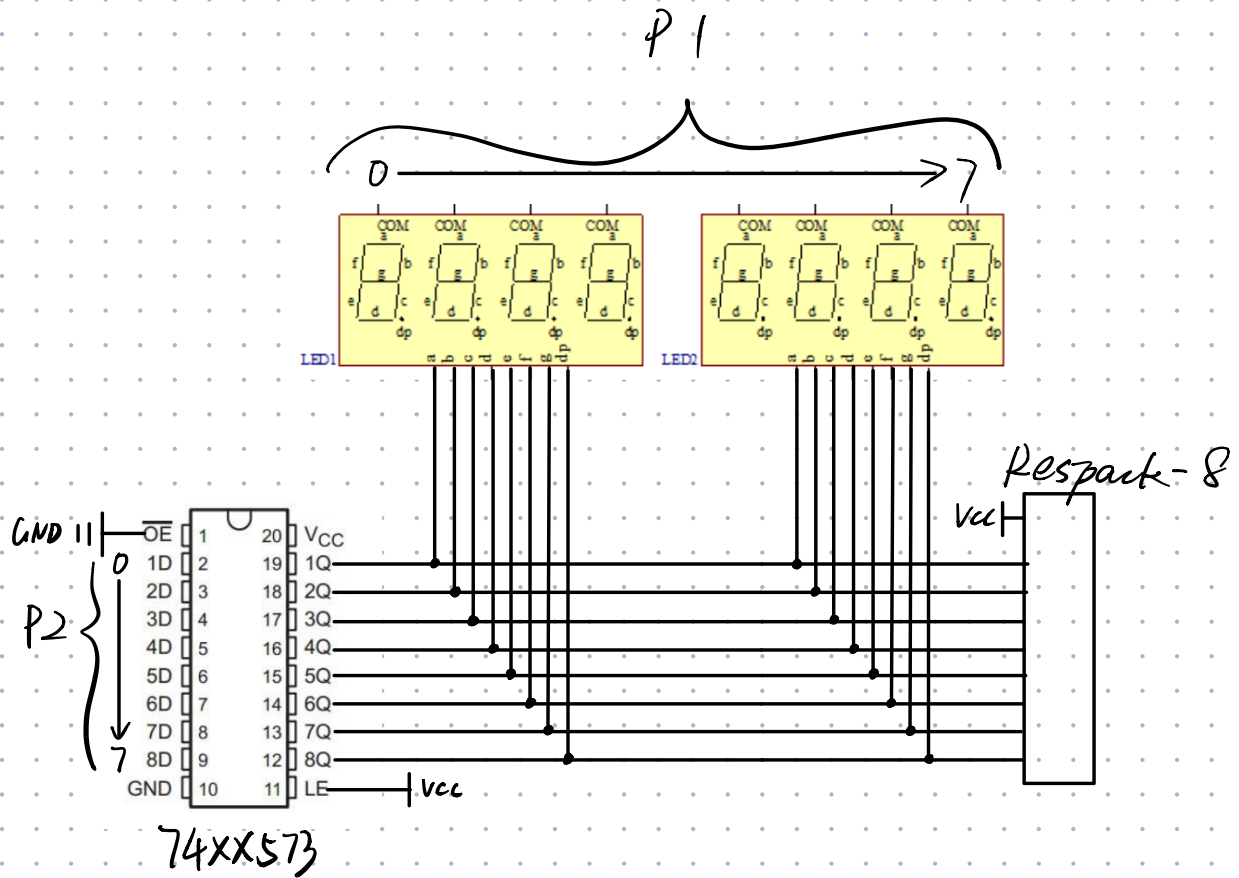
(74xx164) \bullet \longrightarrow \bullet a~g&dp
Q_A~Q_H

com x2 \bullet \longrightarrow $\boxed{470\Omega \times 2}$ \longrightarrow \parallel **GND**



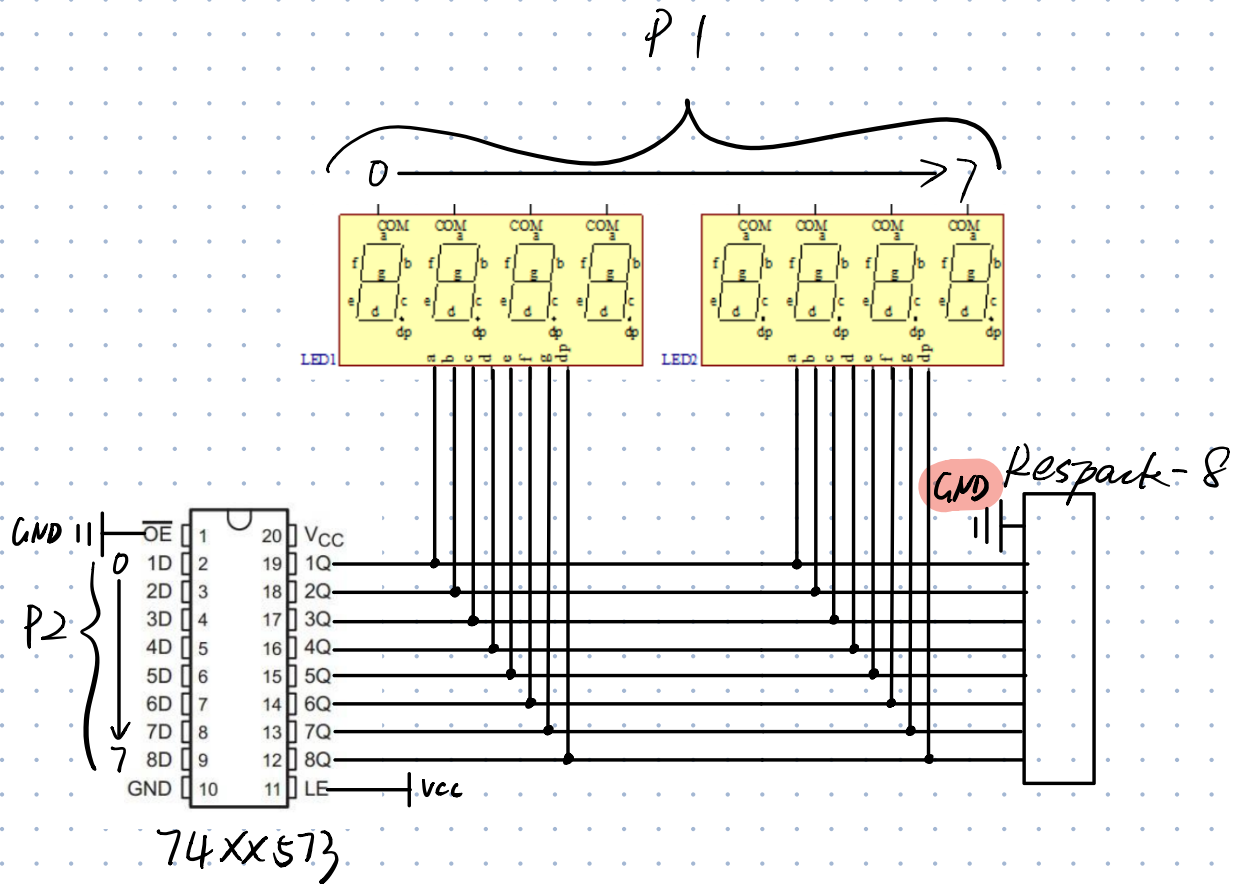
2.3 2LED-8-Segment-Displays

2.3.1 CA



2.3 2LED-8-Segment-Displays

2.3.2 CC



2.4 LED-8-Segment-Displays-74xx138

(Only for CC !!)

