### 8.27 - Notes ELE239

#### AND Truth Table

A	В	Υ
1	1	1
1	0	0
0	1	0
0	0	0

#### NAND Truth Table

A	В	Υ
1	1	0
1	0	1
Θ	1	1
0	0	1

#### OR Truth Table

A	В	Υ
1	1	1
1	0	1
0	1	1
Θ	0	0

#### NOR Truth Table

A	В	Υ
1	1	0
1	0	0
0	1	0
0	0	1

Basic Ladder Logic - IEC Symbols

## Inputs:

# Outputs:

$${\tt Coil} \ {\tt -} \ ( \ ) \!\!\!\! -$$

Negated Coil - 
$$-(/)$$