

# **Device Fission**

### 1 Overview

Note that this sample is essentially the same as the DeviceFission11Ext sample, except that this sample uses the OpenCL 1.2 device fission functionality; the DeviceFission11Ext sample uses OpenCL 1.1 and the clext device fission extension.

#### 1.1 Location

\$<AMDAPPSDKSamplesInstallPath>\samples\opencl\cl\1.x

#### 1.2 How to Run

See the Getting Started guide for how to build samples. You first must compile the sample.

Use the command line to change to the directory where the executable is located. The default executables are placed in  $\$  amplesInstallPath>\samples\opencl\bin\x86 for 32-bit builds and  $\$  and  $\$  amplesInstallPath>\samples\opencl\bin\x86\_64\ for 64-bit builds.

Type the following command(s).

- DeviceFission
   This tests the kernel execution on multi-devices asynchronously.
- DeviceFission -hThis prints the help message.

## 1.3 Command Line Options

Table 1 lists, and briefly describes, the command line options.

Table 1 Command Line Options

Short Form	Long Form	Description
-h	help	Shows all command options and their respective meaning.
<b>-</b> q	quiet	Quiet mode. Suppresses all text output.
-e	verify	Verify results against reference implementation.
-t	timing	Print timing.
	dump	Dump binary image for all devices.
	load	Load binary image, and execute on device.
	flags	Specify compiler flags to build the kernel.
-р	platformId	Select platformId to be used (0 to N-1, where N is the number of available platforms).
<b>-</b> ∿	version	AMD APP SDK version string.
-x	length	Length of the input array.
-d	deviceId	Select deviceld to be used (0 to N-1, where N is the number of available devices).

Device Fission 1 of 2

### 2 Introduction

The Device Fission functionality is supported only on the CPU.

This sample must be run in the OpenCL 1.2 environment. The following APIs are part of OpenCL 1.2:

clCreateSubDevices: Creates an array of sub-devices that each reference a non-intersecting set of compute units within a GPU, according to a partition scheme given by the API parameter properties.

clEnqueueMigrateMemObjects: Enqueues a command to indicate the device with which a set of memory objects is to be associated.

In this sample, a CPU device is partitioned into two sub-devices by using <code>clCreateSubDevices</code> and <code>CL\_DEVICE\_PARTITION\_BY\_COUNTS</code> property. Only one input buffer is created. Only one of the sub-devices is in charge of writing data into the input buffer; then, two sub-devices execute the kernels using data from that input buffer. The two sub-devices call their own kernels ("add" and "sub") on the input buffer and update their output buffers.

Contact

Advanced Micro Devices, Inc. One AMD Place P.O. Box 3453 Sunnyvale, CA, 94088-3453 Phone: +1.408.749.4000 For AMD Accelerated Parallel Processing: URL: developer.amd.com/appsdk

Developing: developer.amd.com/
Support: developer.amd.com/appsdksupport



The contents of this document are provided in connection with Advanced Micro Devices, Inc. ("AMD") products. AMD makes no representations or warranties with respect to the accuracy or completeness of the contents of this publication and reserves the right to make changes to specifications and product descriptions at any time without notice. The information contained herein may be of a preliminary or advance nature and is subject to change without notice. No license, whether express, implied, arising by estoppel or otherwise, to any intellectual property rights is granted by this publication. Except as set forth in AMD's Standard Terms and Conditions of Sale, AMD assumes no liability whatsoever, and disclaims any express or implied warranty, relating to its products including, but not limited to, the implied warranty of merchantability, fitness for a particular purpose, or infringement of any intellectual property right.

AMD's products are not designed, intended, authorized or warranted for use as components in systems intended for surgical implant into the body, or in other applications intended to support or sustain life, or in any other application in which the failure of AMD's product could create a situation where personal injury, death, or severe property or environmental damage may occur. AMD reserves the right to discontinue or make changes to its products at any time without notice.

#### Copyright and Trademarks

© 2015 Advanced Micro Devices, Inc. All rights reserved. AMD, the AMD Arrow logo, ATI, the ATI logo, Radeon, FireStream, and combinations thereof are trademarks of Advanced Micro Devices, Inc. OpenCL and the OpenCL logo are trademarks of Apple Inc. used by permission by Khronos. Other names are for informational purposes only and may be trademarks of their respective owners.