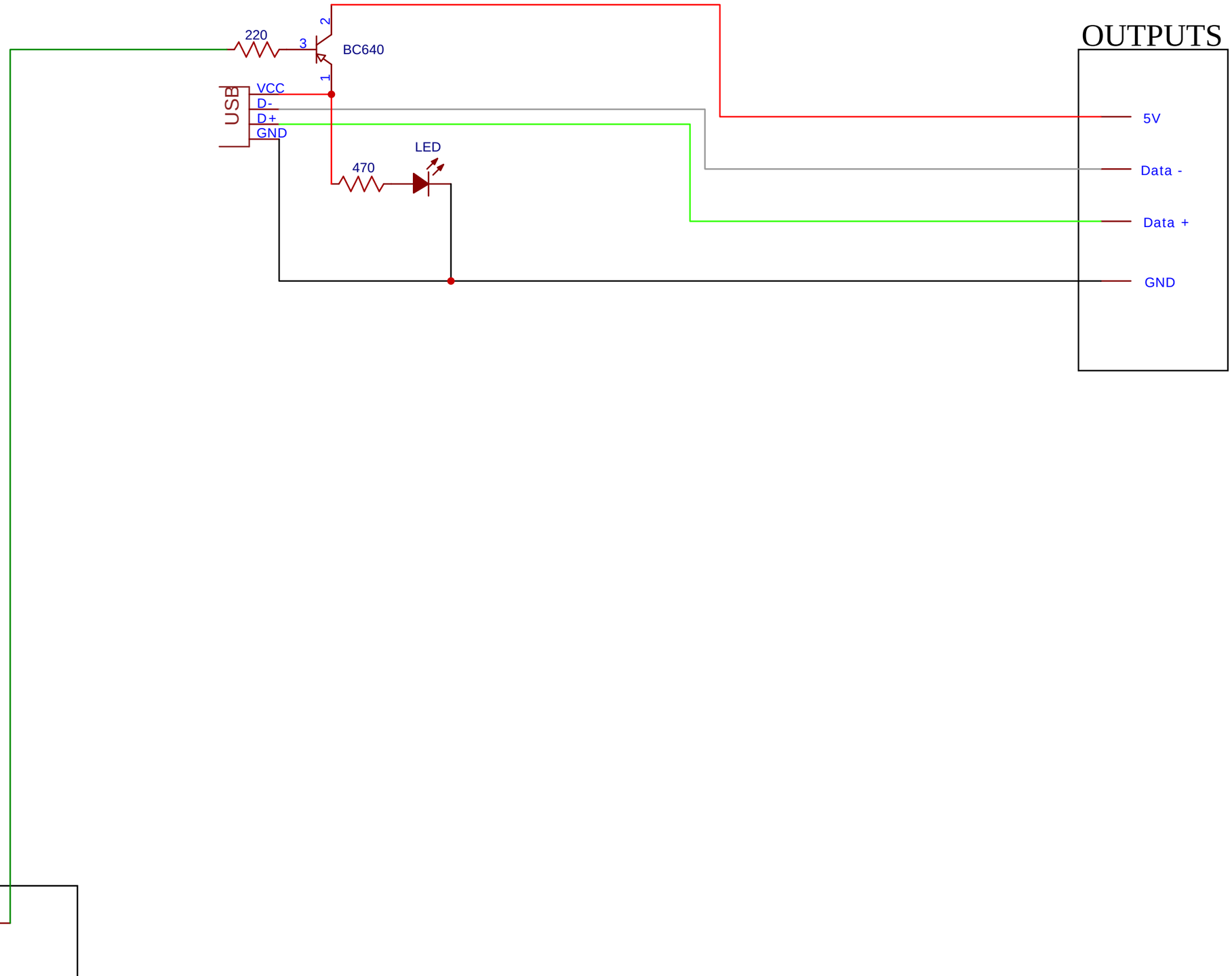
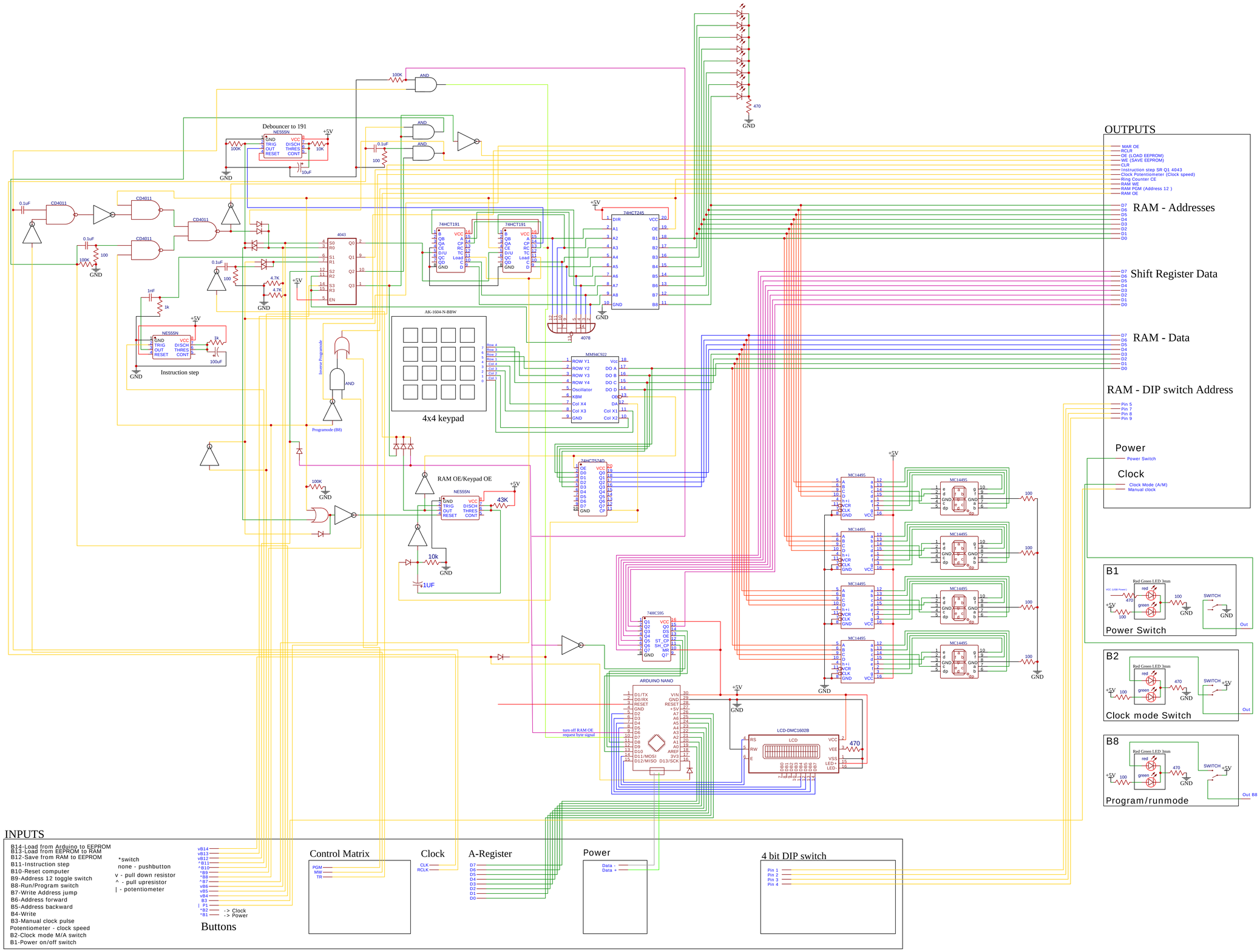
Power



TITLE: Power		REV: 1.0
EasyEDA	Company: Xrayer	Sheet: 7/7
	Date: 2019-09-25 xehono	

Input Module



TITLE:	Input Module
REV:	1.0
Company:	Xrayer
Sheet:	5/7
Date:	2019-08-04
Drawn By:	xehono

EasyEDA

ALU

INPUTS

B-REGISTER

B7 B6 B5 B4 B3 B2 B1 B0

CONTROLL MATRIX

RST CLK ALB ALW ALE AL C ALS2 ALS1 ALS0

+5V

74HCT157

74HCT157

GND

470

GND

+5V

74HCT377

74HCT245

74F382

74F382

GND

4076

4078

470

GND

OUTPUTS

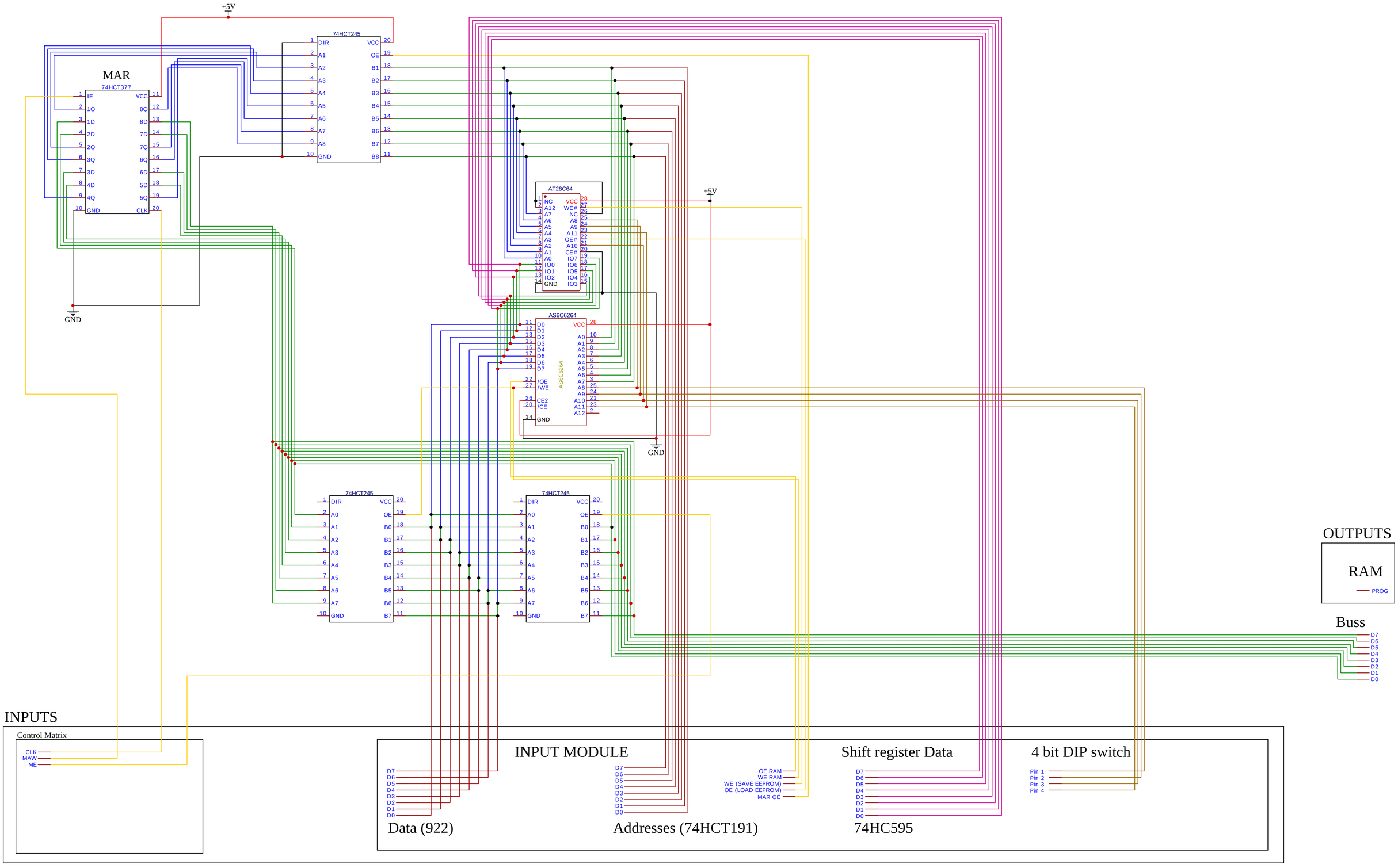
Z N O C

Buss

D7 D6 D5 D4 D3 D2 D1 D0

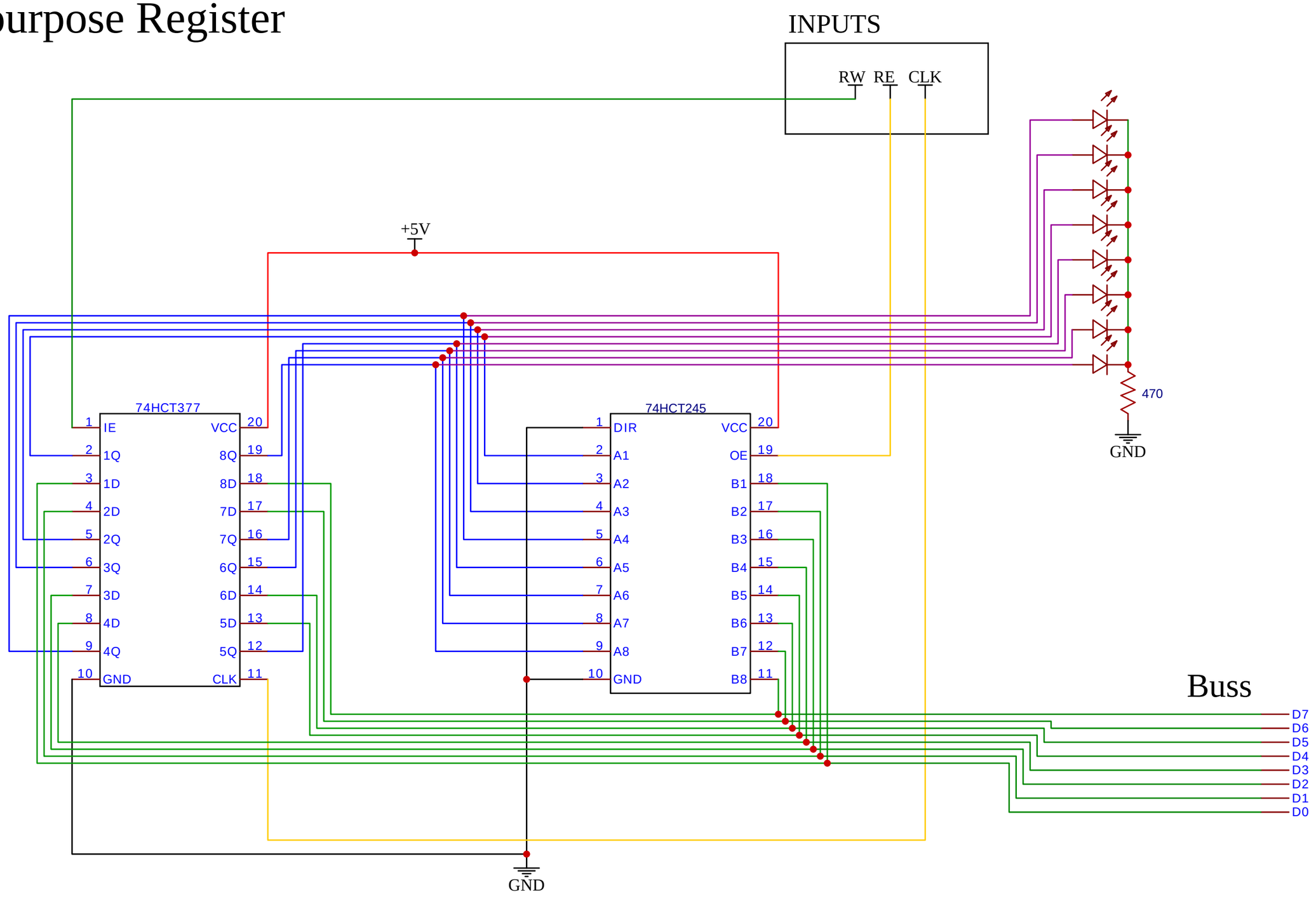
TITLE: ALU		REV: 1.0
EasyEDA	Company: Xrayer	Sheet: 4/7
	Date: 2019-08-03 Drawn By: xehono	

RAM



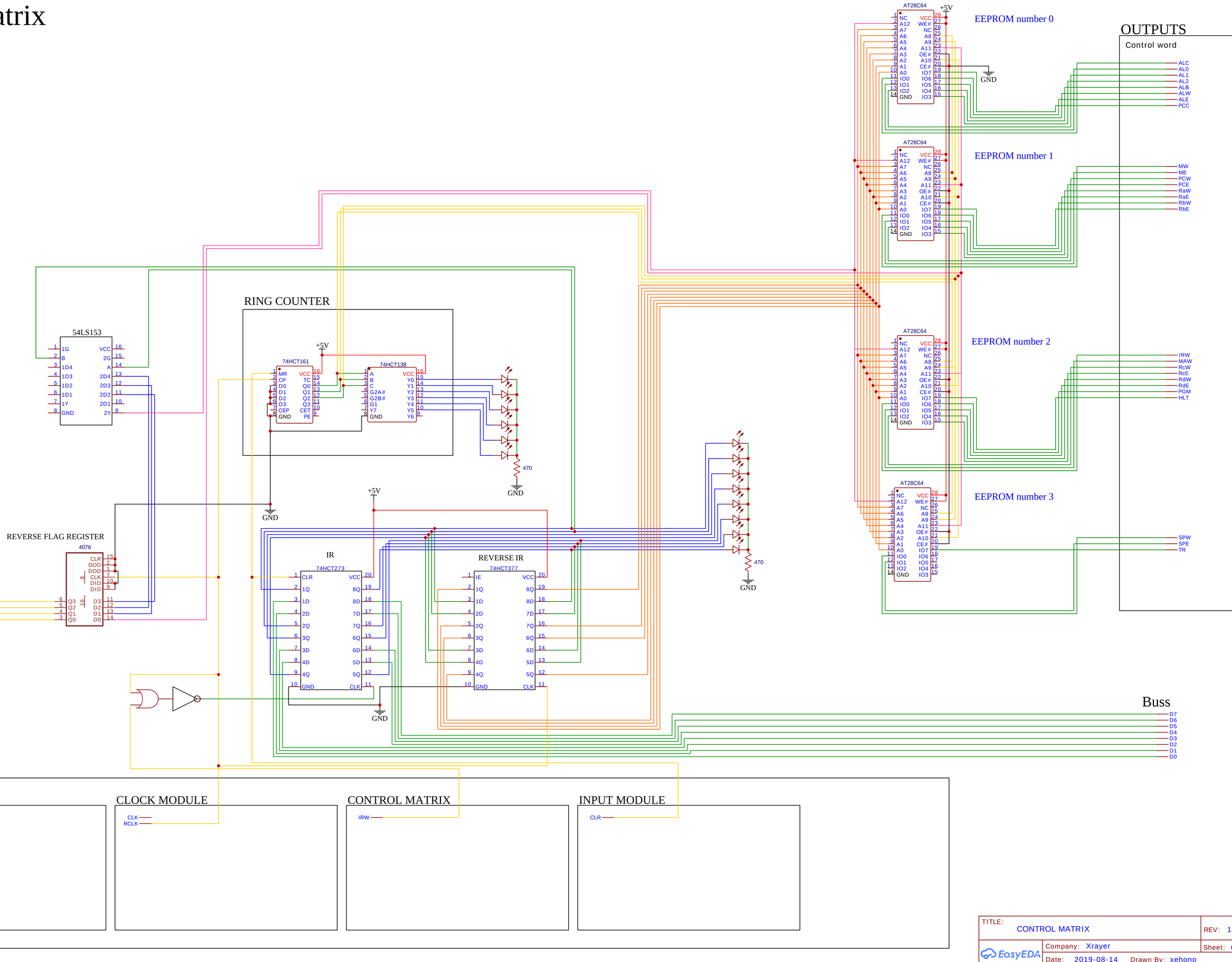
TITLE: RAM			REV: 1.0
EasyEDA	Company: Xrayer		Sheet: 3/7
	Date: 2019-08-03	Drawn By: xehono	

General purpose Register



TITLE: General purpose Register		REV: 1.0
EasyEDA	Company: Xrayer	Sheet: 2/7
	Date: 2019-08-03 Drawn By: xehono	

Control Matrix



TITLE: CONTROL MATRIX	REV: 1.0
Company: Xrayer	Sheet: 6/7
Date: 2019-08-14	Drawn By: xehono

EasyEDA

Clock Module

OUTPUTS

System Clock
rvrs System Clock

+5V
NE555N
GND
TRIG
OUT
RESET
VCC
DISCH
THRES
CONT
1.47k
100uF
GND
CD4011
AND
CD4011
CD4011
CD4011
1k
GND
100k
GND
NE555N
+5V
GND
TRIG
OUT
RESET
VCC
DISCH
THRES
CONT
1k
100uF
0.1uF

INPUTS

Input Module

100K
Clock Speed (Potentiometer)
B3(Manual Clock)
SR Q1 4043 (instruction step)
Clock Mode(A/M)

Control Matrix

HLT

TITLE: Clock Module		REV: 1.0
EasyEDA	Company: Xrayer	Sheet: 1/7
	Date: 2019-09-25 xehono	