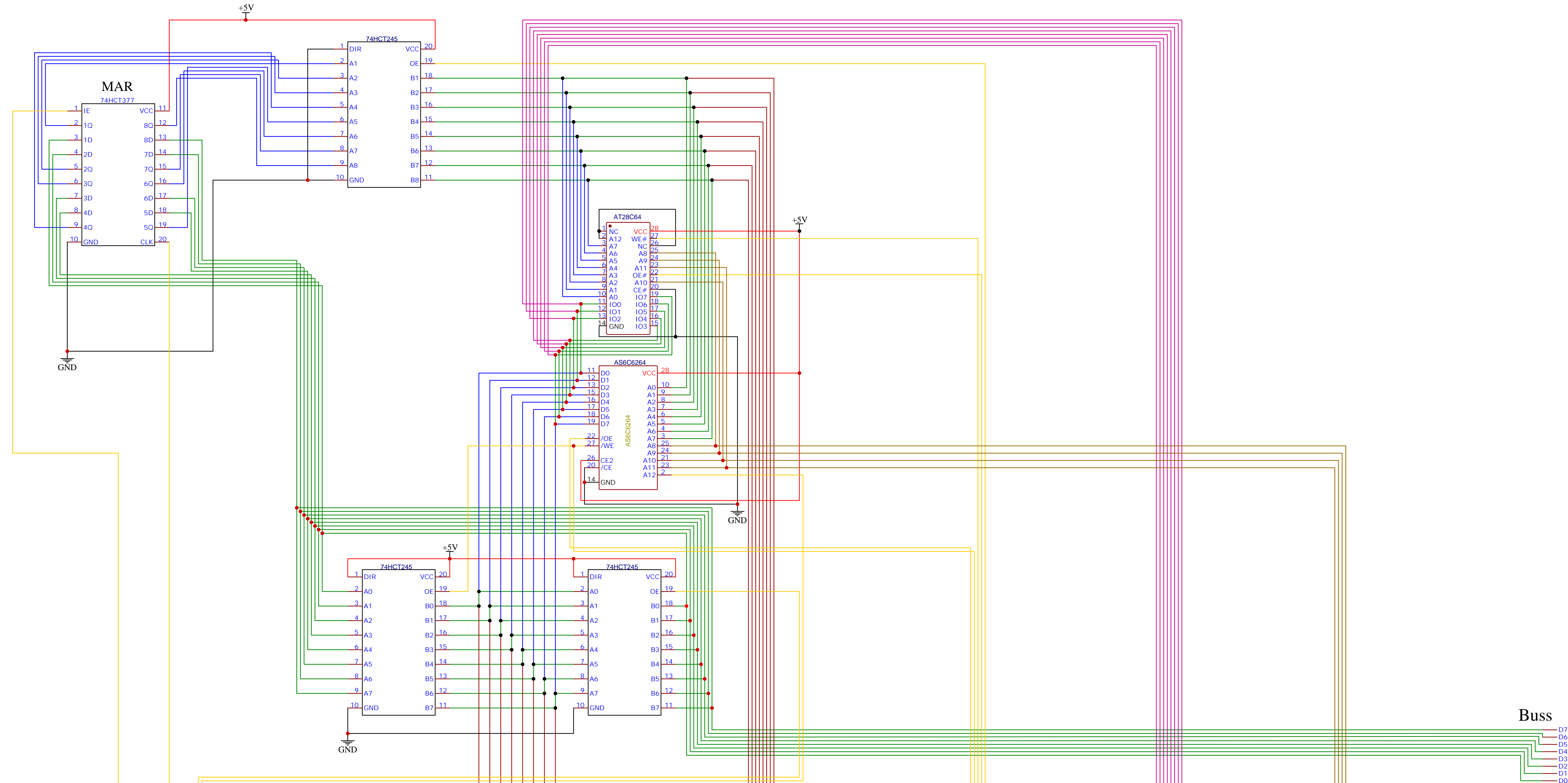


RAM



INPUTS

Control Matrix

CLK

MAW

ME

Program memory

OE RAM

WE RAM

INPUT MODULE

I/O Pins

D7

D6

D5

D4

D3

D2

D1

D0

Data (922)

Addresses (74HCT191)

D7

D6

D5

D4

D3

D2

D1

D0

WE (SAVE EEPROM)

OE (LOAD EEPROM)

MAR OE

Shift register Data

D7

D6

D5

D4

D3

D2

D1

D0

74HC595

4 bit DIP switch

Pin 1

Pin 2

Pin 3

Pin 4