

ARM cores

- Supervision
- Serial communication
- Data recording

Clock Generator

- ADC clock
- DAQ clock
- Logic clock

DDS

- Phase shifted ref.
- f, 2f; Sin&Cos

DDS

- Modulation Signal
- f

DAC

- Modulation
- Phase output

ADC

- Signal input

Multiplier

- Signal*Cos
- f, 2f
- for phase locking
(software PID)

Multiply&Add

- Signal*Modulation
- Amplitude adjust

Multiply&Add

- Signal*Modulation
- Amplitude adjust

CIC Low Pass Filter

- f/4

Arctan (CORDIC)

- Phase output

Phase Unwrapper