Inter-Integrated Circuit (I2C)

Table 304   List of I2C Interface Signals (cont'd)

Interface Signals	I/O	Description
SDAF	in	**Serial Data Input 5** The last letter indicates the input of the input multiplexer.
SDAG	in	**Serial Data Input 6** The last letter indicates the input of the input multiplexer.
SDAH	in	**Serial Data Input 7** The last letter indicates the input of the input multiplexer.
SCL	out	**Serial Clock Output**
SDA	out	**Serial Data Output**
HS_MODE	out	**High-Speed-Mode Active**
HS_PAD_SWITCH	out	**Control Signal for High Speed Pad**
SLEEP	in	**Sleep Request**

34.7   Revision History

Table 305   Revision History

Reference	Change to Previous Version	Comment
V2.3.4		
–	No changes.	–
V2.3.5		
Page 51	3 questionmarks ("?") in footnote of register "Access Enable Register 0" removed.	
V2.3.6		
Page 9	Replaced list item.	
Page 59	Changed Reset value to 5705 hex for **Module Identification Register**.	

User's Manual
I2CV2.3.6

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V2.0.0
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