27.5 IO Interfaces

The table below list of interfaces of the STM to other modules.

Table 3   List of STM Interface Signals

Interface Signals	I/O	Description
sx_fpi		**FPI slave interface**
sx_irq_stm		**STM Interrupt Socket**
SR0_INT	out	**System Timer Service Request 0**
SR1_INT	out	**System Timer Service Request 1**

27.6 Revision History

Table 4   Revision History

Reference	Change to Previous Version	Comment
V9.2.3		
Page 23	Previous versions removed from revision history.	
V9.2.4		
Page 19	Description of bit field "SUS" of register "OCDS Control and Status Register" updated.	

User's Manual
STMV9.2.4

27-23
OPEN MARKET VERSION 2.0

V2.0.0
2021-02

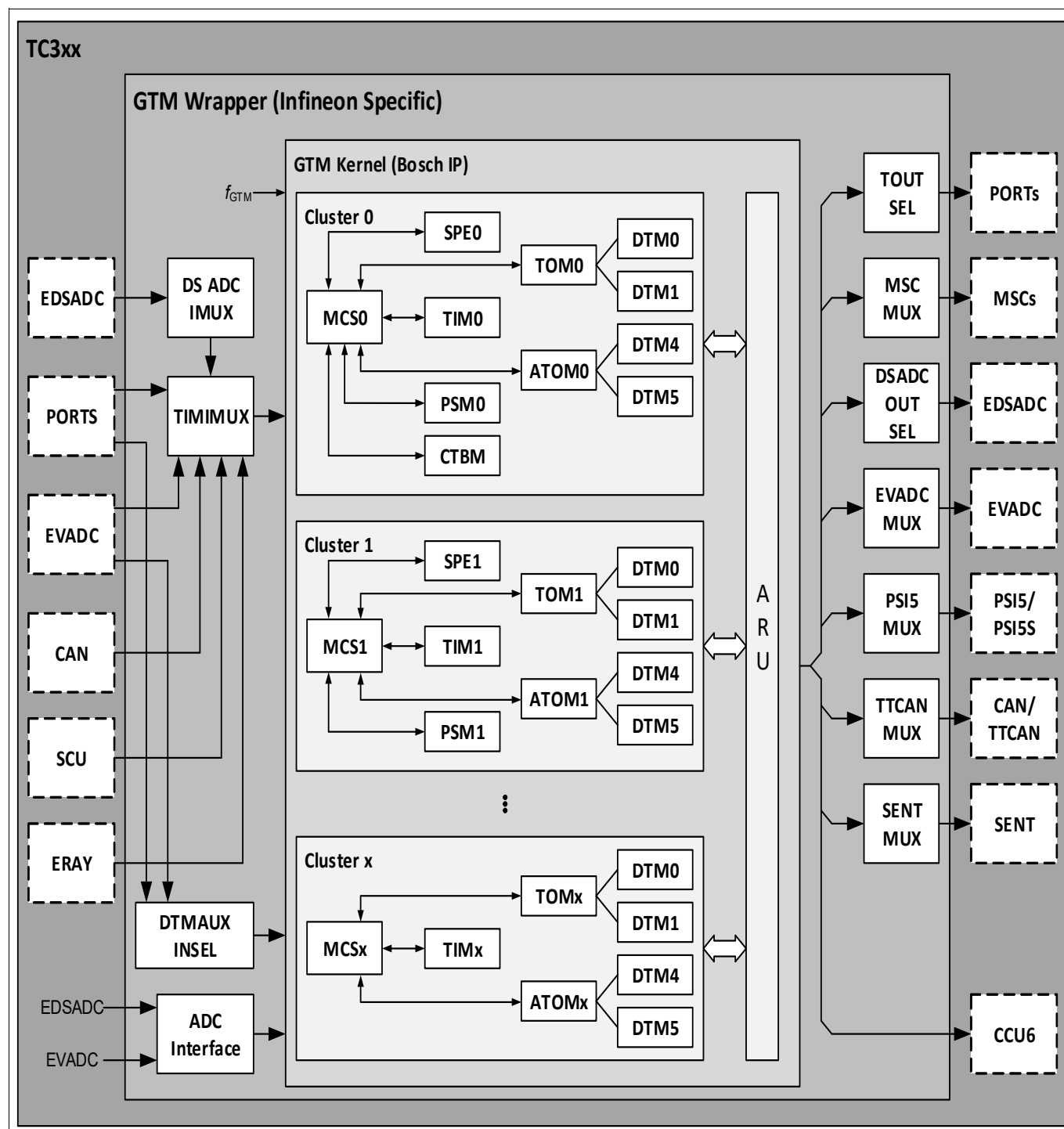# 28 Generic Timer Module (GTM)

GTM general Architecture

Figure 4 GTM Architecture block diagram

Note: The GTM system runs with the GTM global clock f_{GTM} (referred in this chapter as SYS_CLK)

User's Manual
GTMV2.2.24

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OPEN MARKET VERSION 2.0

V2.0.0
2021-02