40.7   Revision History

Table 388   Revision History

Reference	Change to Previous Version	Comment
V1.19.8		
Page 10, **Page 24**, **Page 172**	Note added for recommendation on CAN FD maximum bit rate, port configuration for CAN FD communication and Trigger Memory Element 0.	
V1.19.9		
-	No change in family specification.	
V1.19.10		
Page 25	Typo "layerm" corrected to "layer" in the note paragraph.	
Page 65	Access term "OEN" added to OCS register.	
Page 14, **Page 15**	STM Timer Trigger signals naming aligned as given in the Connectivity table in Appendix.	
Page 64	Updated figure (removed wrong ACCEN1).	
Page 177	Added note at the end of IO Interfaces table.	
V1.19.11		
Page 5	Wrong f_{MCANH} changed to f_{MCAN} at 'f_{ASYN} is supplied from f_{MCAN}'. Additionally some typos fixed in this paragraph.	
Page 27	Wrong TX changed to RX at 'TDCRi (i=0-3).TDCF AND RX is low'.	
Page 112	Corrected register IRi bit field 29 to '0'/'r' (external/UM only).	
V1.19.12		
Page 2	Updated **Section 40.1**.	
V1.19.13		
Page 64	Updated information on bit implementation in A-step.	