The configuration of each 4 upper F2A streams can be done separately for each stream in the configuration register **F2A[i]_CTRL**.

Note that the corresponding upper FIFO channel (4…7) cannot be used in this configuration.

28.9.4   F2A Configuration Register Overview

Table 24   F2A Configuration Register Overview

Register name	Description	see Page
F2A[i]_ENABLE	F2Ai stream activation register	**99**
F2A[i]_CH[z]_ARU_RD_FIFO	F2Ai channel z read address register	**99**
F2A[i]_CH[z]_STR_CFG	F2Ai stream z configuration register	**100**
F2A[i]_CTRL	F2Ai stream control register	**101**

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