33.15 Revision History

This is a summary of the modifications that have been applied to this chapter.

Table 295 Revision History

Reference	Change to Previous Version	Comment
V3.0.0		
Page 22	Describe 2:1 clock prescaler.	
Several	Replace f_{PER} with f_{ADC}.	
Page 11	Describe details of the module kernel reset.	
Page 21	Correct modulator selection in figure.	
Page 10	Add note about suspend mode to register OCS.	
Page 95	Add note to table “Properties of Operating Ranges”.	
Page 1, **67**	Correct data paths for auxiliary filter in figures.	
Page 61	Correct typo in formula for CIC3 group delay.	
V3.0.1		
Page 25	Changes in description and note for Slow Standby mode and Fast Standby mode.	
Page 97	Description for “WAIT” in Initialization Sequence changed from “Pause for extended wakeup time (approx. 28 us)” to “Pause for wakeup time (approx. 20 µs)”.	
Page 26	Wakeup Time changed.	
V3.0.2		
Page 91	Typo fixed (“ist” -> “is”).	
Page 95	Footnote added for 30 kHz, 50 kHz and 100 kHz.	
Page 95	Explanation for how to choose FIR1 decimation rate added.	
Page 95	Passband ≤ 10 kHz changed to < 10 kHz.	
Page 61	Headline “Group Delay” enhanced to “Group Delay and Settling Time”, additional information regarding settling time new.	
Page 61	Table “Settling Time Summary” new.	
Page 61	Enhanced example by settling time.	
Page 62	Added note regarding filter chain.	
V3.0.3		
Page 56	Updated section “Starting the Integration Window”.	
Page 13	Corrected Application Reset Value for register KRSTCLR.	
V3.0.4		
Page 55	Updated and replaced „Integrator Operation” figures.	
Page 57	Added 4. sub-item.	
Page 61	Changed values for CIC3 and FIR1.	
Page 95	Added two new sentences.	
V3.0.5		
Page 61	Updated formula.	

Table 295 Revision History (cont'd)

Reference	Change to Previous Version	Comment
Page 30	Updated result value ranges.	
V3.0.6		
Page 16, **Page 97**	Added description regarding GLOBRC register.	
Page 45	Formula updated to fit the example.	