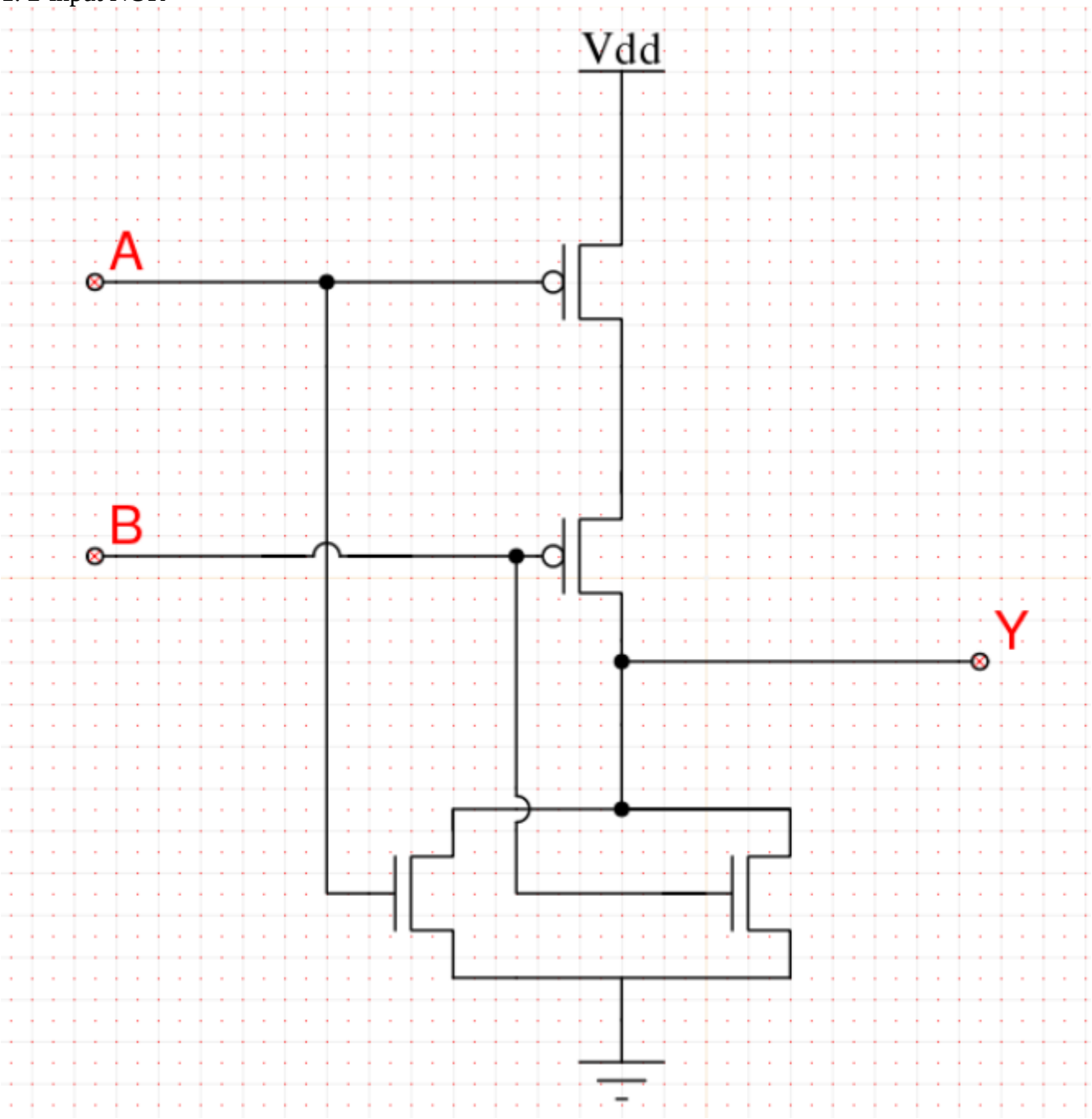
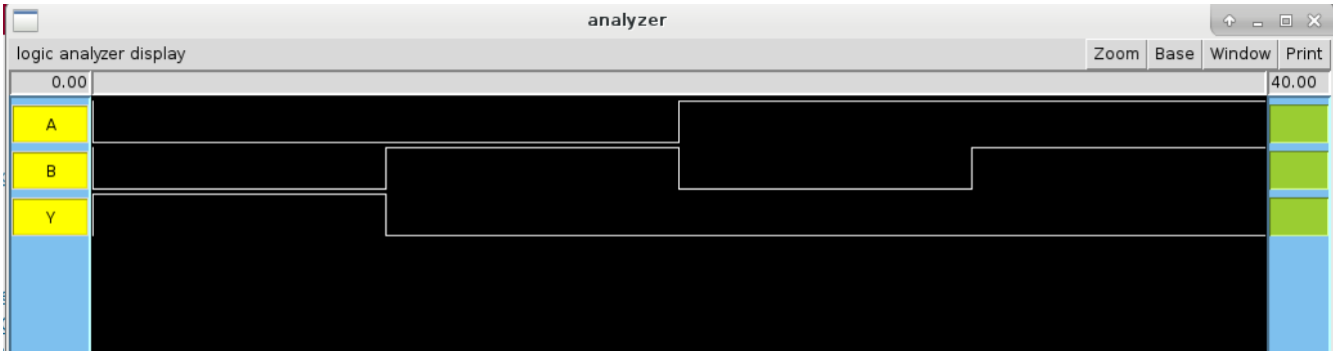
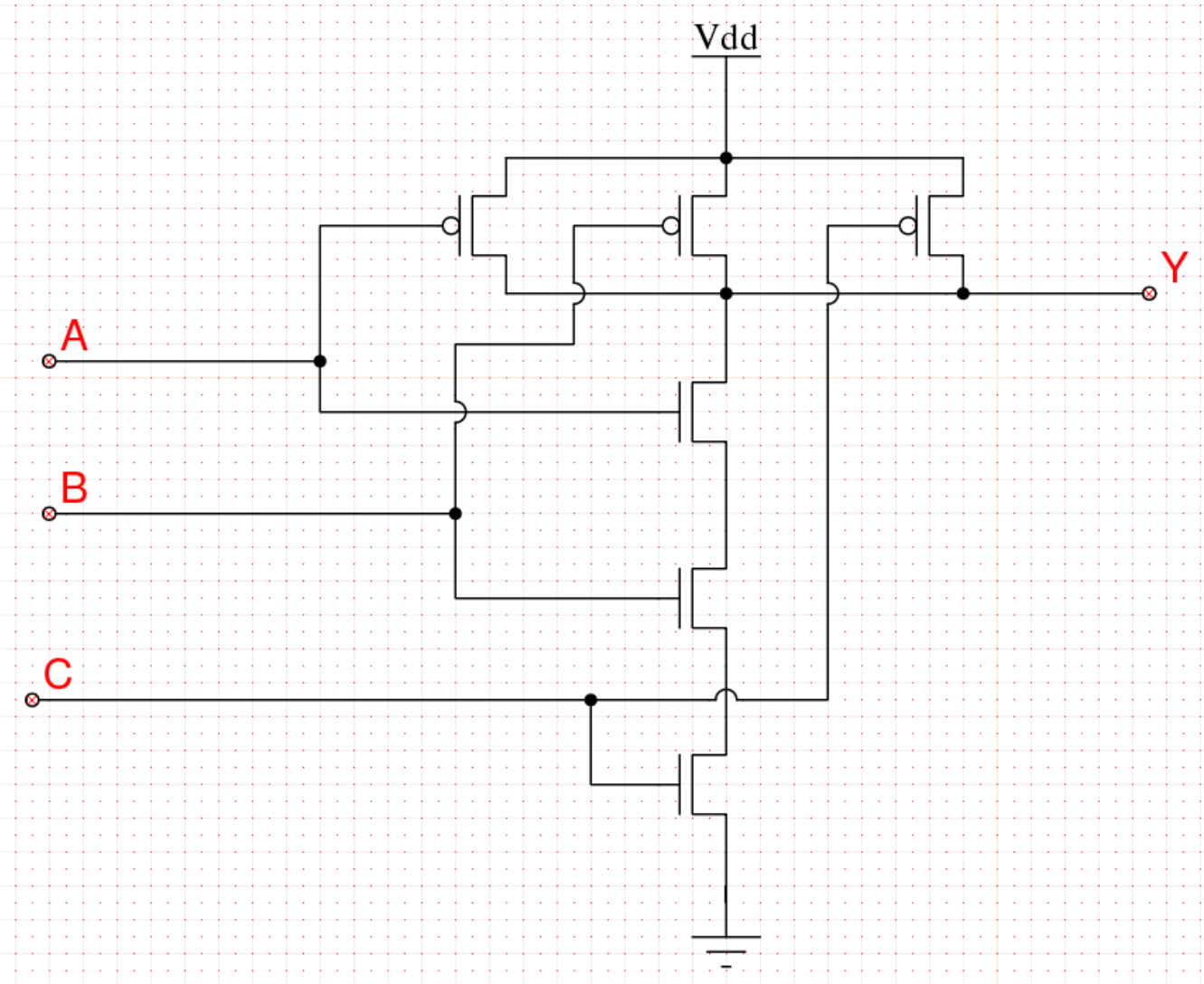
Marty Townley
Adam Ness
Lab 1 – Schematic Capture and Simulation

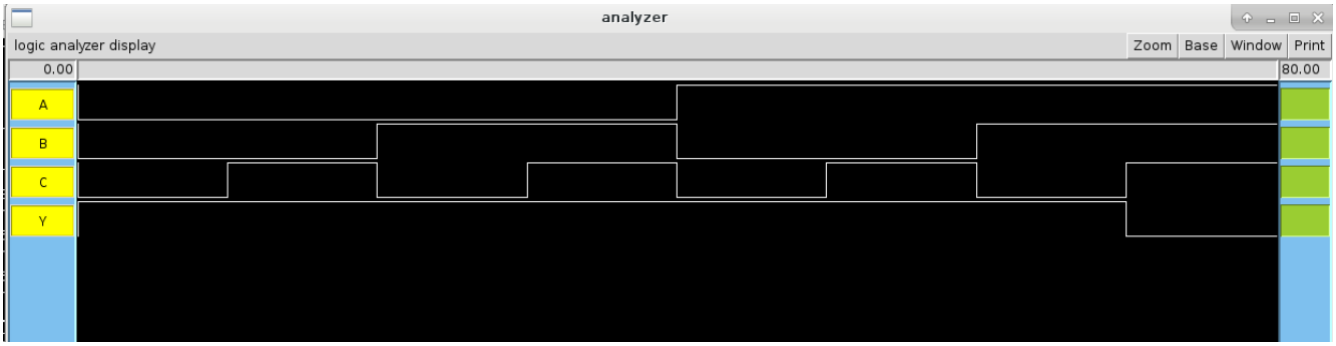
1. 2-input NOR




analyzer
logic analyzer display
Zoom
Base
Window
Print
0.00
40.00
A
B
Y

2. 3-input NAND





Command File for 3 input nand test:

```
h Vdd
l Gnd
analyzer A B C Y
vector in A B C
setvector in 000
s
setvector in 001
s
setvector in 010
s
setvector in 011
s
setvector in 100
s
setvector in 101
s
setvector in 110
s
setvector in 111
s
```