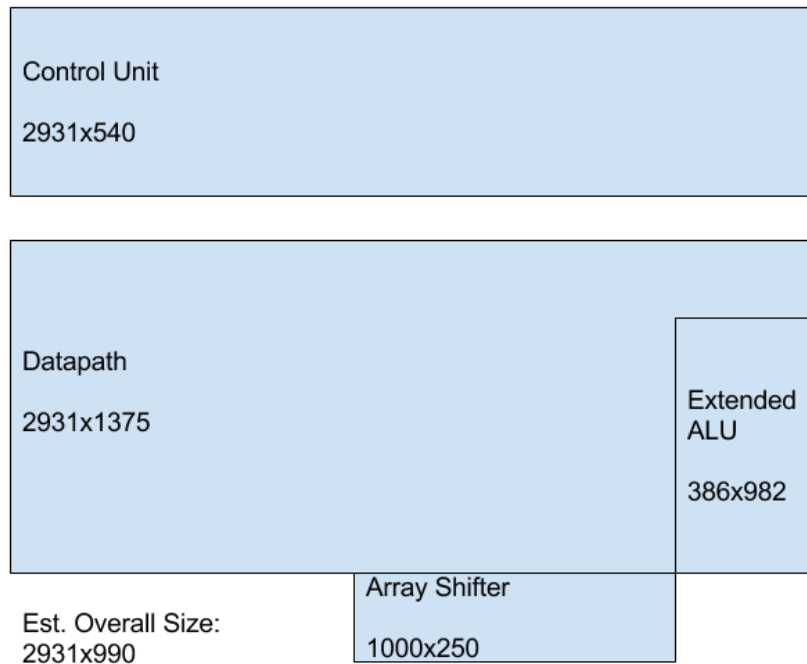


Adam Ness and Marty Townley  
ECE425  
4/11/17

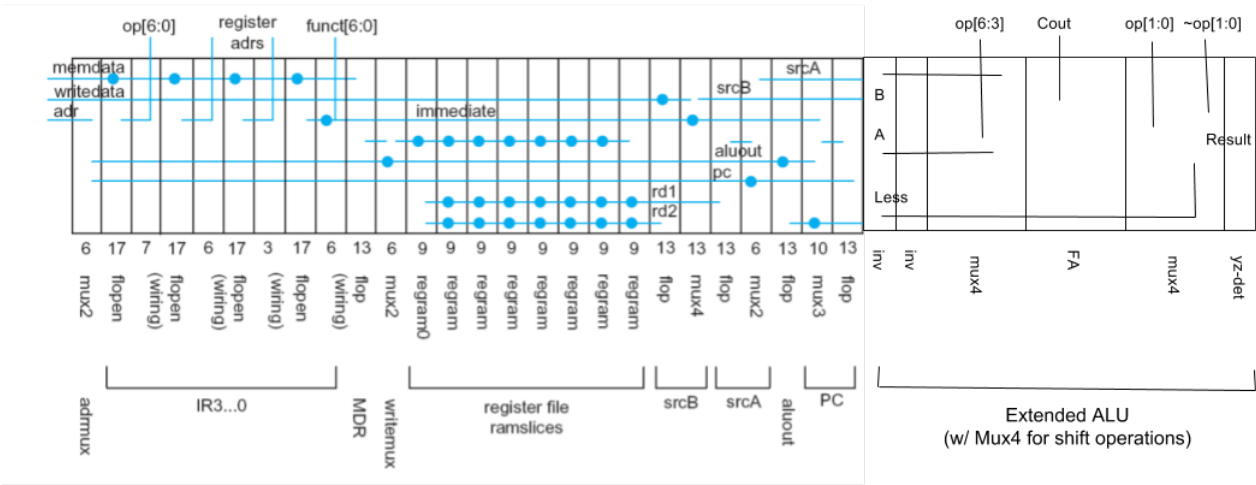
## **Design Plan**

### **Chip Floor Plan:**



Note: Shifter shown includes source gen. + decoder logic  
Dimensions listed in lambda

Datapath Slice Plan:



Shifter Slice Plan:

