Slave

to implement Read operation FSM (ARVALID & RREADY) (ARVALID & & RREADY) assert ARREADY ADDR IDLE deasserr ARREADY DATA RESP assert RVALID RREADY de assert-, RREADY RVALID

## SLAVE

to implement write operation FSM (AWVALID & WVALID) AWVALID 22 WVALID IDUE ADDR ) asseyl-AWREADY & WREADY RESP doassert-BREADY DATA BVALID 9 deassert A WRGADY & WREADY a Assert BVALID 1 BREADY

MASTER read operation implement FSM 40 Ad-en hh (Read-Address == ARADDR) (rd-en ht Read-Address = ARADDE) LARRUADY assert-DUE ARVALID RREADY Jack-ARREADY deasser RESP DATA ARVALIN RVALID deumerl

RREADY

[ RVALID

MASTER

Fsm to implement write operation (Write-address = 2 AWDDR & & wh-en 22 write\_Data == WDATA) (Wr-en & & Corite-Address = = AWDDR write\_Data = = WDATA IDLE assert ADDR AWVALID WVALID BREADY JWROADY RESP WREADY BVALID deassert DATA deassert ANUALIDY WYALID BREADY BUALID

MASTER to implement read operation FSM 2d-en 22 (Read-Addron = = ARADOR) rd-en 22 (Read-Address! = ARADRR) IDLE ADDR C ARVALID
ERREADY JARREADY ARREADY RESP doassen- RVALID g deasur - ARVALID RREADY DATA

1 RVALID