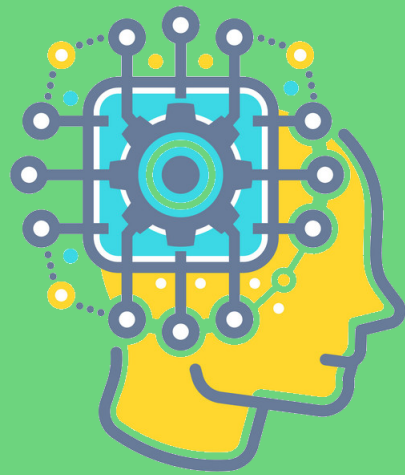
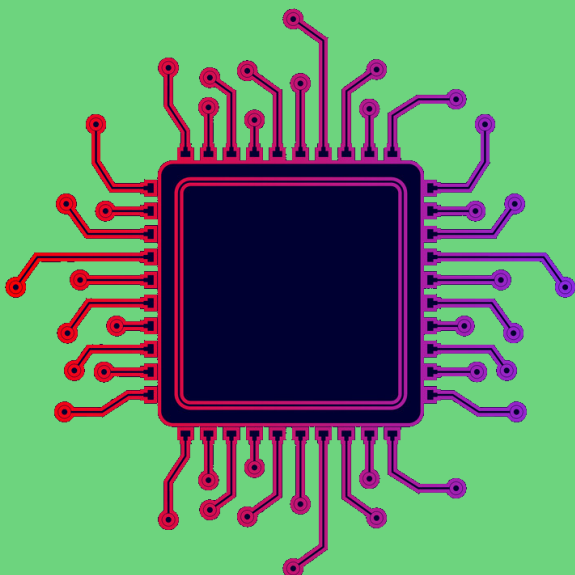


This insight is now the basis for his startup, **F5BML BHJD**, which launched its first suite of products on June 18, 2020. Their idea is to allow any company to deploy **B % NPEFM XJJ PVUU F OFFE GPSTQFDBM FE I BSEXBSF**. It would not only lower the cost of applying DL algorithms, but also make AI more widely accessible.



#VUI PX EJE U JT I BQQFO %FFQ ~~MBSQCH~~ BMIPSU NT BSF DPN QVUBUPOB ~~M~~ FYQFOTJW BOE SFRVJSF QPX FSGM I BSEXBSF UP PQFSBUF ' PSU JT ~~M~~UT EMF JOP U F UPQD PG (16T BOE \$ 16T

GPU chips were initially designed to ~~SFOEF~~SHSBQI JDT JO BQQM ~~DBU~~POT such as video games. Unlike CPUs, which generally have ~~UP~~ DPN QM ~~Y~~ DPSFT for doing a variety of computations, (16T I BW T of simple cores that can perform only specific operations, but the cores can tackle their operations at the same time rather than one after another, TI SOLCH U F UN F JUUBLFT UP DPN QM ~~U~~ BO JOUFOTJW DPN QVUBUPO.



The AI research community soon realized that this massive parallelization makes GPUs great for deep learning. Like graphics rendering, % JOVP ~~MFT~~ TJN QM NBU FN BUDBM ~~DBN~~ ~~DBU~~POT performed hundreds of thousands of times.