


# REVISION HISTORY

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- P02: BLOCK&POWER TREE
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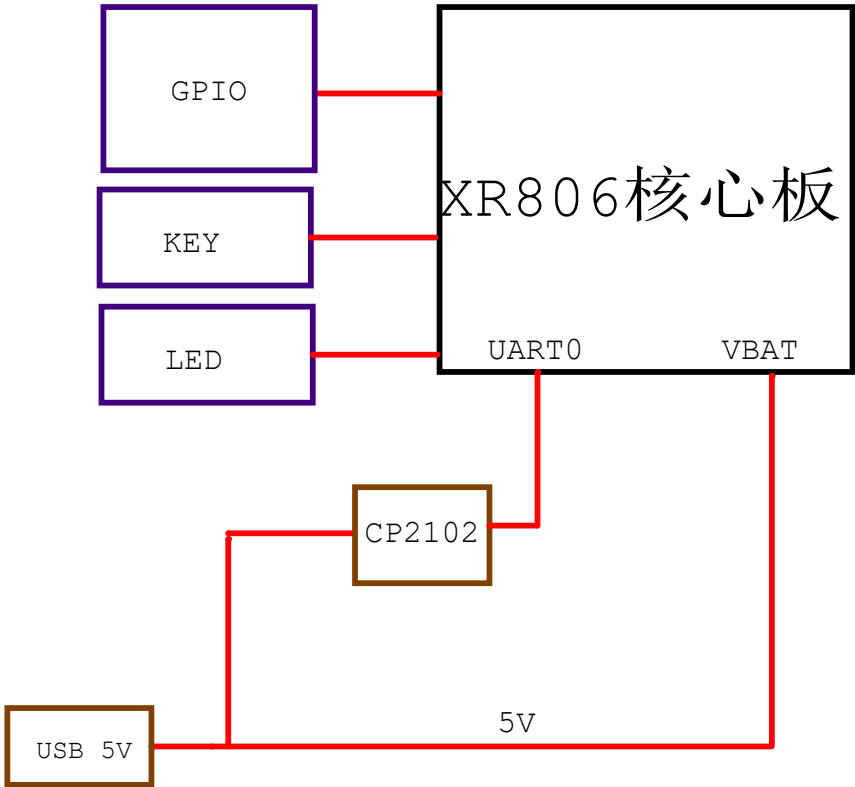
Revision	Description	Date	Drawn
Ver 1.0	draft	2021-8-26	huangzhaojian



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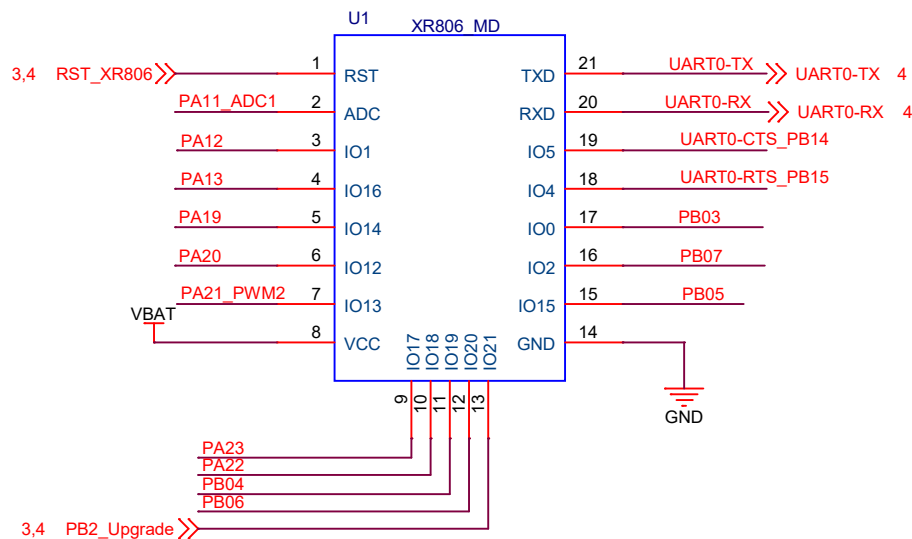
AllWinner Online Technology Co.,Ltd		
Design Name		
XR806AF2L_BaseBoard		
Size	Page Name	Rev
A4	01 VERSION HISTORY	
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# Block & Power Tree

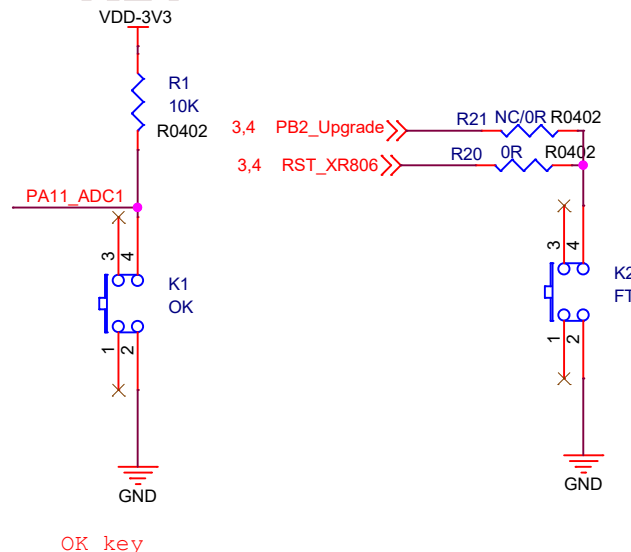


AllWinner Online Technology Co.,Ltd			
Design Name			
XR806AF2L_BaseBoard			
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A4	02 BLOCK & POWER TREE		
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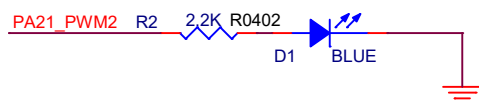
# XR806AF2L Core



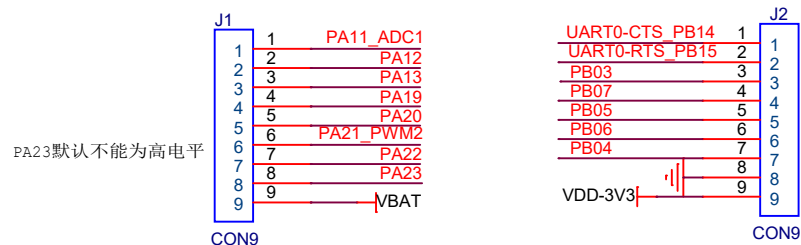
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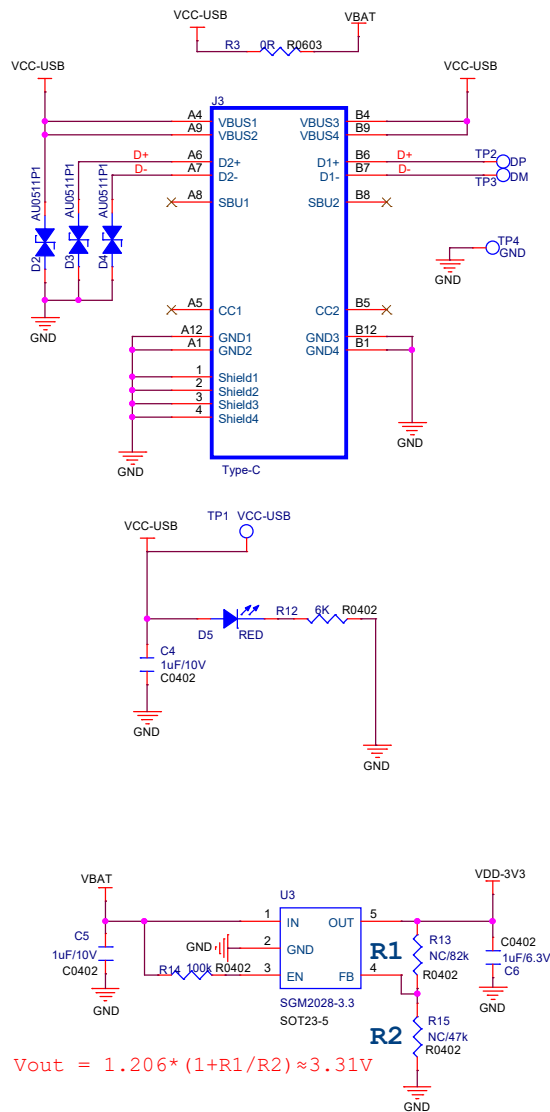
## LED/PWM



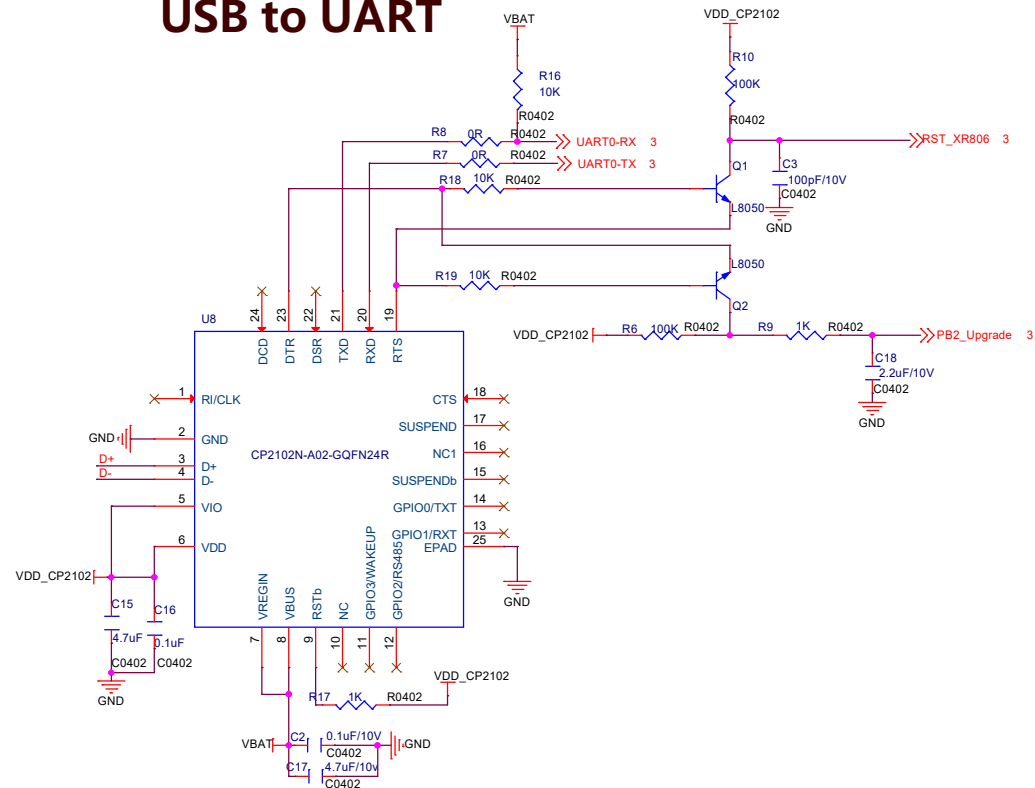
## INTERFACE



# POWER



# USB to UART



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Design Name  
XR806AF2L\_BaseBoard

Size  
A3  
Page Name  
04 POWER&USB

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