

Basics on code optimization & OpenMP

Simon Scheidegger
simon.scheidegger@unil.ch
September 6th, 2018

Cowles Foundation – Yale University

Today's Roadmap:

1. Basic optimization of serial code.
2. Shared memory parallelism – OpenMP (background).
3. Exploring the some first features of OpenMP
 - Loops
 - Sections
 - Reductions (max, summation,...)
4. Discrete-state dynamic programming with OpenMP

Some literature & other resources

Full standard/API specification:

- <http://openmp.org>

Tutorials:

- <https://computing.llnl.gov/tutorials/openMP/>

Books:

- “Introduction to High Performance Computing for Scientists and Engineers”
Georg Hager, Gerhard Wellein

Basic optimization of serial code

In the age of multi-1000-processor parallel computers, writing code that runs efficiently on a single CPU has grown slightly old-fashioned.

Nevertheless there can be no doubt that single-processor optimizations are of premier importance.

- If a **speed-up of two** can be achieved by some **simple code changes**, the user will be satisfied with much fewer CPUs in the parallel case.
- This frees resources for other users and projects, and puts hardware that was often acquired for **considerable amounts of money to better use**.
- If an existing parallel code is to be optimized for speed, it must be the first goal to **make the single processor** run as fast as possible.

Profiling*

- Gathering information about a program's behaviour, specifically its use of resources, is called **profiling**.
- The most important “resource” in terms of high performance computing is **runtime**.
 - Hence, a common profiling **strategy is to find out how much time is spent in the different functions**, and maybe even lines, of a code in order to **identify hot spots**, i.e., the parts of the program that require the dominant fraction of runtime.
- These hot spots are subsequently analysed for possible optimization opportunities.
 - Software for profiling (e.g. GNU gprof,...)

Definition of Profiling

The performance needs of software vary, but it's probably not surprising that many applications have **very stringent speed requirements**.

In general, for all but the simplest applications, **the better the performance, the more useful and popular the application will be**. For this reason, performance considerations are (or should be) in the forefront of many application developers' minds.

Unfortunately, much of the effort that is expended attempting to make applications faster is wasted, because **developers will often micro-optimize their software** without fully exploring how the program operates at a macro scale. For instance, you might spend a large amount of time making a particular function run twice as fast, which is all well and good, but if that function is called very rarely (when a file is opened, say) then **reducing the execution time from 200ms to 100ms isn't going to make much difference to the overall execution time of the software**.

A more fruitful use of your time would be spent optimizing those parts of the software that are called more frequently.

It is therefore vital that you **have accurate information on exactly where the time is being spent within your applications** -- and for real input data -- if you hope to have a chance of optimizing it effectively. This activity is called code profiling.

Profiling a program → Where does it spend its time?

GNU compiler – gprof

<https://sourceware.org/binutils/docs/gprof/>

In general, code should be written with the following three goals, in order of importance:

1. **Make the software work correctly.** This must always be the focus of development. In general, there is no point writing software that is very fast if it does not do what it is supposed to! Obviously, correctness is something of a grey area; a video player that works on 99 percent of your files or plays video with the occasional visual glitch is still of some use, **but in general, correctness is more important than speed.**
2. **Make the software maintainable.** This is really a sub-point of the first goal. In general, if software is not written to be maintainable, then even if it works to begin with, sooner or later you (or someone else) will end up breaking it trying to fix bugs or add new features.
3. **Make the software fast.** Here is where profiling comes in. Once the software is working correctly, then start profiling to help it run more quickly.

→ gprof can profile C, C++, and Fortran applications.

Profiling enabled while compilation

In this first step, we need to make sure that the profiling is enabled when the compilation of the code is done. This is made possible by adding the **'-pg'** option in the compilation step.

-pg : Generate extra code to write profile information suitable for the analysis program gprof. You must use this option when compiling the source files you want data about, and you must also use it when linking.

- compile code: > **g++ -pg myfile.cpp -o example_gprof.exec**
- run code: > **./example_gprof.exec**
- **gmon.out** was generated, contains all profiling information
- run gprof > **gprof "NameOfYourExecutable", i.e. >gprof example_gprof.exec**
(probably pipe it into some file by **>gprof example_gprof.exec | tee out.txt**)

Example gprof

https://people.sc.fsu.edu/~jburkardt/f_src/gprof/gprof.html

1. Go to the folder

> cd YaleParallel2018/day2/code/gprof_example

2. Compile the code by typing

> make

3. run the code in debug

>./example_gprof.exec (FORTRAN)

>./example_gprof_cpp.exec (CPP)

>gprof example_gprof.exec | tee profile.txt

4. Look at the output

>less profile.txt

First chunk of output

This time consists only of
time spent in this function, and
not in anything that function calls!!

Flat profile:

Each sample counts as 0.01 seconds.

% time	cumulative seconds	self seconds	calls	self ms/call	total ms/call	name
83.99	0.47	0.47	501499	0.00	0.00	daxpy_
10.72	0.53	0.06	2000000	0.00	0.00	d_random_
3.57	0.55	0.02	1	20.01	498.49	dgefa_
1.79	0.56	0.01	999	0.01	0.01	idamax_
0.00	0.56	0.00	993	0.00	0.00	d_swap_
0.00	0.56	0.00	2	0.00	30.02	d_matgen_
0.00	0.56	0.00	2	0.00	0.00	timestamp_
0.00	0.56	0.00	1	0.00	560.41	MAIN_
0.00	0.56	0.00	1	0.00	1.87	dgesl_

- %
time the percentage of the total running time of the program used by this function.
- cumulative
seconds a running sum of the number of seconds accounted for by this function and those listed above it.
- self
seconds the number of seconds accounted for by this function alone. This is the major sort for this listing.
- calls the number of times this function was invoked, if this function is profiled, else blank.
- self
ms/call the average number of milliseconds spent in this function per call, if this function is profiled, else blank.
- total
ms/call the average number of milliseconds spent in this function and its descendents per call, if this function is profiled, else blank.

Second chunk of output

The second table is different.
It displays the time a function takes,
and everything it calls.

- main takes all the time.
- this table contains information on **how much time a function spends in all of his children.**
- e.g. dgefa_ was called 1x from main.

Note: some compiler switch on optimizations automatically.

- **never run with -O3 for profiling!**
- use **-O2** flag in that case! (this should not re-arrange your code too much.

granularity: each sample hit covers 2 byte(s) for 1.78% of 0.56 seconds

index	% time	self	children	called	name
[1]	100.0	0.00	0.56	1/1	main [2]
		0.02	0.48	1/1	MAIN_ [1]
		0.00	0.06	2/2	dgefa_ [3]
		0.00	0.00	1/1	d_matgen_ [6]
		0.00	0.00	2/2	dgesl_ [8]
		0.00	0.00	2/2	timestamp_ [10]
<spontaneous>					
[2]	100.0	0.00	0.56	1/1	main [2]
		0.00	0.56	1/1	MAIN_ [1]
		0.02	0.48	1/1	MAIN_ [1]
[3]	89.0	0.02	0.48	1	dgefa_ [3]
		0.47	0.00	499500/501499	daxpy_ [4]
		0.01	0.00	999/999	idamax_ [7]
		0.00	0.00	993/993	d_swap_ [9]
		0.00	0.00	1999/501499	dgesl_ [8]
		0.47	0.00	499500/501499	dgefa_ [3]
[4]	83.9	0.47	0.00	501499	daxpy_ [4]
		0.06	0.00	2000000/2000000	d_matgen_ [6]
[5]	10.7	0.06	0.00	2000000	d_random_ [5]
		0.00	0.06	2/2	MAIN_ [1]
[6]	10.7	0.00	0.06	2	d_matgen_ [6]
		0.06	0.00	2000000/2000000	d_random_ [5]
		0.01	0.00	999/999	dgefa_ [3]
[7]	1.8	0.01	0.00	999	idamax_ [7]

Questions?

1. Advice – <http://imgtfy.com/>
<http://imgtfy.com/?q=gprof>



Common sense optimizations

- Very simple code changes can often lead to a significant performance boost.
- Some of those hints may **seem trivial**, but experience shows that many scientific codes can be improved by the simplest of measures.

→ **e.g. do less work**

```
1 logical :: FLAG
2 FLAG = .false.
3 do i=1,N
4   if(complex_func(A(i)) < THRESHOLD) then
5     FLAG = .true.
6   endif
7 enddo
```

If `complex_func()` has no side effects, **the only information that gets communicated to the outside of the loop is the value of FLAG**. In this case, depending on the probability for the conditional to be true, much computational effort can be saved by **leaving the loop as soon as FLAG changes state**.

EXIT THE LOOP

```
1 logical :: FLAG
2 FLAG = .false.
3 do i=1,N
4   if(complex_func(A(i)) < THRESHOLD) then
5     FLAG = .true.
6   ► exit
7   endif
8 enddo
```

Common sense optimizations II

Avoid branching!

In this multiplication of a matrix with a vector, the upper and lower triangular parts get different signs and the diagonal is ignored. The **if** statement serves to decide about which factor to use.

→ Fortunately, the loop nest can be transformed so that all if statements vanish:

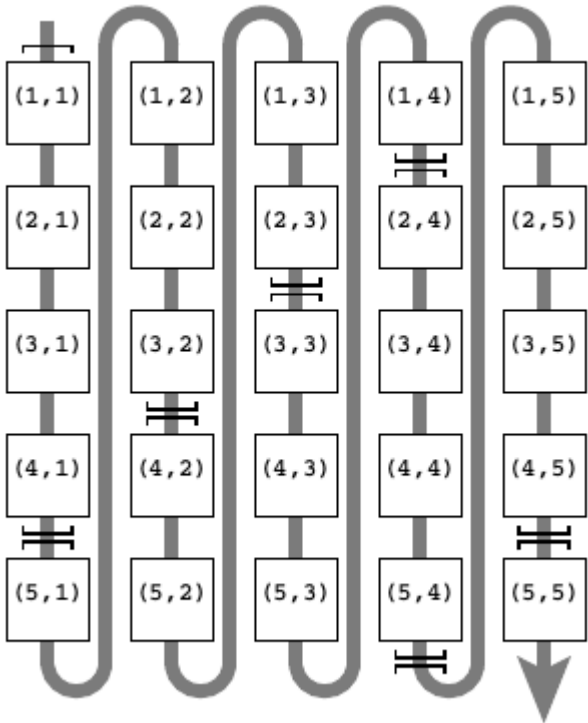
```
1 do j=1,N
2   do i=1,N
3     if(i.ge.j) then
4       sign=1.d0
5     else if(i.lt.j) then
6       sign=-1.d0
7     else
8       sign=0.d0
9     endif
10    C(j) = C(j) + sign * A(i,j) * B(i)
11  enddo
12 enddo
```

```
1 do j=1,N
2   do i=j+1,N
3     C(j) = C(j) + A(i,j) * B(i)
4   enddo
5 enddo
6 do j=1,N
7   do i=1,j-1
8     C(j) = C(j) - A(i,j) * B(i)
9   enddo
10 enddo
```

Data access (example)

Stride-N access

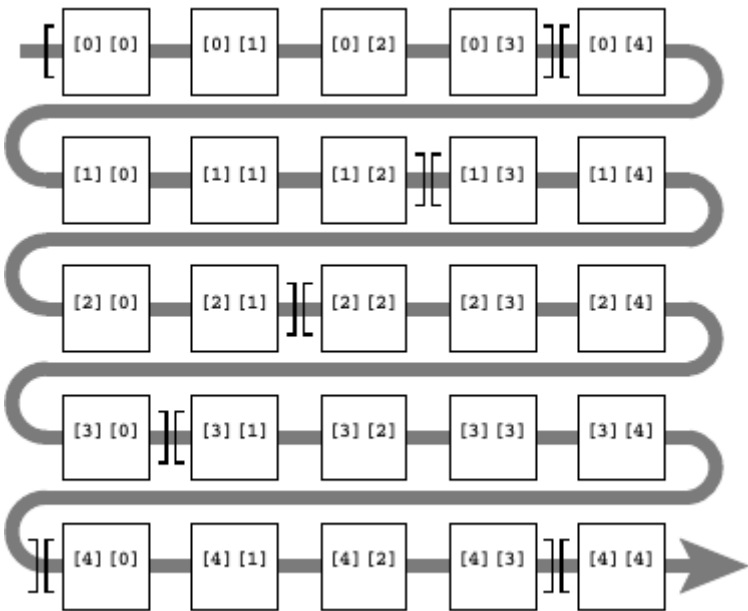
```
1 do i=1,N
2   do j=1,N
3     A(i,j) = i*j
4   enddo
5 enddo
```



Fortran: Column major

Stride-1 access

```
for(i=0; i<N; ++i) {
  for(j=0; j<N; ++j) {
    a[i][j] = i*j;
  }
}
```



C: Row major

Small exercise on loop ordering

check directory:

```
> cd YaleParallel2018/day2/code/loop/loop.f90
```

compile:

```
> ./compile_loop.sh
```

run:

```
> ./test_loop
```

To be done:

a) Inspect code

b) run code (play with array size & re-compile)

Using SIMD instruction sets → Vectorization

Vectorization performs multiple operations in **parallel** on a core with a single instruction (SIMD – single instruction multiple data).

Data is loaded into vector registers that are described by their width in bits:

- 256 bit registers: 8 x float, or 4x double
- 512 bit registers: 16x float, or 8x double

Vector units perform arithmetic operations on vector registers simultaneously.

Vectorization is key to maximising computational performance.

Vectorization illustrated

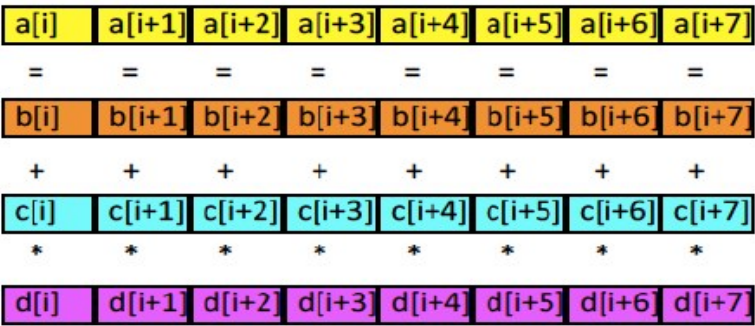
sequential

```
a [i] = b[i] + c[i] * d[i];
```



8× vectorization

```
a [i:8] = b[i:8] + c[i:8] * d[i:8];
```



In an optimal situation all this is carried out by the compiler automatically. Compiler directives can be used to give hints as to where vectorization is safe and/or beneficial.



```
1 ! vectorized part
2 rest = mod(N,4)
3 do i=1,N-rest,4
4   load R1 = [x(i),x(i+1),x(i+2),x(i+3)]
5   load R2 = [y(i),y(i+1),y(i+2),y(i+3)]
6   ! "packed" addition (4 SP flops)
7   R3 = ADD(R1,R2)
8   store [r(i),r(i+1),r(i+2),r(i+3)] = R3
9 enddo
10 ! remainder loop
11 do i=N-rest+1,N
12   r(i) = x(i) + y(i)
13 enddo
```

Advanced Vector Extensions (AVX)

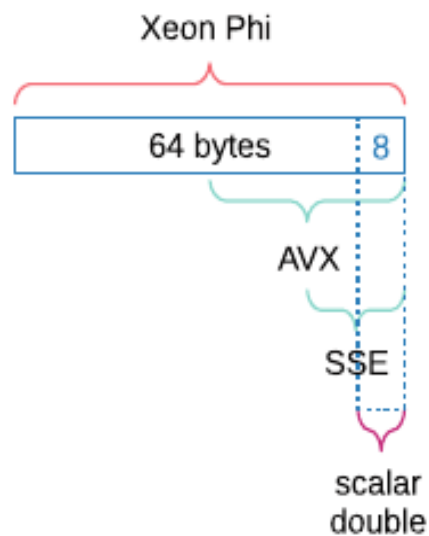


Fig. 7. Vector registers on modern CPUs: a scalar program can utilize only 1/4 of computational parallelism on AVX-enabled CPUs, e.g. the SandyBridge.

How to use vectorization

- **use vector intrinsics (see example)**
 - explicit hardware-specific instructions.
 - high performance.
 - non-portable and hard to maintain.
 - see, e.g. <https://software.intel.com/sites/landingpage/IntrinsicsGuide/>
- **automatic compiler vectorization**
 - compiler will vectorize where it is possible.
 - compilers can do a poor job.
- **use libraries that are already vectorized**
 - let somebody else do the work for you.

Does my code vectorize?

- Not clear a priori.
- Compilers can generate reports that summarise which loops vectorized.
- You can ask for different levels of detail e.g. only loops that failed to vectorize e.g. whether to explain why a loop didn't vectorize.
- The flags vary from compiler to compiler, e.g.:

- **Intel** : `-vec-report=n`, or `-opt-report=n`
- **GCC**: `-ftree-vectorizer-verbose=n`
- **Cray**: `-h list=a`

You can also use the `disassemble` command in **gdb**, if you like reading assembly.

Small example on vectorization

check directory:

```
> cd YaleParallel2018/day2/code/vectorization/avx-intrinsics.cpp
```

compile:

```
> ./compile_avx.sh
```

run:

```
> ./avx-example
```

To be done:

a) Inspect code

Compilers

- Most high-performance codes benefit, to varying degrees, from employing **compiler-based optimizations**, e.g. standard optimization options (**-O0, -O1, . . .**).
- Every modern compiler has command line switches that allow a (more or less) fine-grained tuning of the available optimization options.
- Sometimes it is even worthwhile **trying a different compiler** just to check whether there is more performance potential. One should be aware that the compiler has the extremely complex job of mapping source code written in a high-level language to machine code, thereby utilizing the processor's internal resources as well as possible.
- However, there is no guarantee that this is actually the case and the programmer should at least be aware of the basic strategies for automatic optimization and potential stumbling blocks that prevent the latter from being applied. **It must be understood that compilers can be surprisingly smart and stupid at the same time.**
- A common statement in discussions about compiler capabilities is **"The compiler should be able to figure that out."** This is often a false assumption.

Break with mountains



Shared memory parallelism: OpenMP

(Open Multi Processing)

- **OpenMP** website is a good source of information:

→ openmp.org



- You can find there:
- tutorials and examples for all levels.
 - the standard.
 - quick references guide.

Reminder: Free Lunch is over

- For a long time high-performance computing had a "free-lunch".
- The density of transistors in chips increased, decreasing the size of integrated circuits.
 - same number of transistors with less power.
 - More transistors to add functionality.
- The clock speeds steadily rose, increasing the number of operations per second (from MHz to GHz).
- The free lunch has been over for a few years now.
 - We are reaching the limitations of transistor density.
 - Increasing clock frequency requires too much power.

We used to focus on floating point operations per second! now we also think about floating point operations per Watt!

Multicore

- The number of transistors is still increasing:
 - Sandy Bridge 28 nm, Haswell 22 nm, Broadwell 14 nm.
 - Projected hard limit of **5-7 nm**.
- This has lead to three trends:
 - more cores the
 - Sandy Bridge processor on Cray XC50 has 12 cores
 - Xeon Phi KNL has 72 cores (e.g. Nersc's Cori)
 - reducing clock speed
 - simplify/specialized cores

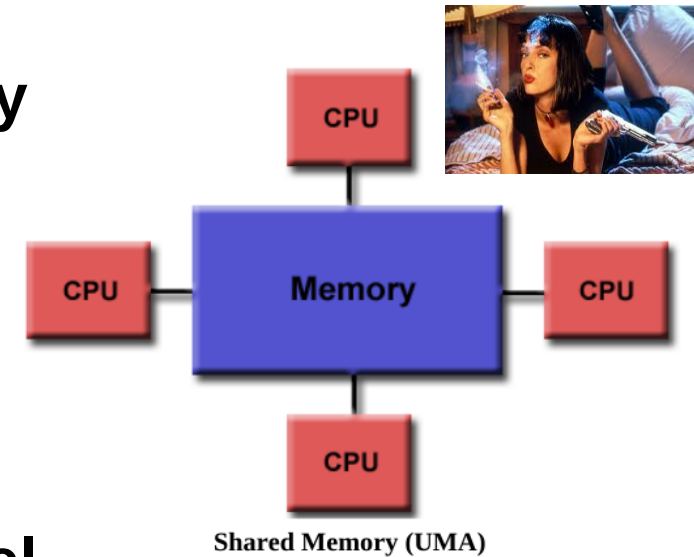
An extreme example of this are GPUs, which have in the order of 100/1000s of cores specialized for tasks common in graphics.

Shared memory systems

- Process can access same GLOBAL memory

- **Uniform Memory Access (UMA)** model

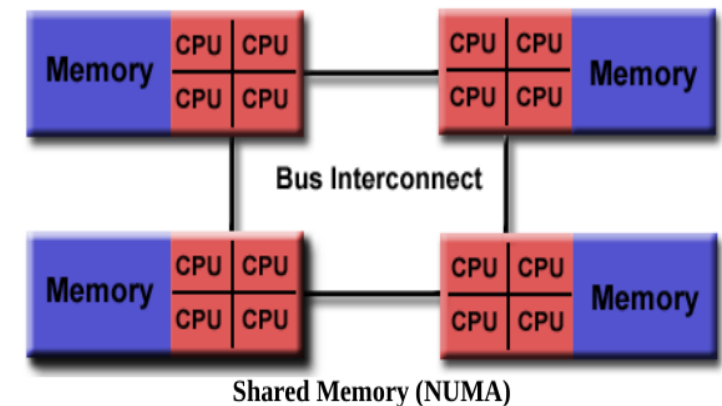
- Access time to memory is uniform.
- Local cache, all other peripherals are shared.



- **Non-Uniform Memory Access (NUMA)** model

- Memory is physically distributed among processors.
- Global virtual address spaces accessible from all processors.
- Access time to local and remote data is different.

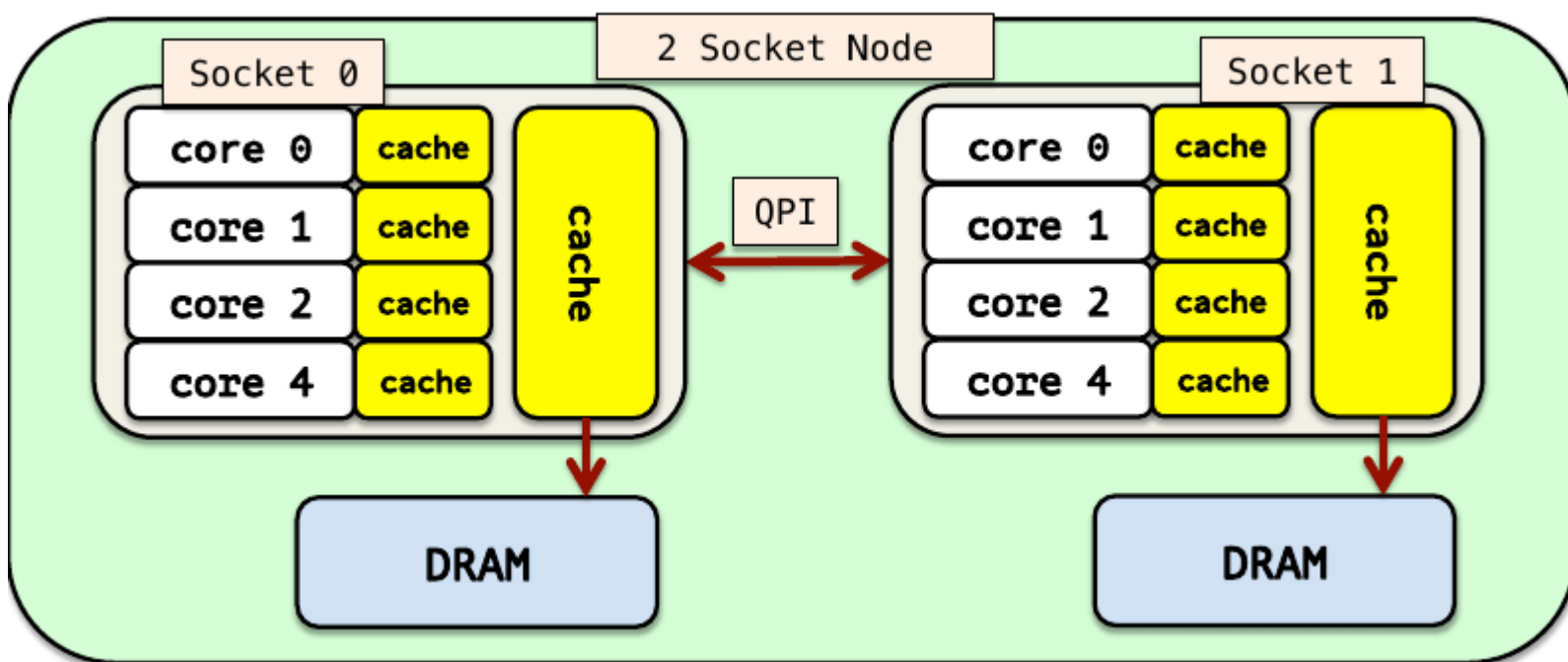
→ **OpenMP**, but other solutions available (e.g. Intel's TBB).



Multicore Nodes

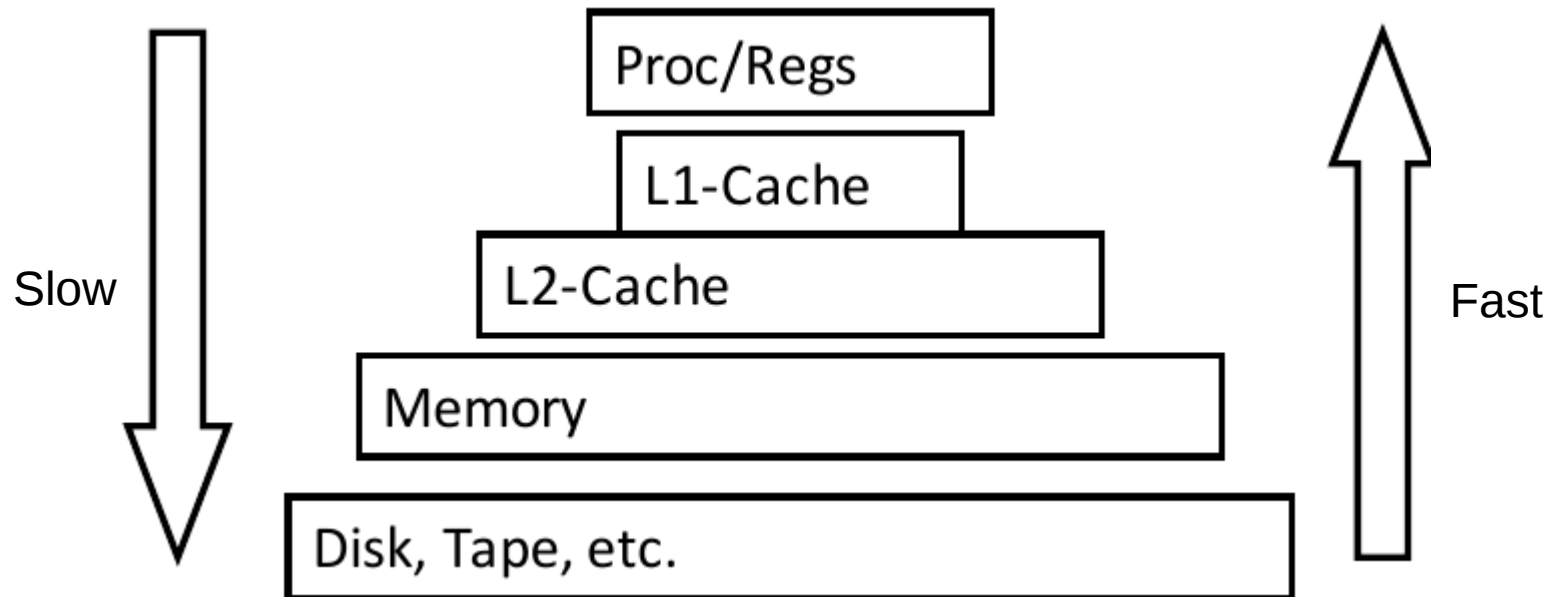
A CPU-based node can have multiple sockets, each having multiple cores.

Cores on a socket share cache and uniform DRAM access.



Memory speed & Cache

- Small, fast storage used to improve average access time to slow memory.
- Exploits **spatial** and **temporal** locality.



Data access speed

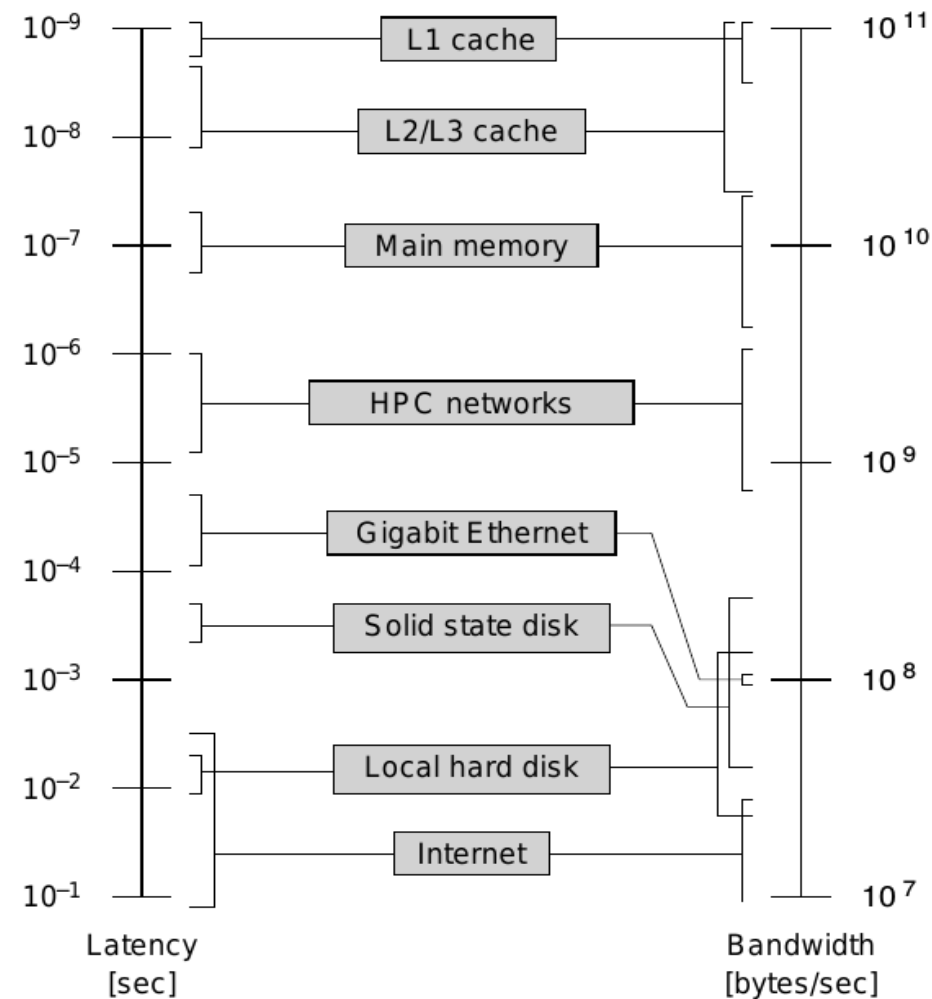


Figure 3.1: Typical latency and bandwidth numbers for data transfer to and from different devices in computer systems. Registers have been omitted because their “bandwidth” usually matches the computational capabilities of the compute core, and their latency is part of the pipelined execution.

Shared Memory – Pro's & Con's

Pro's

- Global address space provides a **userfriendly programming perspective** to memory.
- Data sharing between tasks is both fast and uniform due to the proximity of memory to CPUs.

Con's

- **Lack of scalability** between memory and CPUs. Adding more CPUs **geometrically increases traffic** on the shared memory-CPU path...
- Programmer responsibility for synchronization constructs that ensure "correct" access of global memory.

What is OpenMP?

Application Program Interface (API), jointly defined by a group of major computer hardware and software vendors (e.g. Intel, Cray, PGI,...).

OpenMP provides a **portable, scalable model** for developers of shared memory parallel applications.

Supports C/C++ and Fortran on a wide variety of architectures.

→ API may be used to explicitly direct multi-threaded, shared memory parallelism.

The API comprised of three main components:

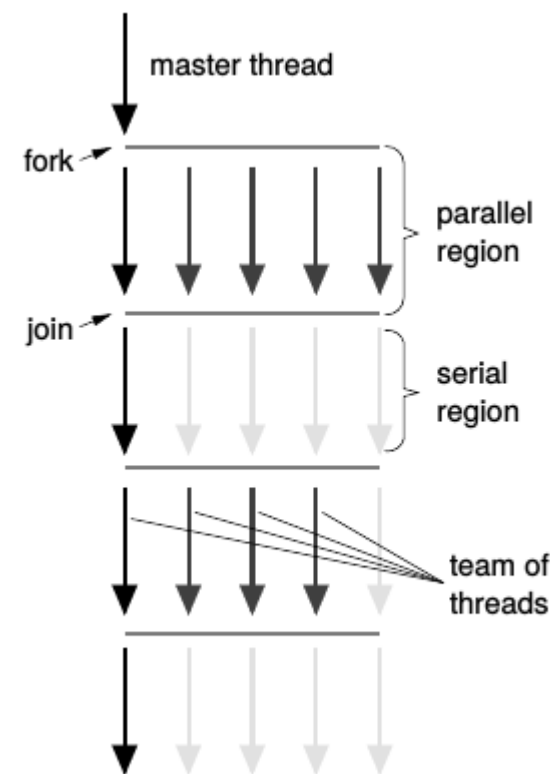
- 1) **Compiler Directives**
- 2) **Runtime Library Routines**
- 3) **Environment variables**

Goals of OpenMP

- Standardization
- Ease of use:
 - Concise and simple set of directives.
 - **Possible to get good speed-up with a handful of directives.**
 - You can incrementally add it to the code without major changes.
- Should I use OpenMP?
 - **Path least resistance to parallelize your code...**
 - For many-core architectures like Xeon Phi,
lightweight threading is required since MPI does not scale there...

Fork and Join Model

- OpenMP uses **fork** and **join** model for threading.
- The application starts with a master thread:
 - **FORK**: a team of parallel worker threads is started at the beginning of each parallel block.
 - The block is executed in parallel by each thread.
 - **JOIN**: the worker threads are synchronized at the end of the parallel block and join with the master thread.
- Threads are numbered **0:N-1** (N is the total number of threads).
- The **master** thread is always numbered 0.



OpenMP compiler directives

Compiler directives appear as comments in your source code and are ignored by compilers unless you tell them otherwise usually by **specifying the appropriate compiler flag**.

OpenMP compiler directives are used for various purposes:

- Spawning a parallel region.
- Dividing blocks of code among threads.
- Distributing loop iterations between threads.
- Serializing sections of code.
- Synchronization of work among threads.

- Compiler directives have the following syntax:

sentinel directive-name [clause, ...]

For example:

Fortran	!\$OMP PARALLEL DEFAULT(SHARED) PRIVATE(BETA,PI)
C/C++	#pragma omp parallel default(shared) private(beta,pi)

Compiling OpenMP

- Most compilers require a **flag** to enable OpenMP compilation
 - without any flag, the **#pragma** or **!\$** directives are ignored by the compiler and a serial application is created.
- Compilers that don't understand OpenMP will simply ignore the directives (no portability problems).

Cray	: on by default for -O1 and greater, disable with -h noomp
Intel	: off by default, enable with -openmp
GNU	: off by default, enable with -fopenmp
PGI	: off by default, enable with -mp

Runtime library

- OpenMP API includes a growing number of **runtime library routines**.

These are used for a variety of purposes:

- Setting and querying the **number of threads**.
 - **Querying a thread's unique identifier** (thread ID), a thread's ancestor's identifier, the thread team size.
 - Setting and querying the **dynamic threads feature**.
 - Querying if in a parallel region, and at what level.
 - Setting and querying nested parallelism.
 - Setting, initializing and terminating locks and nested locks.
 - Querying wall clock time and resolution.
- For C/C++, all of the runtime library routines are actual subroutines.
 - For Fortran, some are actually functions, and some are subroutines.

Runtime library II

- OpenMP has runtime library routines for controlling your application, including

Function:	<code>omp_get_num_threads()</code>
C/ C++	<code>int omp_get_num_threads(void);</code>
Fortran	<code>integer function omp_get_num_threads()</code>
Description: Returns the total number of threads currently in the group executing the parallel block from where it is called.	

Function:	<code>omp_get_thread_num()</code>
C/ C++	<code>int omp_get_thread_num(void);</code>
Fortran	<code>integer function omp_get_thread_num()</code>
Description: For the master thread, this function returns zero. For the child nodes the call returns an integer between 1 and <code>omp_get_num_threads()-1</code> inclusive.	

- There are many others, however these are probably the most commonly used.

Runtime library III

The runtime library requires that the OpenMP **header/module** is included:

<pre><u>#include <omp.h></u> ... int threads = omp_get_max_threads(); int outside = omp_get_num_threads(); int inside; #pragma omp parallel { inside = omp_get_num_threads(); } printf("%d in, %d out, %d max \n", inside, outside, threads);</pre>	<pre><u>use omp_lib</u> ... integer :: threads, inside, outside threads = omp_get_max_threads() outside = omp_get_num_threads() !\$omp parallel inside = omp_get_num_threads() !\$omp end parallel print *, inside, ' in ', outside, ' out ', threads, ' max'</pre>
<pre>> OMP_NUM_THREADS=8 ./a.out 8 in, 1 out, 8 max</pre>	

Running OpenMP applications

- The default number of threads is set with an **environment variable** **OMP_NUM_THREADS**

csh/tcsh	setenv OMP_NUM_THREADS 8
sh/bash	export OMP_NUM_THREADS=8

- Compiling and running:

```
g++ 1a.hello_world.cpp -fopenmp -o 1a.hello_world.exec  
export OMP_NUM_THREADS=8  
./1a.hello_world.exec
```

compile openmp code

set 8 threads

run

“Hello world” in OpenMP (C/C++)

```
#include <omp.h>
#include <stdio.h>
#include <stdlib.h>

int main ()
{
    int nthreads, tid;

    /* Fork a team of threads giving them their own copies of variables */
    #pragma omp parallel private(nthreads, tid)
    {
        /* Obtain thread number */
        tid = omp_get_thread_num();
        printf("Hello World from thread = %d\n", tid);

        /* Only master thread does this */
        if (tid == 0)
        {
            nthreads = omp_get_num_threads();
            printf("Number of threads = %d\n", nthreads);
        }

    } /* All threads join master thread and disband */
}
```

“Hello world” in OpenMP (C++)

```
#include <omp.h>
```

```
main ()
```

```
{
```

```
    int nthreads, tid;
```

```
    #pragma omp parallel private(nthreads, tid)
```

```
{
```

```
    tid = omp_get_thread_num();
```

```
    printf("Hello World from thread = %d\n", tid);
```

```
    if (tid == 0)
```

```
    {
```

```
        nthreads = omp_get_num_threads();
```

```
        printf("Number of threads = %d\n", nthreads);
```

```
    }
```

```
}
```

```
}
```

Non shared copies of
data for each thread

OpenMP directive to
indicate START
segment to be
parallelized

Code segment that
will be executed in
parallel

OpenMP directive to
indicate END
segment to be
parallelized

Data scoping

- Any variables that existed before a parallel region still exist inside, and are **by default shared between all threads**.
 - True work sharing, however, makes sense only if **each thread can have its own, private variables**.
 - OpenMP supports this concept by defining a separate stack for every thread.
1. A variable that exists before entry to a parallel construct can be **privatized**, i.e., made available as a **private instance for every thread**, by a **PRIVATE** clause to the OMP PARALLEL directive. The private variable's scope extends until the end of the parallel construct.
 2. **The index variable of a work-sharing loop is automatically made private.**
 3. **Local variables in a subroutine called from a parallel region are private to each calling thread.** This pertains also to copies of actual arguments generated by the call-by-value semantics, and to variables declared inside structured blocks in C/C++. However, local variables carrying the SAVE attribute in Fortran (or the static storage class in C/C++) will be shared. **Shared variables that are not modified in the parallel region do not have to be made private.**

Scope of Variables

OpenMP provides clauses that describe how variables should be shared between threads

- **shared**: all variables access the same copy of a variable.
 - this is the default behaviour.
 - WARNING: take care when writing to shared variables.
- **private**: each thread gets its own copy of the variable
 - private copy is uninitialized.
 - use *firstprivate* to initialize variable with value from master.

Example 1: "hello world from thread"

1. go to YaleParallel2018/day2/code/openmp:

> **cd YaleParallel2018/day2/code/openmp**

2. Have a look at the code

> **vi 1.hello_world.f90 / vi 1a.hello_world.cpp**

3. compile by typing:

> **make**

4. Experiment with different numbers of threads

> **export OMP_NUM_THREADS=1 (play around with #threads)**

> **./hello_world.exec (FORTRAN)**

> **./1a.hello_world.exec (CPP)**

Example 1: "hello world from thread"

5. experiment with slurm
(the settings)

> **vi submit_openmp_openmp.sh**

```
#!/bin/bash

#SBATCH --job-name=omp_job
#SBATCH --output=omp_job.txt
#SBATCH --ntasks=1
#SBATCH --cpus-per-task=4
#SBATCH --time=10:00

export OMP_NUM_THREADS=$SLURM_CPUS_PER_TASK

### openmp executable
./1.hello_world.exec
```

6. see **1a.hello_world.cpp** line 10: #pragma omp parallel private(nthreads, tid)

→ what happens if we remove **private(tid)** → try out!

Shared memory model

- OpenMP uses a shared memory model.
- All threads can read and write to the same memory locations simultaneously.
- By default variables are shared, so one copy is used by all threads.
- The result of computations where **multiple threads attempt to read/write to a variable are undefined.**

→ see [YaleParallel2018/day2/code/openmp/2a.example_racing_cond.cpp](https://yaleparallel2018.github.io/day2/code/openmp/2a.example_racing_cond.cpp)

→ this is a very common parallel programming bug called **race condition.**

Example 2: Racing condition

```
#include <iostream>
#include <omp.h>

int main(void){
    int num_threads = omp_get_max_threads();
    std::cout << "sum with " << num_threads << " threads" << std::endl;

    int sum=0;

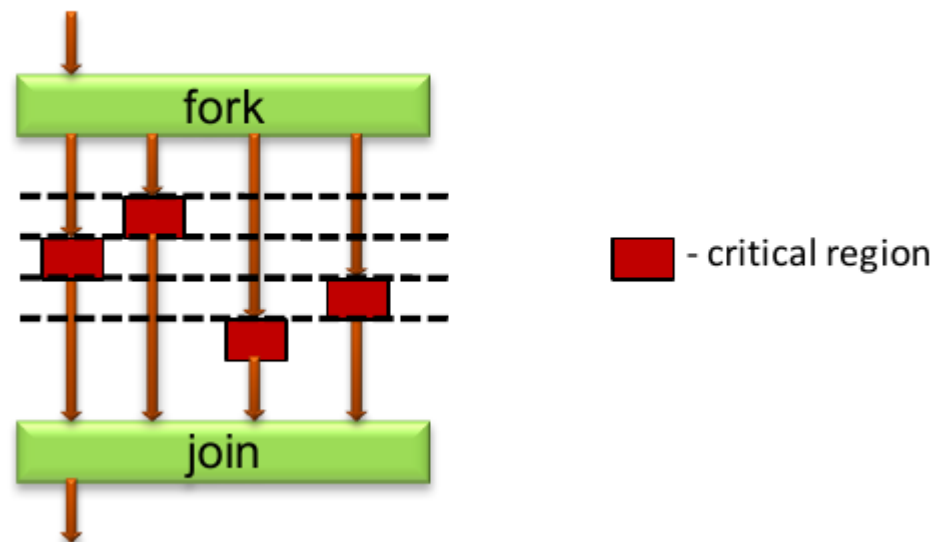
    #pragma omp parallel
    {
        sum += omp_get_thread_num()+1;
    }

    // use formula for sum of arithmetic sequence: sum(1:n) = (n+1)*n/2
    int expected = (num_threads+1)*num_threads/2;
    std::cout << "sum " << sum
              << (sum==expected ? " which matches the expected value "
                                : " which does not match the expected value ")
              << expected << std::endl;

    return 0;
}
```

Synchronization/critical regions

- **Concurrent write access to a shared variable** or, in more general terms, a shared resource, **must be avoided** by all means to circumvent race conditions.
- **Critical regions** solve this problem by making sure that at most one thread at a time executes some piece of code.
- If a thread is executing code inside a critical region, and another thread wants to enter, **the latter must wait (block) until the former has left the region.**



Example 2 fixed: Racing conditions

1. `cd YaleParallel2018/day2/code/openmp/3a.racing_cond_fix.cpp`

2. Have a look at the code

3. compile by typing:

> make

4. Experiment with different numbers of threads

the **CRITICAL** and **END CRITICAL** directives bracket the update to ***sum*** so that the result is always correct.

→ **WARNING: SYNCHRONIZATION (AND SERIAL CODE REGIONS) CAN QUICKLY LIMIT POTENTIAL SPEED-UP FROM PARALLELISM**

Example 3: Race conditions fixed

```
#include <iostream>

#include <omp.h>

int main(void){
    int num_threads = omp_get_max_threads();
    std::cout << "sum with " << num_threads << " threads" << std::endl;

    int sum=0;

    #pragma omp parallel
    {
        #pragma omp critical
        sum += omp_get_thread_num()+1;
    }

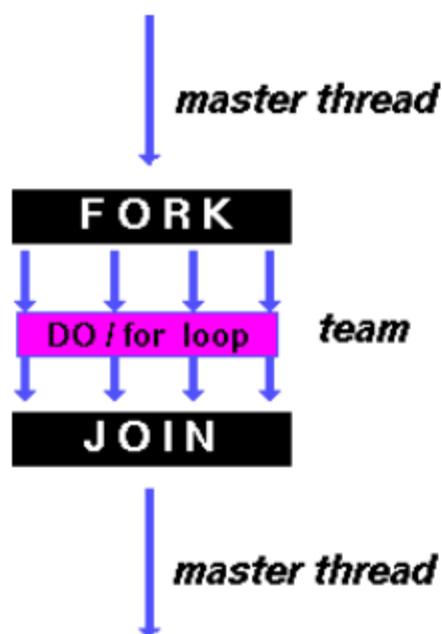
    // use formula for sum of arithmetic sequence: sum(1:n) = (n+1)*n/2
    int expected = (num_threads+1)*num_threads/2;
    std::cout << "sum " << sum
        << (sum==expected ? " which matches the expected value "
            : " which does not match the expected value ")
        << expected << std::endl;

    return 0;
}
```

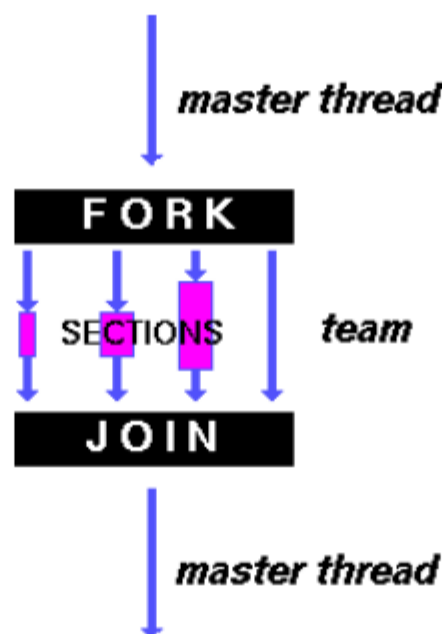
Worksharing constructs in OpenMP

See <https://computing.llnl.gov/tutorials/openMP/>

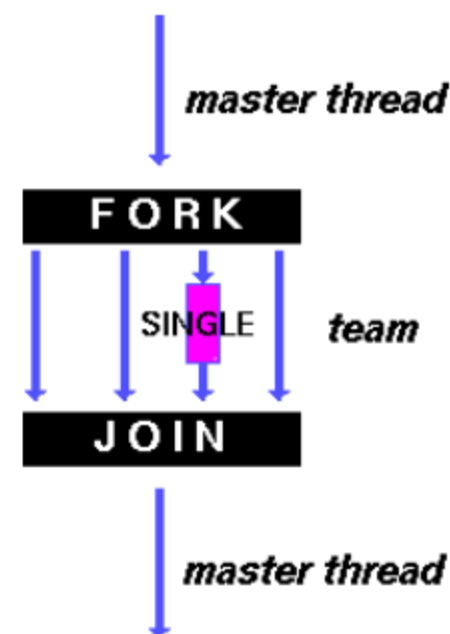
DO / for - shares iterations of a loop across the team. Represents a type of "data parallelism".



SECTIONS - breaks work into separate, discrete sections. Each section is executed by a thread. Can be used to implement a type of "functional parallelism".



SINGLE - serializes a section of code

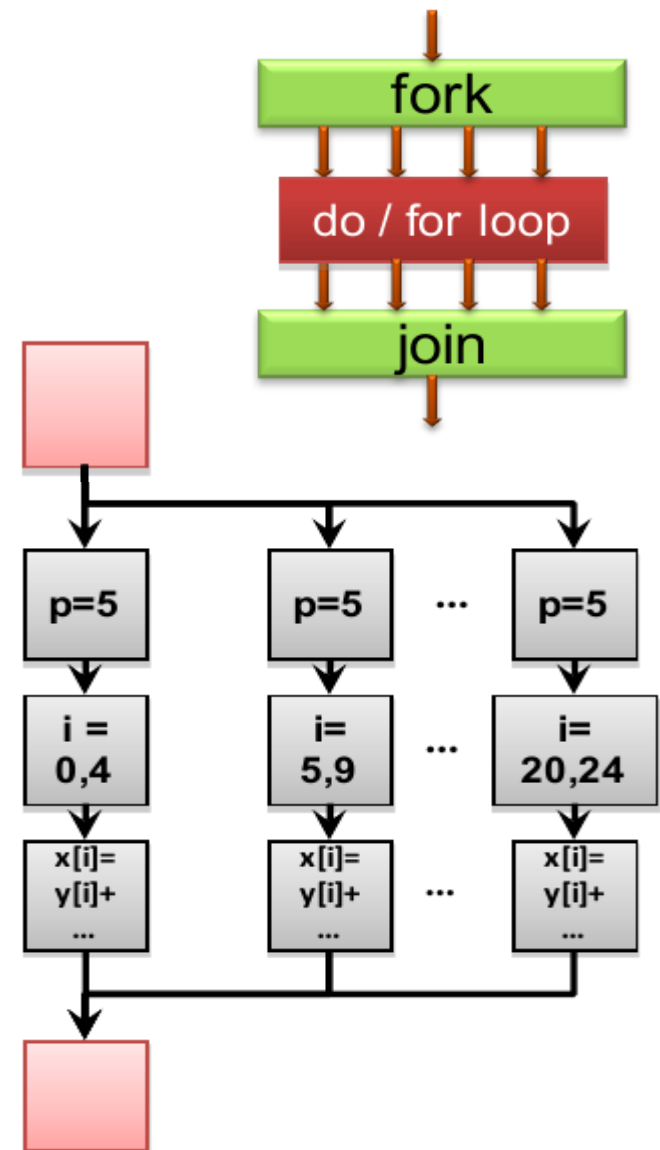


OpenMP worksharing “for/do loops”

See “A beginner's guide to supercomputing” – Sterling & Anderson

- *do/for* directive helps share iterations of a loop between a group of threads.
- If *nowait* is specified then the threads do not wait for synchronization at the end of a parallel loop.
- The *schedule* clause describes how iterations of a loop are divided among the threads in the team.

```
#pragma omp parallel private(p)
{
    p=5;
    #pragma omp for
        for (i=0; i<25; i++)
            x[i]=y[i]+p*(i+3);
    ...
} /* omp end parallel */
```



Format (Fortran/C/C++)

See <https://computing.llnl.gov/tutorials/openMP/>

Fortran	<pre>!\$OMP DO [<i>clause ...</i>] SCHEDULE (<i>type</i> [,<i>chunk</i>]) ORDERED PRIVATE (<i>list</i>) FIRSTPRIVATE (<i>list</i>) LASTPRIVATE (<i>list</i>) SHARED (<i>list</i>) REDUCTION (<i>operator</i> <i>intrinsic</i> : <i>list</i>) COLLAPSE (<i>n</i>) <i>do_loop</i> !\$OMP END DO [NOWAIT]</pre>
C/C++	<pre>#pragma omp for [<i>clause ...</i>] <i>newline</i> schedule (<i>type</i> [,<i>chunk</i>]) ordered private (<i>list</i>) firstprivate (<i>list</i>) lastprivate (<i>list</i>) shared (<i>list</i>) reduction (<i>operator</i>: <i>list</i>) collapse (<i>n</i>) nowait <i>for_loop</i></pre>

Example: “do loops”

Serial code

```
double *x, *y, *z;
int n;
for(int i=0; i<n; ++i) {
    z[i] = x[i] + y[i];
}
```

C++

```
real(kind=8) :: x(:), y(:), z(:)
integer      :: i, n
do i=1,n
    z(i) = x(i) + y(i)
end do
```

Fortran

Parallel code

- compiler handles loop bounds for you.
- there is a compact single-line directive.
- **!\$OMP DO** (in Fortran)

```
double *x, *y, *z;
int n, i;
#pragma omp parallel
{
    #pragma omp for
    for(i=0; i<n; ++i) {
        z[i] = x[i] + y[i];
    }
}
```

C++

loop index
variable i is
private by
default

```
real(kind=8) :: x(:), y(:), z(:)
integer      :: i, n
!$omp parallel
!$omp do
do i=1,n
    z(i) = x(i) + y(i)
end do
!$omp end do
!$omp end parallel
```

Fortran

- let's attempt to parallelize the integral

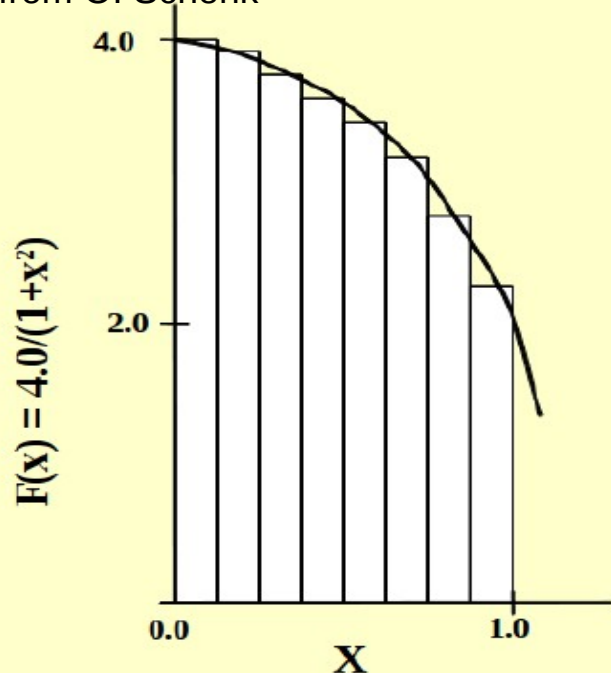
$$\pi = \int_0^1 dx \frac{4}{1+x^2}$$

by using techniques learnt so far (“**summation the hard way**”).

- [YaleParallel2018/day2/code/openmp/4.integration_pi.f90](#)
- [YaleParallel2018/day2/code/openmp/4a.integration_pi.cpp](#)

Computing Pi – the hard way

Fig. from O. Schenk



$$\int_0^1 \frac{4.0}{(1+x^2)} dx = \pi$$

$$\sum_{i=0}^N F(x_i) \Delta x \approx \pi$$

```
#define _USE_MATH_DEFINES

const int num_steps = 500000000;

int main( void ){
    int i;
    double sum = 0.0;
    double pi = 0.0;

    std::cout << "using " << omp_get_max_threads() << " OpenMP threads" << std::endl;

    const double w = 1.0/double(num_steps);

    double time = -omp_get_wtime();

    #pragma omp parallel firstprivate(sum)
    {
        #pragma omp for
        for (int i=0; i<num_steps; ++i)
        {
            double x = (i+0.5)*w;
            sum += 4.0/(1.0+x*x);
        }
        #pragma omp critical
        {
            pi = pi + w*sum;
        }
    }

    time += omp_get_wtime();

    std::cout << num_steps
              << " steps approximates pi as : "
              << pi
              << ", with relative error "
              << std::fabs(M_PI-pi)/M_PI
              << std::endl;
    std::cout << "the solution took " << time << " seconds" << std::endl;
}
```

Some clauses

SCHEDULE: Describes how iterations of the loop are divided among the threads in the team. The default schedule is implementation dependent

STATIC

Loop iterations are divided into pieces of size chunk and then statically assigned to threads. If chunk is not specified, the iterations are evenly (if possible) divided contiguously among the threads.

DYNAMIC

Loop iterations are divided into pieces of size chunk, and dynamically scheduled among the threads; when a thread finishes one chunk, it is dynamically assigned another. The default chunk size is 1.

AUTO

The scheduling decision is delegated to the compiler and/or runtime system.

NO WAIT / nowait: If specified, then threads do not synchronize at the end of the parallel loop.

Example – default work sharing

See [YaleParallel2018/day2/code/openmp/4d.work.cpp](#)

```
#include <omp.h>
#include <stdio.h>
#include <stdlib.h>
#include <iostream>
using namespace std;

int main()
{
    const int N = 100;
    int nthreads, threadid;
    int i;
    double a[N], b[N], c[N];

    //Initialize
    for (i = 0; i < N; i++){
        a[i] = 1.0*i;
        b[i] = 2.0*i;
    }

    #pragma omp parallel shared(a,b,c,nthreads) private(i,threadid)
    {
        threadid = omp_get_thread_num();
        if (threadid == 0) {
            nthreads = omp_get_num_threads();
            printf("Number of threads = %d\n", nthreads);
        }
        printf("My threadid = %d\n", threadid);

        #pragma omp for
        for (i = 0; i < N; i++){
            c[i] = a[i] + b[i];
        }

        } //join

    cout << "TEST c[99] = " << c[99] << endl;

    return 0;
}
```

Example of loop work sharing

Root/code_day2/openmp\$./4d.work-static.exec

```
My threadid = 2
My threadid = 1
My threadid = 3
Number of threads = 4
My threadid = 0
TEST c[99] = 297
```

Example – default work sharing

See YaleParallel2018/day2/code/openmp/4e.work-print.cpp

Print out the thread ID and the index.
Use default scheduling.

```
#include <omp.h>
#include <stdio.h>
#include <stdlib.h>
#include <iostream>
using namespace std;

int main()
{
    const int N = 20;
    int nthreads, threadid;
    int i;
    double a[N], b[N], c[N];

    //Initialize
    for (i = 0; i < N; i++){
        a[i] = 1.0*i;
        b[i] = 2.0*i;
    }

    #pragma omp parallel shared(a,b,c,nthreads) private(i,threadid)
    {
        threadid = omp_get_thread_num();
        if (threadid == 0) {
            nthreads = omp_get_num_threads();
            printf("Number of threads = %d\n", nthreads);
        }
        printf("My threadid = %d\n", threadid);

        #pragma omp for
        for (i = 0; i < N; i++){
            c[i] = a[i] + b[i];
            printf("Thread id = %d working on index %d\n", threadid, i);
        }

        } //join

    cout << "TEST c[19] = " << c[19] << endl;

    return 0;
}
```

./4e.work-static-print.exec

```
Number of threads = 4
My threadid = 0
Thread id = 0 working on index 0
Thread id = 0 working on index 1
Thread id = 0 working on index 2
Thread id = 0 working on index 3
Thread id = 0 working on index 4
My threadid = 2
Thread id = 2 working on index 10
Thread id = 2 working on index 11
Thread id = 2 working on index 12
Thread id = 2 working on index 13
Thread id = 2 working on index 14
My threadid = 3
Thread id = 3 working on index 15
Thread id = 3 working on index 16
Thread id = 3 working on index 17
Thread id = 3 working on index 18
Thread id = 3 working on index 19
My threadid = 1
Thread id = 1 working on index 5
Thread id = 1 working on index 6
Thread id = 1 working on index 7
Thread id = 1 working on index 8
Thread id = 1 working on index 9
TEST c[19] = 57
```

Example – static work sharing

See [YaleParallel2018/day2/code/openmp/4f.work-static.cpp](https://yaleparallel2018/day2/code/openmp/4f.work-static.cpp)

```
#include <omp.h>
#include <stdio.h>
#include <stdlib.h>
#include <iostream>
using namespace std;

int main()
{
    const int N = 20;
    int nthreads, threadid;
    int i;
    double a[N], b[N], c[N];

    //Initialize
    for (i = 0; i < N; i++){
        a[i] = 1.0*i;
        b[i] = 2.0*i;
    }

    int chunk = 3;

#pragma omp parallel shared(a,b,c,nthreads) private(i,threadid)
    {
        threadid = omp_get_thread_num();
        if (threadid == 0) {
            nthreads = omp_get_num_threads();
            printf("Number of threads = %d\n", nthreads);
        }
        printf("My threadid = %d\n", threadid);

#pragma omp for schedule(static,chunk)
        for (i = 0; i < N; i++){
            c[i] = a[i] + b[i];
            printf("Thread id = %d working on index %d\n", threadid,i);
        }

        //join

    }

    cout << "TEST c[19] = " << c[19] << endl;

    return 0;
}
```

Print out the thread id and the index.
Use static scheduling.

```
Number of threads = 4
My threadid = 0
Thread id = 0 working on index 0
Thread id = 0 working on index 1
Thread id = 0 working on index 2
Thread id = 0 working on index 12
Thread id = 0 working on index 13
Thread id = 0 working on index 14
My threadid = 2
Thread id = 2 working on index 6
Thread id = 2 working on index 7
Thread id = 2 working on index 8
Thread id = 2 working on index 18
Thread id = 2 working on index 19
My threadid = 1
Thread id = 1 working on index 3
Thread id = 1 working on index 4
Thread id = 1 working on index 5
Thread id = 1 working on index 15
Thread id = 1 working on index 16
Thread id = 1 working on index 17
My threadid = 3
Thread id = 3 working on index 9
Thread id = 3 working on index 10
Thread id = 3 working on index 11
TEST c[19] = 57
```

Example – dynamic work sharing

See [YaleParallel2018/day2/code/openmp/4g.work-dynamic.cpp](https://yaleparallel2018/day2/code/openmp/4g.work-dynamic.cpp)

Print out the thread id and the index.
Use dynamic scheduling.

```
#include <omp.h>
#include <stdio.h>
#include <stdlib.h>
#include <iostream>
using namespace std;

int main()
{
    const int N = 20;
    int nthreads, threadid;
    int i;
    double a[N], b[N], c[N];

    //Initialize
    for (i = 0; i < N; i++){
        a[i] = 1.0*i;
        b[i] = 2.0*i;
    }

    int chunk = 3;

#pragma omp parallel shared(a,b,c,nthreads) private(i,threadid)
    {
        threadid = omp_get_thread_num();
        // if (threadid == 0) {
        //     nthreads = omp_get_num_threads();
        //     printf("Number of threads = %d\n", nthreads);
        // }
        // printf("My threadid = %d\n", threadid);

#pragma omp for schedule(dynamic,chunk)
        for (i = 0; i < N; i++){
            c[i] = a[i] + b[i];
            printf("Thread id = %d working on index %d\n", threadid,i);
        }

        //join

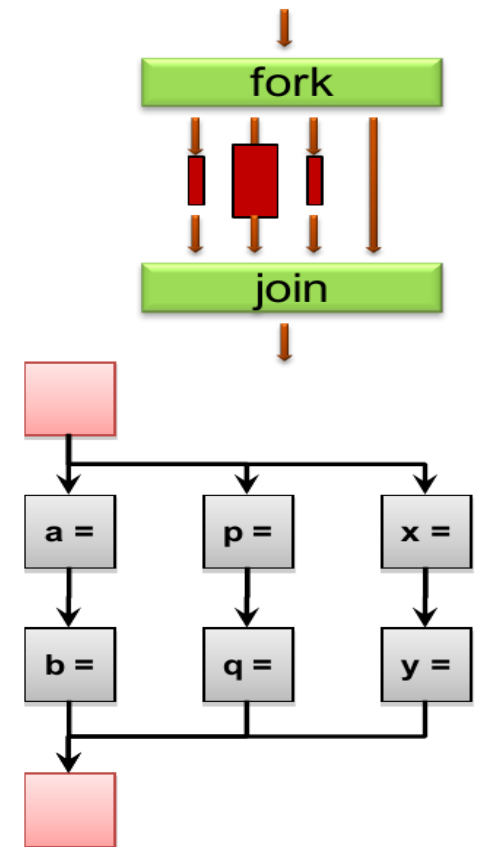
        cout << "TEST c[19] = " << c[19] << endl;
    }
}
```

```
Thread id = 0 working on index 0
Thread id = 0 working on index 1
Thread id = 0 working on index 2
Thread id = 1 working on index 9
Thread id = 1 working on index 10
Thread id = 1 working on index 11
Thread id = 1 working on index 15
Thread id = 1 working on index 16
Thread id = 1 working on index 17
Thread id = 1 working on index 18
Thread id = 1 working on index 19
Thread id = 3 working on index 3
Thread id = 3 working on index 4
Thread id = 3 working on index 5
Thread id = 0 working on index 12
Thread id = 0 working on index 13
Thread id = 0 working on index 14
Thread id = 2 working on index 6
Thread id = 2 working on index 7
Thread id = 2 working on index 8
TEST c[19] = 57
```

OpenMP worksharing “sections”

- **sections** directive is a non iterative work sharing construct.
- Independent section of code are nested within a **sections** directive.
- It specifies enclosed **section** of codes between different threads.
- Code enclosed within a **section** directive is executed by a thread within the pool of threads.

```
#pragma omp parallel private(p)
{
  #pragma omp sections
  {{ a=...;
      b=...; }
    #pragma omp section
    { p=...;
      q=...; }
    #pragma omp section
    { x=...;
      y=...; }
  } /* omp end sections */
} /* omp end parallel */
```



Fortran/C/C++ “sections”

Fortran	<pre> !\$OMP SECTIONS [clause ...] PRIVATE (list) FIRSTPRIVATE (list) LASTPRIVATE (list) REDUCTION (operator intrinsic : list) !\$OMP SECTION block !\$OMP SECTION block !\$OMP END SECTIONS [NOWAIT] </pre>
C/C++	<pre> #pragma omp sections [clause ...] newline private (list) firstprivate (list) lastprivate (list) reduction (operator: list) nowait { #pragma omp section newline structured_block #pragma omp section newline structured_block } </pre>

Example on “sections”

See [YaleParallel2018/day2/code/openmp/5a.vec_add_sections.cpp](#)

```
#include <iostream>
#include <omp.h>
#define N 1

using namespace std;

int main ()
{
    int i;
    float a[N], b[N], c[N], d[N];

    /* Some initializations */
    for (i=0; i < N; i++) {
        a[i] = i * 1.5;
        b[i] = i + 22.35;
    }

    #pragma omp parallel shared(a,b,c,d) private(i)
    {

        #pragma omp sections nowait
        {

            #pragma omp section
            for (i=0; i < N; i++)
            {
                c[i] = a[i] + b[i];
                cout << "Section 1: hello from thread " << omp_get_thread_num() << " of " << omp_get_num_threads() << " index " << i << endl;
            }

            #pragma omp section
            for (i=0; i < N; i++)
            {
                d[i] = a[i] * b[i];
                cout << "Section 2: hello from thread " << omp_get_thread_num() << " of " << omp_get_num_threads() << " index " << i << endl;
            }

            #pragma omp section
            {
                cout << "Section 3: hello from thread " << omp_get_thread_num() << " of " << omp_get_num_threads() << endl;
                cout << "This section 3 does nothing " << endl;
            }

        } /* end of sections */

    } /* end of parallel region */
}
```

3 sections

>export OMP_NUM_THREADS = 2
./5.vec_add_sections.f90

>export OMP_NUM_THREADS = 3
>export OMP_NUM_THREADS = 4

→ what do we observe?

Reductions

→ e.g. summation the “easy way”

The **REDUCTION** clause performs a reduction on the variables that appear in the list.

→ **reduction(op:list)**

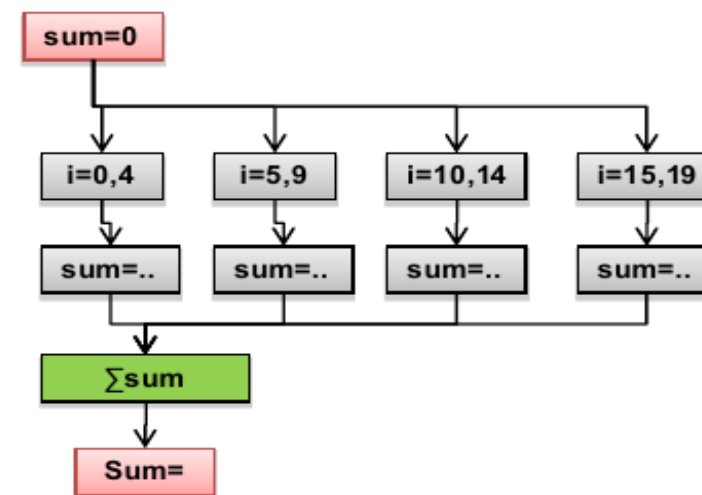
e.g. **reduction(+:pi),**
reduction(max:Maxval)

A **private copy** for each list variable is created for each thread.

At the end of the reduction, the reduction variable is applied to all private copies of the shared variable, and the final result is written to the global shared variable.

```
#pragma omp parallel for \
    default(shared) private(i) \
    schedule(static,chunk) \
    reduction(+:result)
```

```
for (i=0; i < n; i++)
    result += (a[i] * b[i]);
```



Example: Reduction

> [YaleParallel2018/day2/code/openmp/6a.integration_pi_reduction.cpp](#)
(play with OMP_NUM_THREADS & timing)

```
#include <iostream>
#include <cmath>

#include <omp.h>

#define _USE_MATH_DEFINES

const int num_steps = 500000000;

int main( void ){
    int i;
    double sum = 0.0;
    double pi = 0.0;

    std::cout << "using " << omp_get_max_threads() << " OpenMP threads" << std::endl;

    const double w = 1.0/double(num_steps);

    double time = -omp_get_wtime();

    #pragma omp parallel for reduction(+:sum)
    for(int i=0; i<num_steps; ++i) {
        double x = (i+0.5)*w;
        sum += 4.0/(1.0+x*x);
    }

    pi = sum*w;

    time += omp_get_wtime();

    std::cout << num_steps
              << " steps approximates pi as : "
              << pi
              << ", with relative error "
              << std::fabs(M_PI-pi)/M_PI
              << std::endl;
    std::cout << "the solution took " << time << " seconds" << std::endl;
}
```

Reduction



4. Parallel Dynamic Programming

$$V_{new}(k, \Theta) = \max_c (u(c) + \beta \mathbb{E}\{V_{old}(k_{next}, \Theta_{next})\})$$

$$\text{s.t. } k_{next} = f(k, \Theta_{next}) - c$$

$$\Theta_{next} = g(\Theta)$$

States of the model:

- k : today's capital stock \rightarrow There are many independent k 's
- Θ : today's productivity state \rightarrow The Θ 's are independent

Choices of the model:

- k_{next}

$\rightarrow k, k_{next}, \Theta$ and Θ_{next} are limited to a finite number of values

solver.cpp

See [YaleParallel2018/day1/code/DynamicProgramming/serial_DP](#)

```
for (int itheta=0; itheta<ntheta; itheta++) {  
    /*  
     Given the theta state, we now determine the new values and optimal policies corresponding to each  
     capital state.  
     */  
    for (int ik=0; ik<nk; ik++) {  
        // Compute the consumption quantities implied by each policy choice  
        c=f(kgrid(ik), thetagrid(itheta))-kgrid;  
  
        // Compute the list of values implied implied by each policy choice  
        temp=util(c) + beta*ValOld*p(thetagrid(itheta));  
  
        /* Take the max of temp and store its location.  
         The max is the new value corresponding to (ik, itheta).  
         The location corresponds to the index of the optimal policy choice in kgrid.  
         */  
        ValNew(ik, itheta)=temp.maxCoeff(&maxIndex);  
  
        Policy(ik, itheta)=kgrid(maxIndex);  
    }  
}
```

loops to worry about
with OpenMP

One loop parallelized

See [YaleParallel2018/day2/code/DynamicProgramming/openmp_DP/solver.cpp](https://yaleparallel2018/day2/code/DynamicProgramming/openmp_DP/solver.cpp)

```

for (int itheta=0; itheta<ntheta; itheta++) {
// OpenMP is initialized and the threads are created.
#pragma omp parallel private(maxIndex, c, temp)
{
    /* We distribute the following for loop among the threads.
    * That is, we distribute the capital states
    * among the threads */
    #pragma omp for
    for (int ik=0; ik<nk; ik++) {

        /*
        Given the theta state, we now determine the new value and optimal policies corresponding to each
        capital state.
        */

        // Compute the consumption quantities implied by each policy choice
        c=f(kgrid(ik), thetagrid(itheta))-kgrid;

        // Compute the list of values implied implied by each policy choice
        temp=util(c) + beta*ValOld*p(thetagrid(itheta));

        /* Take the max of temp and store its location.
        The max is the new value corresponding to (ik, itheta).
        The location corresponds to the index of the optimal policy choice in kgrid.

        We store the new value and the corresponding policy information in the ValNew and Policy matrices.
        */

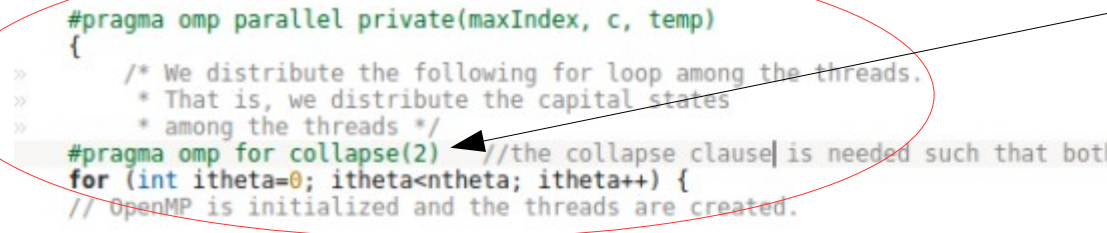
        ValNew(ik, itheta)=temp.maxCoeff(&maxIndex);
        Policy(ik, itheta)=kgrid(maxIndex);
    }
}
}

```

Nestded loops parallelized

See [YaleParallel2018/day2/code/DynamicProgramming/openmp_DP/solver.nested.cpp](https://yaleparallel2018/day2/code/DynamicProgramming/openmp_DP/solver.nested.cpp)

The ***collapse*** clause will allow for parallelizing the indices of the nested loops at once.



```

#pragma omp parallel private(maxIndex, c, temp)
{
    >> /* We distribute the following for loop among the threads.
    >>  * That is, we distribute the capital states
    >>  * among the threads */
    #pragma omp for collapse(2) //the collapse clause is needed such that both loops are parallelized at the same time
    for (int itheta=0; itheta<ntheta; itheta++) {
        // OpenMP is initialized and the threads are created.

        for (int ik=0; ik<nk; ik++) {

            /*
            Given the theta state, we now determine the new value and optimal policies corresponding to each
            capital state.
            */

            // Compute the consumption quantities implied by each policy choice
            c=f(kgrid(ik), thetagrid(itheta))-kgrid;

            // Compute the list of values implied implied by each policy choice
            temp=util(c) + beta*ValOld*p(thetagrid(itheta));

            /* Take the max of temp and store its location.
            The max is the new value corresponding to (ik, itheta).
            The location corresponds to the index of the optimal policy choice in kgrid.

            We store the new value and the corresponding policy information in the ValNew and Policy matrices.

            */

            ValNew(ik, itheta)=temp.maxCoeff(&maxIndex);
            Policy(ik, itheta)=kgrid(maxIndex);
        }
    }
}

```

Questions?

1. Advice – <http://imgtfy.com/>
<http://imgtfy.com/?q=open+mp>

