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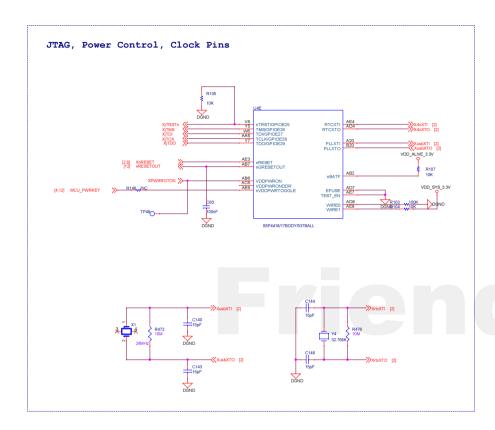
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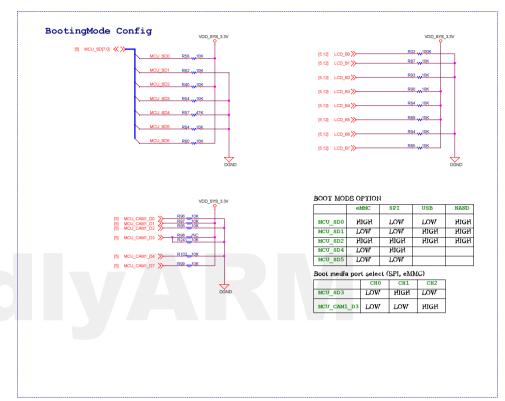
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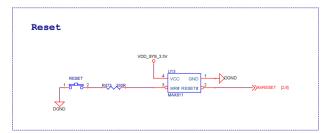
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S5P4418-Sys/Boot Option

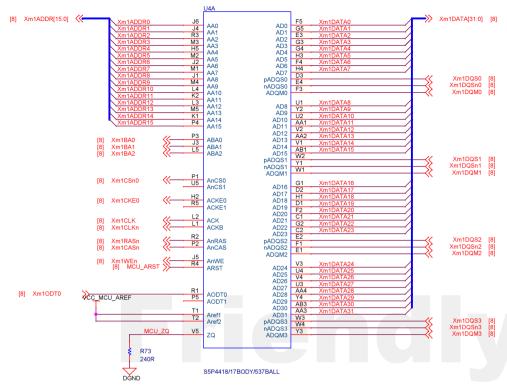






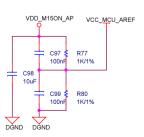
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S5P4418 - DDR3





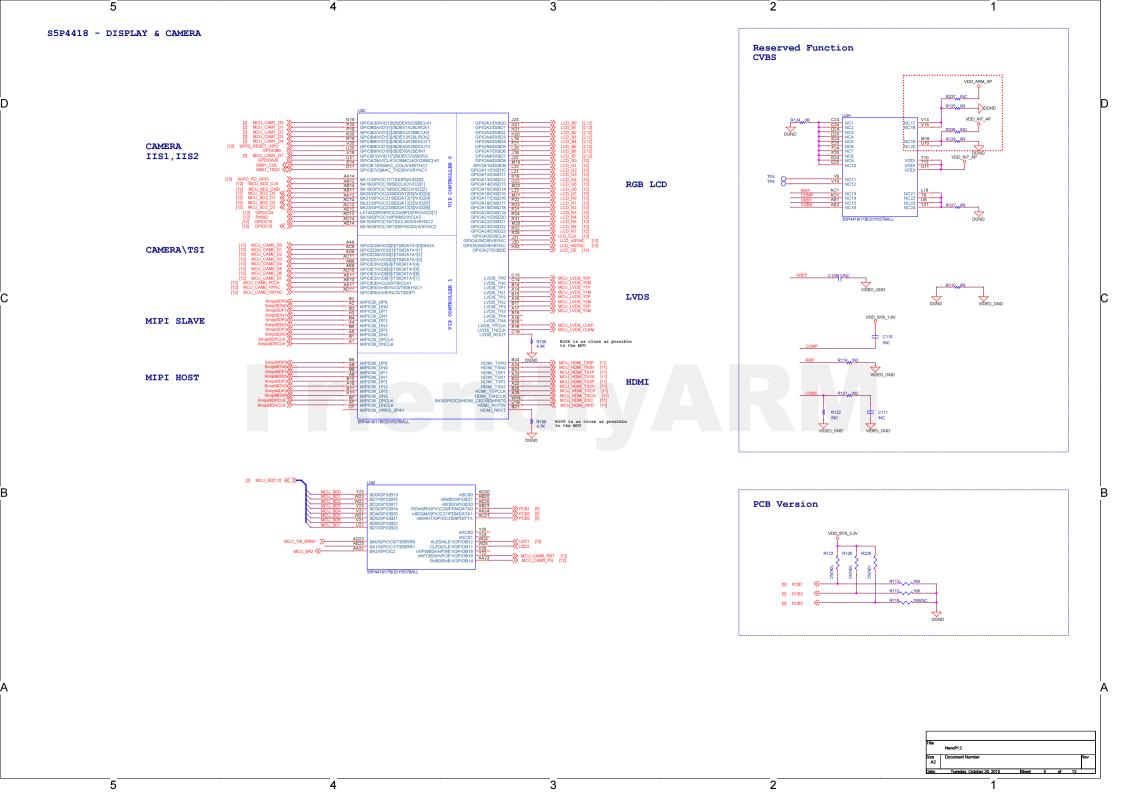




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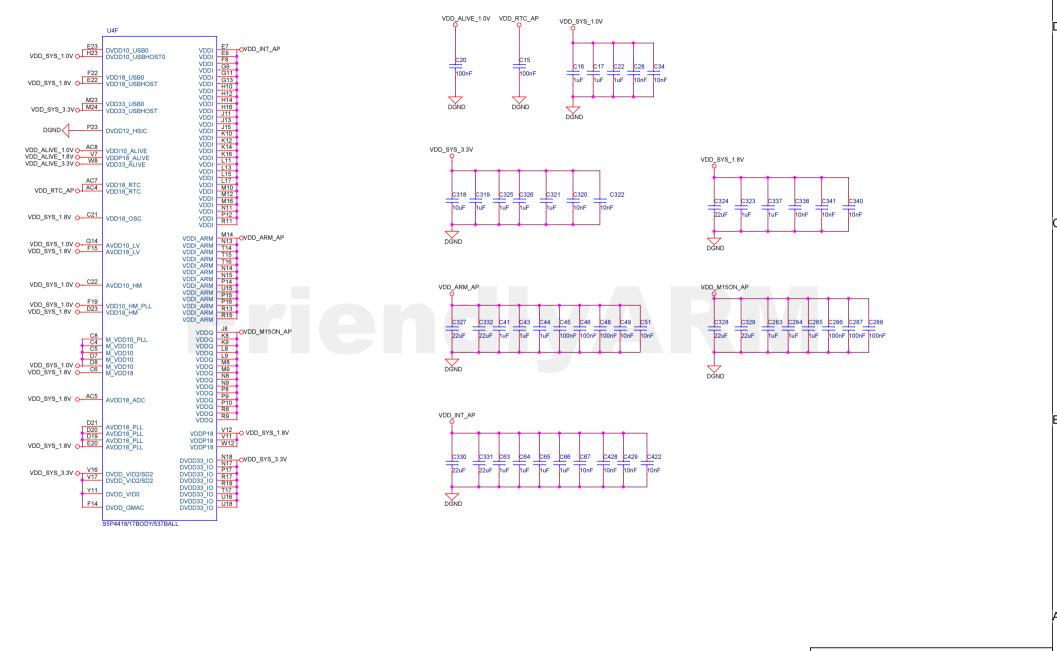
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Date: Tuesday, October 20, 2015 | Sheet 4 of 12



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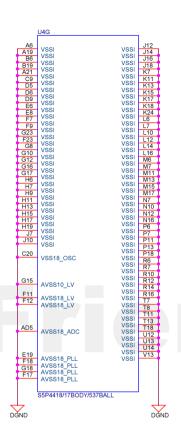
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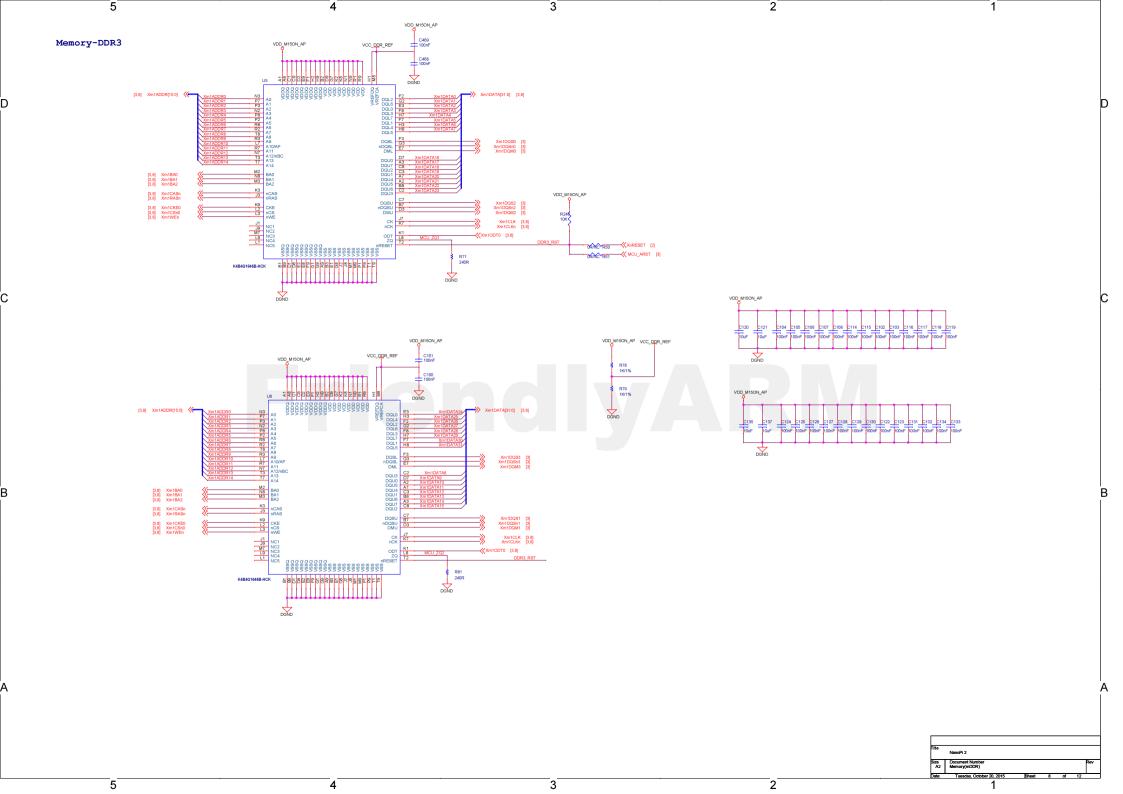
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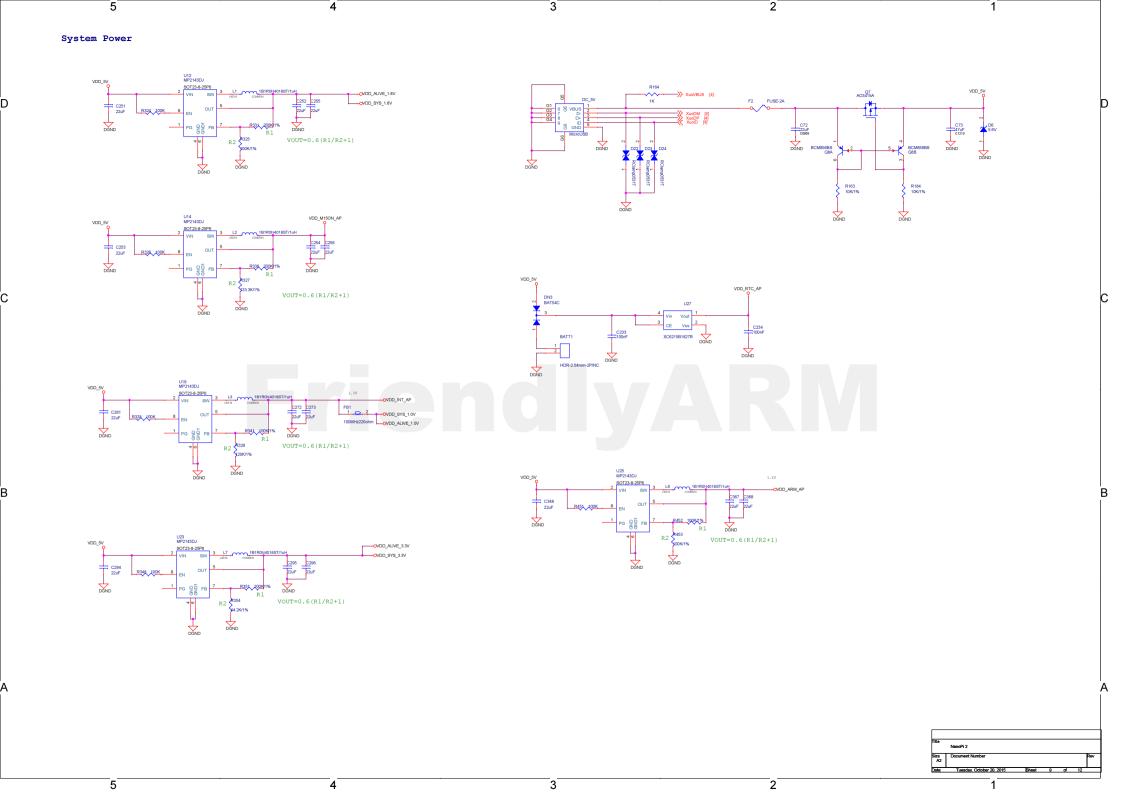
NanoPi 2

S5P4418-VSS





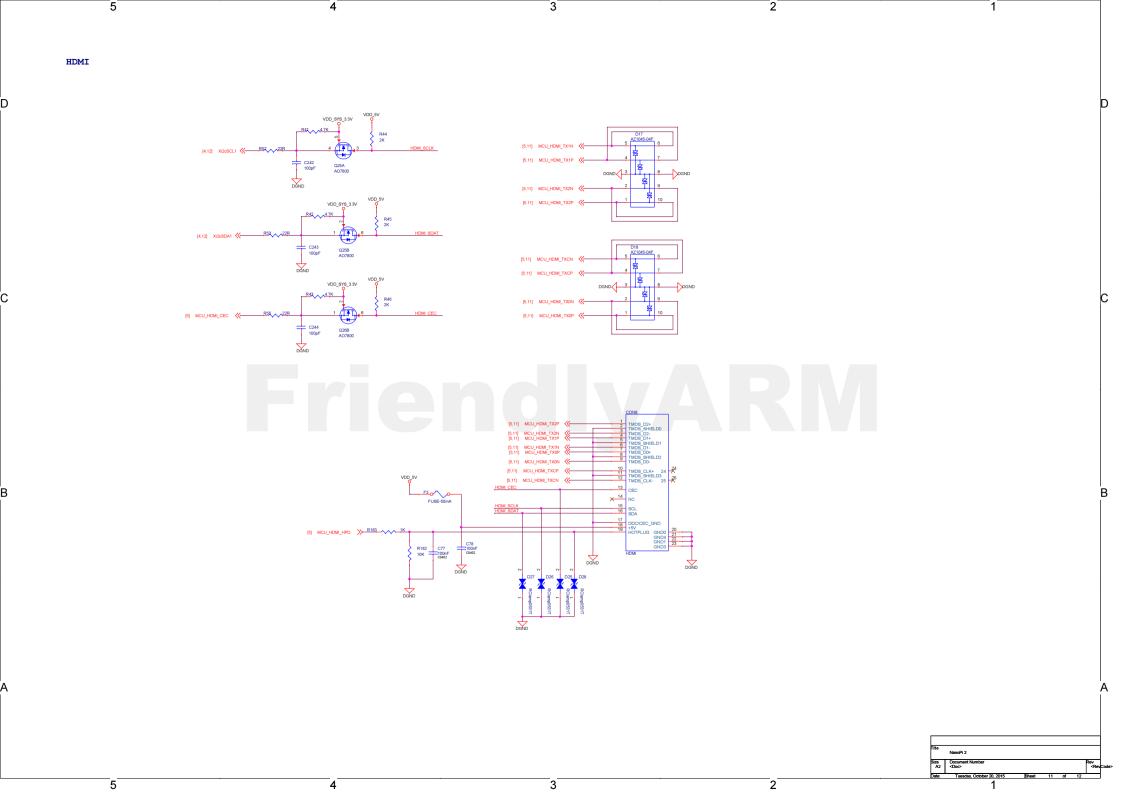




WiFi+BT VDD_SYS_3.3V DGND C500 /NC ANT3 2 AN2051 [4] GPIOB24 SF [4] MCU_SD1_CLK R442 OR R444 OR C508 C511 NC NC VDD_SYS_3.3V 38 BT_XTAL_OUT BT_XTAL_IN [5,10] WIFIO_RESET_GPIO >> VDD_SYS_3.3V Size Document Nun. A2 <Doc> 5 3 2

2

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Board Connectors

