UNISONIC TECHNOLOGIES CO., LTD

LV321

LINEAR INTEGRATED CIRCUIT

SINGLE GENERAL PURPOSE. LOW VOLTAGE, RAIL-TO-RAIL **OUTPUT OPERATIONAL AMPLIFIER**

DESCRIPTION

The UTC LV321 is a single op amp with low supply current and low voltage (2.7-5.5V) well economic consideration. It brings nice performance and to low voltage, low power systems. With a 1MHz unity-gain frequency, The UTC LV321 has a guaranteed 1 V/µs slew rate and low supply current. It provides heavy rail-to-rail (R-to-R) output swing loads and the input common-mode voltage range including ground. Besides, it is also capable for comfortably driving large capacitive loads.

The UTC LV321 has bipolar input and CMOS output for improved noise performance and higher output current drive. It's the most cost effective solution for the applications where low voltage operation, space saving and low price are required.

FEATURES

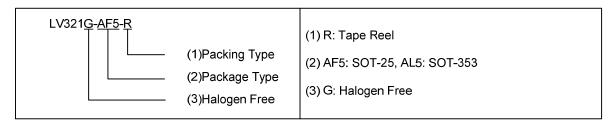
- * 2.7V and 5V Performance Guaranteed
- * No Crossover Distortion
- * 130µA Low Supply Current
- * Rail-to-Rail Output Swing @10kΩ Load: V⁺-10mV

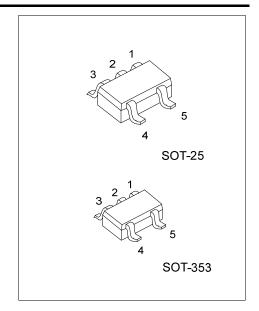
V +65mV

- * V_{CM} From -0.2V to V⁺ -0.8V
- Halogen Free

ORDERING INFORMATION

Ordering Number	Package	Packing
LV321G-AF5-R	SOT-25	Tape Reel
LV321G-AL5-R	SOT-353	Tape Reel

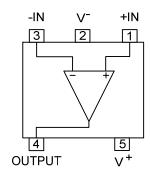




■ MARKING

PACKAGE	MARKING			
SOT-25	3 2 1 V3UG			
	4 5			
	5 4			
SOT-353	V3UG			
	1 2 3			

■ PIN CONFIGURATION



■ ABSOLUTE MAXIMUM RATINGS

PARAMETER		SYMBOL	RATINGS	UNIT
Supply Voltage		V _{SS}	2.7 ~ 5.5	V
Supply Voltage (V ⁺ - V ⁻)		V _{SS}	5.5	V
Differential Input Voltage		$V_{I(DIFF)}$	±Supply Voltage	
Output Short Circuit	V ⁺		(Note 2)	
	V	I _{O(SC)}	(Note 3)	
Infrared (15 sec)			215	°C
Junction Temperature		T_J	+150	°C
Operation Temperature	•	T _{OPR}	-40~+85	°C
Storage Temperature	•	T _{STG}	-65~+150	°C

- Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

 Absolute maximum ratings are stress ratings only and functional device operation is not implied.
 - 2. Shorting output to V+ will adversely affect reliability
 - 3. Shorting output to V- will adversely affect reliability

■ THERMAL DATA

PARAMETER		SYMBOL	RATING	UNIT
Junction to Ambient	SOT-25	θ_{JA}	265	°C/W
	SOT-353		478	°C/W

■ 2.7V ELECTRICAL CHARACTERISTICS

All limits guaranteed for $T_J = 25^{\circ}C$, $V^+ = 2.7V$, $V^- = 0V$, $V_{CM} = 1.0V$, $V_{OUT} = V^+/2$ and $R_L > 1M\Omega$, unless otherwise specified.

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNIT
DC CHARACTERISTICS						
Input Offset Voltage	Vos			1.7	7	mV
Input Common Mode Voltage Range	V _{CM}	For CMDD > FOAD	0	-0.2		V
		For CMRR≥50dB		1.9	1.7	V
Output Cuina	\ /	R _L =10kΩ to 1.35V	V ⁺ -100	V ⁺ -10		mV
Output Swing	V _{OUT}			60	180	mV
Input Offset Voltage Average Drift	TCVos			5		μV/°C
Input Bias Current	I _{I(BIAS)}			11	250	nA
Input Offset Current	I _{I(OFF)}			5	50	nA
Common Mode Rejection Ratio	CMRR	0V≤V _{CM} ≤1.7V	50	63		dB
Power Supply Rejection Ratio	PSRR	$2.7V \le V^{+} \le 5V, V_{OUT} = 1V$	50	60		dB
Supply Current	I _{SS}			80	170	μΑ
AC CHARACTERISTICS						
Gain Bandwidth Product	GBWP	C _L =200pF		1		MHz
Phase Margin	Φ _m			60		Deg
Gain Margin	G _m			10		dB
Input Referred Voltage Noise	eN	F=1kHz		46		<u>nV</u> √Hz
Input Referred Current Noise	i _n	F=1kHz		0.17		<u>pA</u> √ Hz

■ 5V ELECTRICAL CHARACTERISTICS

All limits guaranteed for T_J =25°C, V^+ =5V, V^- =0V, V_{CM} =2.0V, V_{OUT} = V^+ /2 and R_L >1M Ω , unless otherwise specified.

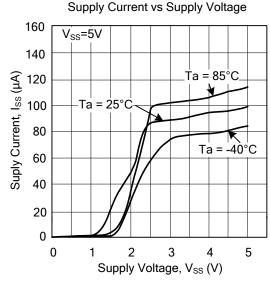
PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNIT
DC CHARACTERISTICS							
Input Offset Voltage	Vos				1.7	7	mV
Innut Common Mode Voltage Dange	V	E OMDD> 50-ID		0	-0.2		V
Input Common-Mode Voltage Range	Mode Voltage Range V _{CM} For CMRR≥50dB				4.2	4	V
Output Swing	V _{OUT}	R_L =2k Ω to2.5V	V_{OH}	V ⁺ -300	V ⁺ -40		mV
			V_{OL}		120	300	mV
Output Owing	V 001	R_L =10k Ω to 2.5V	V _{OH}	V ⁺ -100	V ⁺ -10		mV
		11_10822 to 2.50	V_{OL}		65	180	mV
Input Offset Voltage Average Drift	TCVos				5		μV/°C
Input Bias Current	I _{I(BIAS)}				15	250	nA
Input Offset Current	$I_{I(OFF)}$				5	50	nA
Common Mode Rejection Ratio	CMRR	0V≤V _{CM} ≤4V		50	65		dB
Power Supply Rejection Ratio	PSRR	2.7V≤V ⁺ ≤5V V _{OUT} =1V, V _{CM} =1V		50	60		dB
Large Signal Voltage Gain(Note 1)	G _V	$R_L=2k\Omega$		15	100		V/mV
Output Short Circuit Current	I _{OUT}	Sourcing, V _{OUT} =0V		5	60		mA
Output Short Circuit Current		Sinking, V _{OUT} =5V		10	160		mA
Supply Current	I _{SS}				130	250	μA
AC CHARACTERISTICS							
Slew Rate	SR	(Note 2)			1		V/µs
Gain Bandwidth Product	GBWP	C _L =200pF			1		MHz
Phase Margin	Фт				60		Deg
Gain Margin	Gm				10		dB
Input Referred Voltage Noise	eN	f=1kHz			39		<u>nV</u> √ Hz
Input Referred Current Noise	i _n	f=1kHz			0.21		<u>pA</u> √ Hz

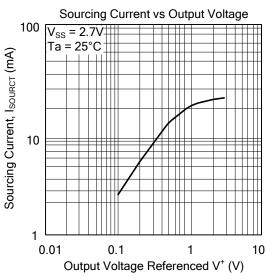
Notes: 1. R_L is connected to V. The output voltage is $0.5V \le V_{OUT} \le 4.5V$.

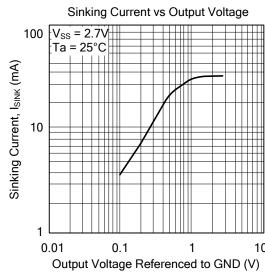
^{2.} Connected as voltage follower with 3V step input. Number specified is these lower of the positive and negative slew rates

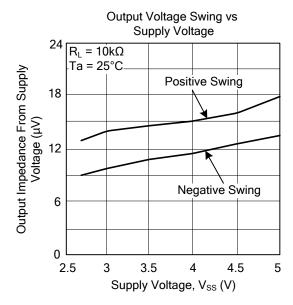
^{3.} all numbers are typical, and apply for packages soldered directly note a PC board is still air.

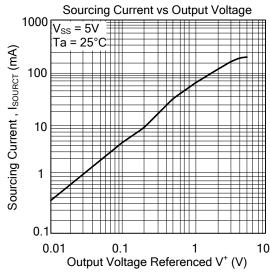
■ TYPICAL CHARACTERISTICS

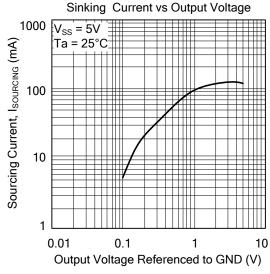


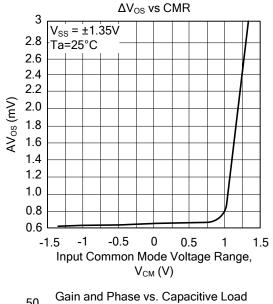


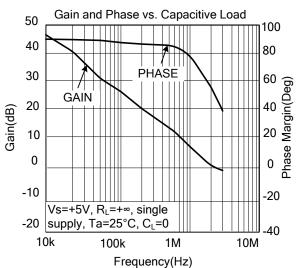


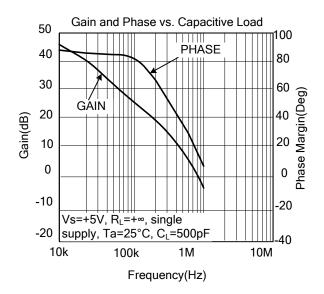


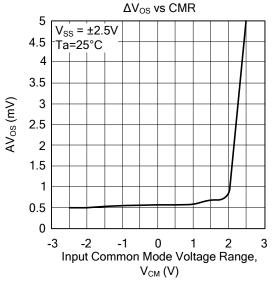


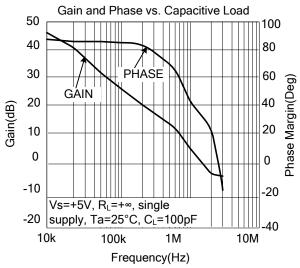


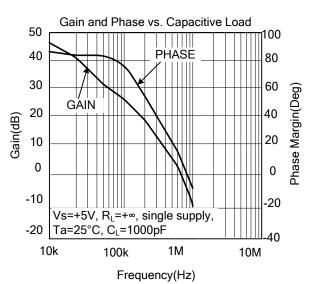


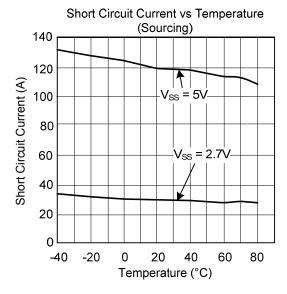


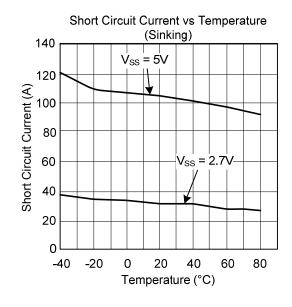


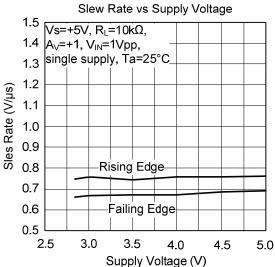


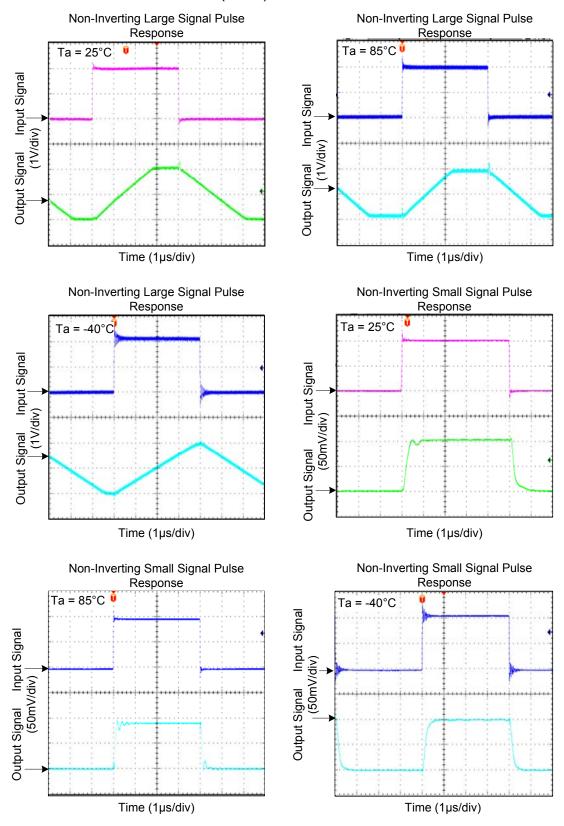


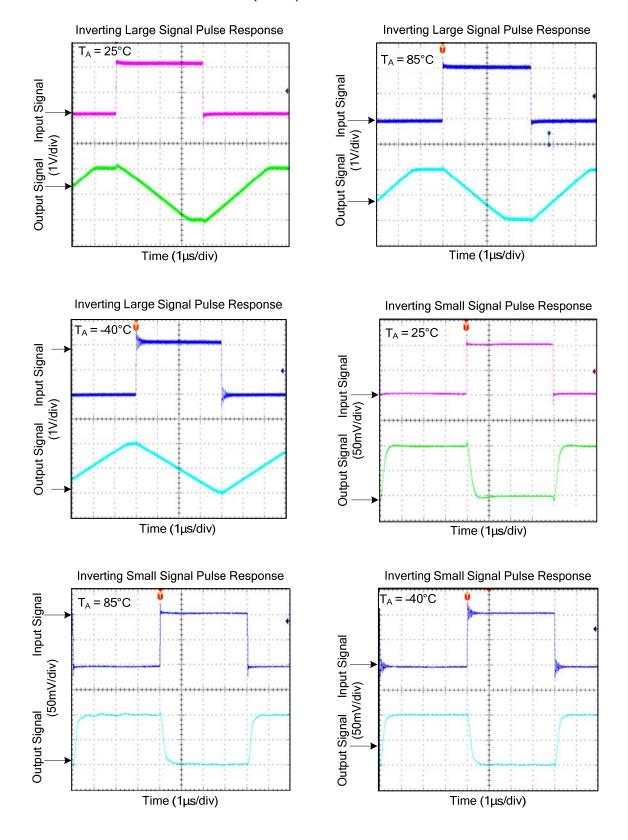












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