EE 577B Fall 2007: Extra Credit Homework #1

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Write some stuff here - the abstract, that is

Categories and Subject Descriptors: VLSI [**Digital IC Design**]: Communication Electronics

General Terms: Error Correction and Detection, Logic Synthesis, Functional Verification

Additional Key Words and Phrases: Error-Correcting Code, Parity-Check Matrix, Generator Matrix, Decoder,Encoder

1. INTRODUCTION
2. GENERATOR MATRIX G AND PARITY CHECK MATRIX H

$$G = \begin{bmatrix} 1 & 1 & 1 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 1 & 1 & 1 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \end{bmatrix}$$

3. ...

...

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Do something
This is great [1; 2; 3; 4].

do more stuff

Do something ELSE

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