

# EE 577B Fall 2007: Extra Credit Homework #1

Zhiyang Ong

Ming Hsieh Department of Electrical Engineering,

Viterbi School of Engineering,

University of Southern California

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Write some stuff here - the abstract,  
that is

Categories and Subject Descriptors: VLSI [**Digital IC Design**]: Communication Electronics

General Terms: Error Correction and Detection, Logic Synthesis, Functional Verification

Additional Key Words and Phrases: Error-Correcting Code, Parity-Check Matrix, Generator Matrix, Decoder, Encoder

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## 1. INTRODUCTION

## 2. GENERATOR MATRIX G AND PARITY CHECK MATRIX H

$$G = \begin{bmatrix} 1 & 1 & 1 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 1 & 1 & 1 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \end{bmatrix}$$

## 3. ...

...

Do something

This is great [?; ?; ?; ?].

do more stuff

Do something ELSE

#### ACKNOWLEDGMENTS

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