Time (in clock cycles)									<b></b>
	CC 1	CC 2	CC 3	CC 4	CC 5	CC 6	CC 7	CC 8	CC 9
Value of register \$2:	10	10	10	10	10/–20	-20	-20	-20	-20
Value of EX/MEM:	X	Χ	Χ	-20	X	Χ	Χ	Χ	Χ
Value of MEM/WB:	Χ	Χ	Χ	Χ	-20	X	Χ	Χ	Χ
Program execution order (in instructions)		l [			<b>_</b>				
sub <b>\$2</b> , <b>\$1</b> , <b>\$3</b>	IM —	Reg			Reg	_			
and \$12, \$2, \$5		IM-	Reg_			-Reg			
or \$13, \$6, <b>\$2</b>			IM-	Reg		-DM-	Reg		
add \$14,\$2,\$2				IM-	Reg		DM-	Reg	
sw \$15, 100(\$2)					IM-	Reg_		DM-	Reg