

## Taint Analysis of Microarchitecture Synthesis Software

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Microarchitecture synthesis software is used to generate cycle-accurate/RTL designs of microarchitectures.

Taint analysis has been used to determine security loopholes of software.

Project Goal: Apply taint analysis to determine the security loopholes of the microarchitecture synthesis software.

Questions Can I force a finite state machine (FSM) to enter a non-existent/invalid state? Can I force the processor to write back to memory out of order? Can I degrade the performance of many-core processors?

Insert text here.