

A Comparison of API-based JIT Compiler Overhead during Run-time

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ABSTRACT

Just-in-Time compilation has allowed for significant performance gains by applications. Two popular open-source JIT frameworks are MCJIT and JitBuilder. Both of these compilers can be embedded within applications, offering interfaces to define and generate native code at run-time. LLVM is a collection of modular compiler and toolchain components, while MCJIT is a framework based on these components to provide JIT compilation. Similarly, JitBuilder is framework based on the Eclipse OMR compiler and runtime components. In this report we discuss the different approaches these two frameworks employ. In addition, we attempt to measure the overhead of each framework while compiling and then executing a small handful of test. We found that while LLVM required a larger memory footprint, in certain cases it was able to generate code more quickly. Furthermore, there were cases where the code LLVM generated offered higher throughput. JitBuilder did provide smaller disk and memory footprints and generated faster code than MCJIT in some cases.

CCS CONCEPTS

• **Software and its engineering** → **Compilers.**

KEYWORDS

compilers, just-in-time, optimization

1 INTRODUCTION

Just-in-time compilation, or JIT compilation, is a technique to improve the run-time binary translation of an application [1]. Using information collected from the running application, JIT compilers can further optimize generated code. For instance, by profiling which code paths are executed most frequently, JIT compilers can generate code that is optimized for hot-paths [2]. A JIT compiler might also inline entire blocks of code, generate an inline cache to speed up the dispatch of polymorphic method calls [3], or generate hardware specific instructions.

Runtimes for the high-level language Java make heavy use of JIT compilers, allowing their workloads to execute much faster than if they were entirely interpreted [4]. Given that JIT compilation can add significant overhead to a workload, compilation is typically applied selectively to the code executed most frequently. Furthermore, given that compilation can be viewed as a continuum, where slow, unoptimized code is cheap to generate, and where fast, optimized

code is expensive to generate, a compiler will often support multiple optimization levels [2]. A runtime with such an optimizing JIT compiler can then employ a staged compilation strategy which would allow the runtime to apply compilation and optimization according to heuristics [5].¹

Considering JIT compilers may also want to perform some of the optimizations found in a static compiler such as common sub-expression elimination, loop unrolling and constant propagation [9], an application developer, or language designer interested in enhancing a runtime's performance by adding a JIT compiler, will have a large engineering task ahead of them. In addition, considering that JIT compilers often need to generate native, or architecture specific code, for multiple platforms, the effort required can increase significantly. It is no surprise then that libraries or frameworks encapsulating proven, high-performance JIT compilers are available to software engineers today. For an engineer interested in incorporating an existing JIT framework, it is useful to consider the following questions when making their selection:

- How much processor overhead does the JIT compiler have?
- What is the quality of the generated JIT code and what effect does this have on throughput?
- How is the resident state set (RSS) for the application effected?
- How much larger is the application binary with the inclusion of the JIT framework?
- How configurable is the JIT framework?
- How easy is it for the programmer to incorporate and use the JIT framework?

In this report, we will consider those questions while looking at two such JIT frameworks: LLVM MCJIT [10] and OMR JitBuilder [11]. In Sections 2, and 3 we will discuss the background of each compiler and consider the techniques they employ. In Section 3, we outline the methods we used to answer those questions. In Section 4, we will consider the results. In Section 5 we will discuss related work. In Section 6, we will look at potential future work, and finally in Section 7, we will summarize the report.

2 BACKGROUND

In this section we will discuss the background of the two JIT compiler frameworks we are interested in: LLVM MCJIT and Eclipse OMR JitBuilder. In particular, we will focus on the motivation for

¹ An example of a staged, or tiered compilation strategy, can be seen with the Testarossa JIT compiler (TRJIT) in the Eclipse OpenJ9 JVM [6, 7]. Here an invocation threshold must be met before the JIT compiler will compile a method. Additionally, separate thresholds can be associated with each optimization level [8]. We will discuss TRJIT in more detail when we look at JitBuilder in Section 2.2.

```

1 define i32 @mul_add(i32 %x, i32 %y, i32 %z) {
2   entry:
3     %tmp = mul i32 %x, %y
4     %tmp2 = add i32 %tmp, %z
5     ret i32 %tmp2
6 }

```

Listing 1: LLVM IR for a function multiplying $x * y$ and adding z [13].

each framework, as well as discuss the techniques and features that they provide.

2.1 LLVM

LLVM, which at one time stood for Low Level Virtual Machine, is a popular set of open-source, modular compiler and toolchain components [12]. The compiler framework was originally designed to provide analysis and transformation for an application throughout its entire lifetime: from initial compilation and linking, through to runtime and even while the application was offline (see Figure 1). To achieve this ambitious goal, the framework utilizes a well defined, human-readable, intermediate representation (IR) called LLVM IR. The IR, which is initially generated by the front end, can be packaged with the target architecture binary that includes profiling instructions for later runtime compilation (JIT) as well as more aggressive offline optimizations. Several important characteristics of LLVM IR are as follows:

- The IR maintains Static Single Assignment (SSA) form with unlimited virtual registers.
- Each register is of one of four primitive types: boolean, integer, floating-point or pointer.
- Similar to RISC, memory operations are carried out in registers, while operations between registers and memory use Load and Store instructions.
- The IR is limited to 31 opcodes.
- The IR is organized into basic blocks which must be composed into valid control flow graphs, simplifying the work required for various optimizations.

This report will focus on LLVM's JIT component, which can be accessed through the MCJIT application programming interface (API). The MCJIT framework provides an API that accepts IR, generates optimized machine code, and provides a function pointer for calling the generated code. The JIT compiler offers several levels of optimization: none, less, default, and aggressive. It should be noted that the JIT compiler by default does not perform any IR optimizations or transformations. Instead, a developer must pass the generated IR to a PassManager with specific optimizations they intend to apply. These passes can be categorized as analysis passes, or transformation passes [14]:

- Analysis Passes: collects information about IR for use later by transformations, for debugging or for visualization. A few examples are *print-callgraph*, *print-function*, and *iv-users* for printing the users of a particular induction variable. There are roughly 40 such passes available.
- Transformation Passes: These typically modify IR. Examples include *adce* for dead code elimination, *instcombine* for combining redundant instructions, or *peephole* optimizing, and

```

1 treetop--> istore a
2
3      |
4      |imul--- isub-----+
5      |
6      |iadd
7      |
8      |-----> iload b <---+
9      |
10     |-----> iload a <---+

```

Listing 2: OMR IR representation for $(a+b)*(a-b)$. Note that the *iload* nodes are reused [19].

tailcallelim for eliminating tail calls. There are roughly 60 such passes available.

Generated IR can then be used by the ExecutionEngine to generate code for the target architecture. Before a function is executed by the ExecutionEngine, it first checks if the code cache, or ObjectCache, contains a copy. If the function could not be found, the compiler will generate the JIT code and store it in the ObjectCache before execution [15]. It is worth noting that a newer JIT API, called ORCJIT, is also part of the LLVM project. ORCJIT, or On-Request-Compilation JIT, is intended to compliment the MCJIT API – which compiles eagerly, by adding support for both lazy and concurrent compilations [16].

2.2 JitBuilder

JitBuilder is the embeddable framework for interacting with the Eclipse OMR JIT compiler Testarossa (TRJIT) [11]. Eclipse OMR is an open-source collection of components for building language runtime environments. Some of OMR's components include a garbage collection framework, a thread library, cross-platform port support, virtual machine building blocks, and the TRJIT compiler [6, 17]. Much of the infrastructure driving Eclipse OpenJ9, a popular, open-source Java Virtual Machine, link to OMR components.

Through JitBuilder's API, users generate IR upon which optimizations are applied and from which native code is generated [18]. Based on basic-blocks, the IR is arranged into directed acyclic graph (DAG) structures called trees, composed of nodes which each contain an opcode. The children of these opcode nodes are in turn operands. While the number of available opcodes is high – there are several hundred, most of the general instructions (add, load, compare) have a specific opcode for each data type: integer, pointer, double, float, vector, etc...² Trees with side-effects, which cannot be reordered, belong to a list of elements called treetops [19]. Existing nodes can be reused within a given tree, making optimizations such as common subexpression elimination relatively simple (see Listing 2). Though Testarossa supports several levels of optimization ranging from *cold*, with roughly 20 optimizations applied, all the way to *scorching*, where as many as 170 optimizations are applied, as of writing, JitBuilder is locked at the *Warm* optimization level [20, 21].

3 QUESTIONS AND METHODS

To answer the questions we outlined in the introduction, we wrote three simple programs for both LLVM MCJIT, and JitBuilder. For

² This can be contrasted with LLVM's roughly 31 opcodes, where the data type code is instead embedded within the instruction.

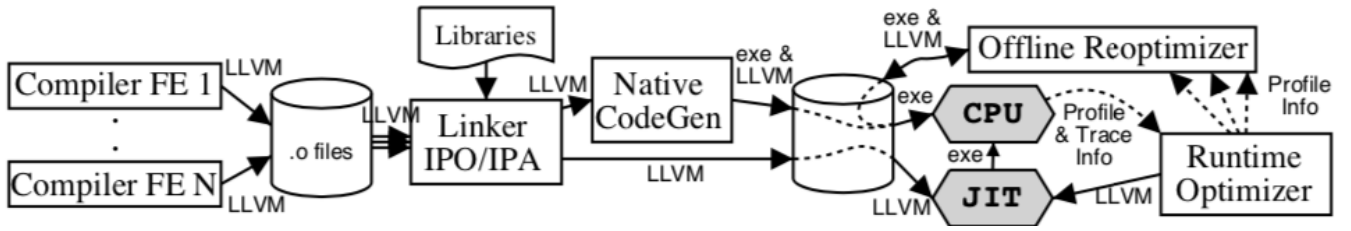


Figure 1: The LLVM Compiler Framework Architecture [12].

```

1 bool IncrementMethod::buildIL()
2 {
3     Return(
4         Add(
5             Load("value"),
6             ConstInt32(1));
7     return true;
8 }

```

Listing 3: Generating JitBuilder IR for the increment program.

the sake of comparison, we also wrote the programs using standard C++ without any JIT framework. The programs are as follows:

- increment - Calls a single function that adds one to an integer argument.
- recursive-fib - Calls a recursive fibonacci function for *fib(20)*.
- iterative-fib - Calls an iterative fibonacci function for *fib(20)*.

For the JIT implementations, the IR was built using the provided APIs, then native code was generated and later used for the actual function calls. We built and benchmarked the programs on an x86-64 Linux workstation running Ubuntu 19.04 with 32GB of RAM and an Intel i7-8700 (6-core, 12 thread) processor. Both LLVM, and OMR were built from source from their respective GitHub repositories [22, 23]. All the source code for this project can be found online [24].

4 RESULTS

4.1 Compilation Time

The first question we looked at was how long each framework took to compile a function. This time includes running the program from start to finish and includes the setup and teardown time of the JIT. Each program was executed 20 times.

In Table 1 we see that for the increment task JitBuilder compiled the function 61.5% faster than MCJIT. Looking into this further, the initial run to compile the function was roughly 900,000 nanoseconds. Repeated executions made by our benchmarking framework [25] were closer to 500,000 nanoseconds as the instructions for this relatively small example were likely be cached. For the other two tasks, we see that MCJIT was able to compile the functions more quickly than JitBuilder (recursive-fib compiled 16.5% faster, and iterative-fib compiled 40.1% faster).

4.2 Execution Time

With JIT compilation completed, we turn to measuring how much CPU time is spent actually executing the function. To measure this, we compiled the function once and then executed the compiled

```

1 static Function *CreateIncrementFunction(
2     Module *M,
3     LLVMContext &c) {
4     FunctionType *f = FunctionType::get(Type::getInt32Ty(c),
5     {Type::getInt32Ty(c)},
6     false);
7
8     Function *incrementF = Function::Create(f,
9     Function::ExternalLinkage,
10    "increment",
11    M);
12
13    BasicBlock *BB = BasicBlock::Create(c,
14    "EntryBlock",
15    incrementF);
16
17    Value *One = ConstantInt::get(Type::getInt32Ty(c), 1);
18
19    Argument *ArgX = &incrementF->arg_begin();
20    ArgX->setName("AnArg");
21
22    Value *Sum = BinaryOperator::CreateAdd(ArgX,
23    One,
24    "addresult",
25    BB);
26
27    ReturnInst::Create(c, Sum, BB);
28    return incrementF;
29 }

```

Listing 4: Generating MCJIT IR for the increment program.

function 1000 times. For each program, the process was repeated 20 times (see Table 2). Aside from the increment program, which favoured JitBuilder, both the recursive and iterative fib functions executed more quickly with LLVM generated code (0.53% faster for recursive, and 0.37% faster for iterative). Compiling the test programs with GCC 5.3, having an optimization level of O3 (large binary, fast code), we saw extremely low execution times for the native increment program as our function calls were essentially optimized away. It is worth noting that the code generated by the LLVM recursive-fib outperformed the native optimized code. Given two of our programs performed more quickly with MCJIT than with JitBuilder, it is interesting to examine the disassembly of the generated code.³

Examining the recursive-fib disassembly for JitBuilder (code listing 6), we see it is using the stack for storage. We expect these additional loads and stores, used for passing arguments and return values, are the reason for the performance disparity. The LLVM disassembly (code listing 5) on the other hand, passes arguments using registers. It's also interesting to see the condition for the jump comparison: *jg 0x2* with LLVM and *j1 0x2* in JitBuilder. We would expect the latter to be the better optimization considering

³We used GDB to print instructions starting at the function pointer address. The disassembly is using Intel style.

Program	LLVM MCJIT			Eclipse OMR JitBuilder		
	mean (ns)	median	std. dev.	mean (ns)	median	std. dev.
increment	1,378,060	1,387,338	39,774	536,741	537,103	2620
recursive-fib	1,548,316	1,547,491	3925	1,854,393	1,850,322	21,754
iterative-fib	2,509,502	2,519,808	60,429	4,192,213	4,191,730	10,419

Table 1: Results of compiling each function 25 times with each JIT framework.

```

1 (gdb) x/24i $1
2 0xffffffffcd000 <fib>:      push    rbp
3 0xffffffffcd001 <fib+1>:    push    r14
4 0xffffffffcd003 <fib+3>:    push    rbx
5 0xffffffffcd004 <fib+4>:    cmp     edi,0x2
6 0xffffffffcd007 <fib+7>:    jg      0xffffffffcd013 <fib+19>
7 0xffffffffcd009 <fib+9>:    mov     eax,0x1
8 0xffffffffcd00e <fib+14>:   pop     rbx
9 0xffffffffcd00f <fib+15>:   pop     r14
10 0xffffffffcd011 <fib+17>:  pop     rbp
11 0xffffffffcd012 <fib+18>:  ret
12 0xffffffffcd013 <fib+19>:  mov     ebx,edi
13 0xffffffffcd015 <fib+21>:  lea     edi,[rbx-0x1]
14 0xffffffffcd018 <fib+24>:  movabs r14,0xffffffffcd000
15 0xffffffffcd022 <fib+34>:  call   r14
16 0xffffffffcd025 <fib+37>:  mov     ebp,eax
17 0xffffffffcd027 <fib+39>:  add     ebx,0xfffffffffe
18 0xffffffffcd02a <fib+42>:  mov     edi,ebx
19 0xffffffffcd02c <fib+44>:  call   r14
20 0xffffffffcd02f <fib+47>:  add     eax,ebp
21 0xffffffffcd031 <fib+49>:  pop     rbx
22 0xffffffffcd032 <fib+50>:  pop     r14
23 0xffffffffcd034 <fib+52>:  pop     rbp
24 0xffffffffcd035 <fib+53>:  ret

```

Listing 5: LLVM MCJIT recursive-fib disassembly (Optimization level 3)

most times n is greater than 2. The code in the LLVM listing would perform relative jumps more often, while the code in the JitBuilder would flow through the instructions more often.

Looking at the estimated execution time minus JIT compilation for 1000 functions calls (see Table 3), we can see that JitBuilder’s generated code outperformed MCJIT both in the iterative-fib programs.⁴

4.3 Binary File Size

Looking at the size of the generated files (see Table 4), the linked LLVM MCJIT programs are roughly six times larger in size than the JitBuilder programs.⁵ Part of the explanation for this may have to do with potentially over-linking the LLVM programs. With over 167 possible linkable modules when using LLVM, we found what we think is the minimum number of modules required to use MCJIT. These 8 modules are *core*, *executionengine*, *interpreter*, *passes*, *mc*, *mcjit*, *support*, *nativecodegen*.

4.4 Resident State Set

The resident state set (RSS) is amount of program memory during execution at any one time. To measure the maximum RSS, we

⁴In our tests, the compile time plus the execution time of the JIT-ed function was less than the measurement for just the compilation. As noted earlier, we suspect this performance has to do with a high degree of cache locality. We subtracted the mean from Table 1 from the mean from Table 2 without accounting for the variance. This may be incorrect. Unfortunately, time constraints limited our ability to properly benchmark 1000 executions of the JIT-ed code.

⁵The JIT frameworks were compiled without debug symbols.

```

1 (gdb) x/32i $1
2 0xffffffff6a81034: sub     rsp,0x18
3 0xffffffff6a81038: mov     QWORD PTR [rsp+0x10],rbx
4 0xffffffff6a8103d: cmp     edi,0x2
5 0xffffffff6a81040: jl      0xffffffff6a8106d
6 0xffffffff6a81042: mov     rbx,rdi
7 0xffffffff6a81045: lea     edi,[rbx-0x2]
8 0xffffffff6a81048: call    0xffffffff6a81034
9 0xffffffff6a8104d: mov     rdi,rbx
10 0xffffffff6a81050: mov     rbx,rax
11 0xffffffff6a81053: sub     edi,0x1
12 0xffffffff6a81056: call    0xffffffff6a81034
13 0xffffffff6a8105b: xchg    rbx,rax
14 0xffffffff6a8105e: mov     rcx,rbx
15 0xffffffff6a81061: add     eax,ecx
16 0xffffffff6a81063: mov     rbx,QWORD PTR [rsp+0x10]
17 0xffffffff6a81068: add     rsp,0x18
18 0xffffffff6a8106c: ret
19 0xffffffff6a8106d: mov     rax,rdi
20 0xffffffff6a81070: mov     rbx,QWORD PTR [rsp+0x10]
21 0xffffffff6a81075: add     rsp,0x18
22 0xffffffff6a81079: ret

```

Listing 6: JitBuilder recursive-fib disassembly (Optimization level Warm)

used the time utility (`/usr/bin/time -v`). Looking at the results in Table 5, we see that LLVM consistently had the largest RSS. The memory overhead required by LLVM is roughly 4.5 times larger than JitBuilder’s requirement. Looking at the number of times inactive pages were reclaimed during execution (minor page faults), we see that LLVM had roughly 4 times the number of inactive pages collected.

4.5 Developer Experience

One additional aspect not typically included when considering overhead is the usability of the frameworks from a developer’s perspective. Under this banner we looked at two items: ease of use, and how configurable the frameworks were.

4.5.1 Usability. While ease of use is not likely to be the highest priority when considering which JIT framework to select, research has shown that improved API usability can positively impact developer productivity, while improving code quality and reducing errors [26]. Overall, we found that JitBuilder took less time to integrate, and provided a more intuitive API for generating IR. When considering usability we took note of the following items:

- Both of the frameworks provide public Github repositories [27, 28].
- The instructions for building the frameworks were readily available and simple to follow.
- Linking LLVM took considerable effort as it was unclear which of the 167 objects were required to use MCJIT. After some trial and error, we discovered that LLVM provides a utility, `llvmlite`, which simplifies this task by generating

Program	LLVM MCJIT			Eclipse OMR JitBuilder			Native (C++)		
	mean (ns)	median	std. dev.	mean (ns)	median	std. dev.	mean (ns)	median	std. dev.
increment	1,463,310	1,463,032	1660	534,192	534,064	869	0.876	0.875	0.002
recursive-fib	13,674,832	13,664,199	26,548	28,707,381	28,699,013	29,480	16,016,325	15,996,370	60,459
iterative-fib	2,657,528	2,668,792	83,388	4,227,967	4,190,232	102,744	0.876	0.875	0.003

Table 2: Results of JIT compiling each function once and executing the generated code 1000 times.

Program	LLVM MCJIT	Eclipse OMR JitBuilder
increment	85,250	0
recursive-fib	12,126,516	26,852,988
iterative-fib	148,026	35,754

Table 3: Estimated time to execute JIT function 1000 times (mean from Table 2 - mean from Table 1). JitBuilder’s increment score is listed as 0 because the compile and execution time was actually slightly lower than the compile time itself. We suspect these low values were due to a high degree of cache locality exhibited by the program.

Program	LLVM MCJIT	Eclipse OMR JitBuilder	Native (C++)
increment	60,640,216	10,148,608	16,616
recursive-fib	60,671,176	10,149,272	16,600
iterative-fib	60,652,728	10,149,184	16,600

Table 4: Total size in bytes of linked binary test programs.

Program	LLVM MCJIT		Eclipse OMR JitBuilder		Native (C++)	
	max RSS (kb)	minor page faults	max RSS (kb)	minor page faults	max RSS (kb)	minor page faults
increment	43,544	1478	9416	344	1548	65
recursive-fib	45,472	1545	9768	384	1532	63
iterative-fib	44,452	1520	10,044	423	1684	66

Table 5: Maximum resident state set (RSS) in kilobytes during execution of a single compilation and execution of the generated function. Note that Native had no run-time compilation phase. A minor page fault occurs when the OS reclaims inactive pages during execution. Page size was 4096 bytes.

header and linking arguments based on a list of provided objects⁶

- JitBuilder was relatively simple to integrate, requiring only a single header file and single module to link: `jitbuilder`, where as LLVM required us to include 20 header files.
- We found that writing the IR generators took less time with JitBuilder than with LLVM. The most challenging was writing the iterative-fib generator for LLVM which included an induction value, a Phi node, several basic blocks for before, during and after the loop, condition values, as well as allocation and store pointers.⁷
- In every case the number of lines of code to generate the IR were fewer for JitBuilder. This can be seen in the increment code listing for JitBuilder2, and for LLVM MCJIT1.
- The in-memory objects generated by LLVM MCJIT can contain debug symbols (DWARF format) allowing debugging to take place in GDB 7.0 and higher [29].

⁶Commands for building the programs can be found in makefiles in the accompanying source code under `src/<framework>/` [24].

⁷Source code for the LLVM iterative-fibonacci program can be found in the CreateFibFunction within `src/llvm/iterative-fib/fib.cpp`

4.5.2 Configuration. While the API of LLVM MCJIT may have an overabundance of controls, it does provide an high level of configuration compared to JitBuilder, including which optimization and analysis passes to run, control over the memory structure of the generated code, control over the object cache, and what level of optimization to generate the code at. As mentioned, JitBuilder on the other hand is currently locked at the *Warm* optimization level, limiting the number of optimizations performed on the IR.

5 RELATED WORK

Given the overhead involved in fetching, decoding, and executing instructions, interpreters are a common place we see JIT compilers employed. Java Virtual Machines [4, 18] have been employing JIT compilers for over two decades, while the Python world has both Numba, a Python JIT compiler based on LLVM [30, 31], and the PyPy project – a Python interpreter with it’s own tracing JIT [32].

JitBuilder has been integrated in several demonstration interpreters [33–35], while work is ongoing to build new runtimes, or to bolster existing runtimes with it’s parent project, Eclipse ORM [36].

There is also work being done to allow JitBuilder to ingest LLVM IR [37]. Not only would this allow JitBuilder to bolt onto existing LLVM based projects, it could help provide deeper insight into the quality of the generated code between the two frameworks.

6 FUTURE WORK

To provide a deeper glimpse into the quality of the generated code, the number of test programs could be expanded to cover such scenarios as arrays, large vector operations, and very large functions. It would be interesting to compare the performance of repeated JIT compilations rather than tearing down the JIT infrastructure between calls. The JIT compiler in Eclipse OMR, TRJIT, supports many levels of compilation, as well as support for synchronous, and asynchronous compilation schemes, however, these options have not yet been exposed to JitBuilder. While we believe JitBuilder should be expanded upon to provide such controls, special attention would have to be given to maintaining the simplicity and usability of the API. Finally, testing the compilers under more complex scenarios such as making JIT-to-JIT function calls and dealing with code cache limitations would provide more real world results.

7 SUMMARY

Considering the compiler behind JitBuilder was designed specifically for dynamic run-time compilation, it makes sense that it has a lighter footprint than MCJIT, which shares its codebase with the static compiler for LLVM. Unlike LLVM's IR, which is meant for life-long usage, the design of OMR's IR does not necessarily have ahead of time, or offline goals in mind. We believe this simplifies the work required when IR interactions are made in JitBuilder and may give it a performance edge as a dynamic compiler. While the code generated by MCJIT did outperform JitBuilder in some cases, we were unable to enable all of the optimizations for JitBuilder.

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