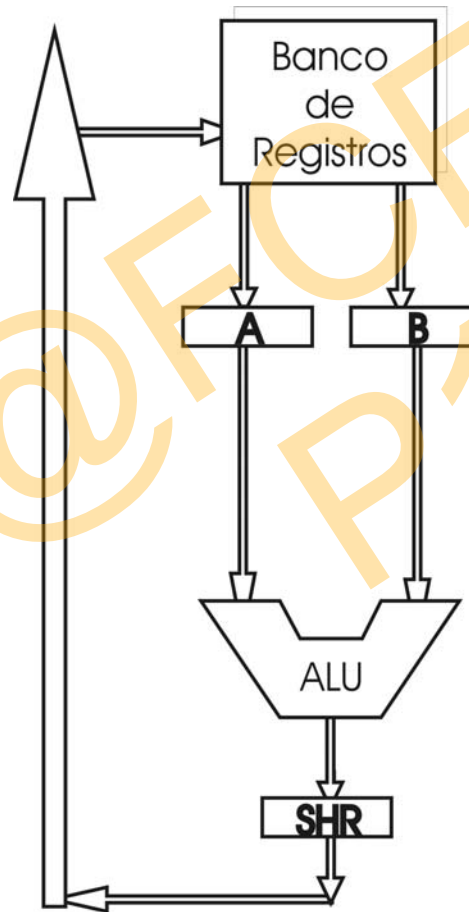


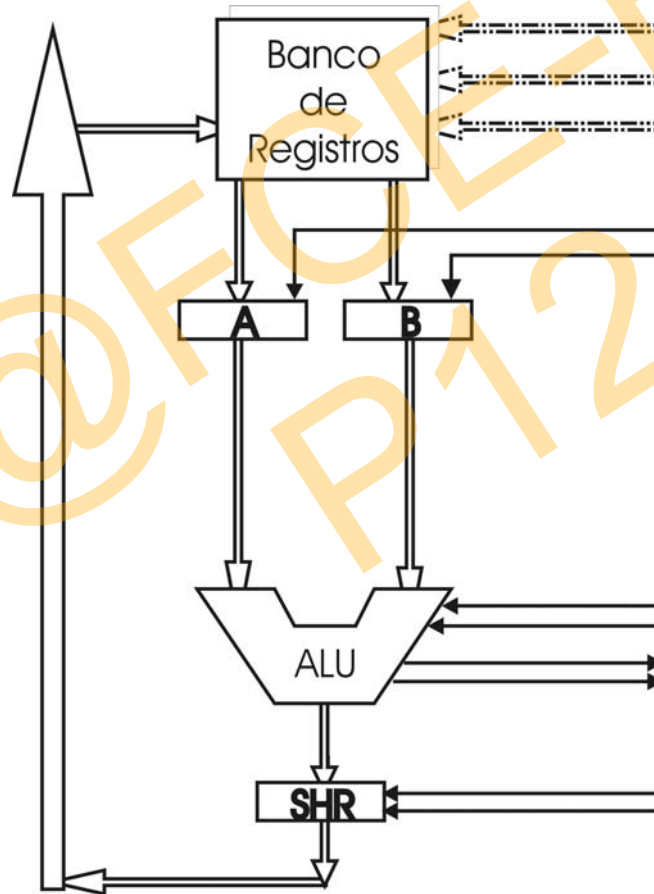
Diseño del ***μProcesador***

Aurelio Jacinto Nolasco
FCE-BUAP
OArqDeCompP12

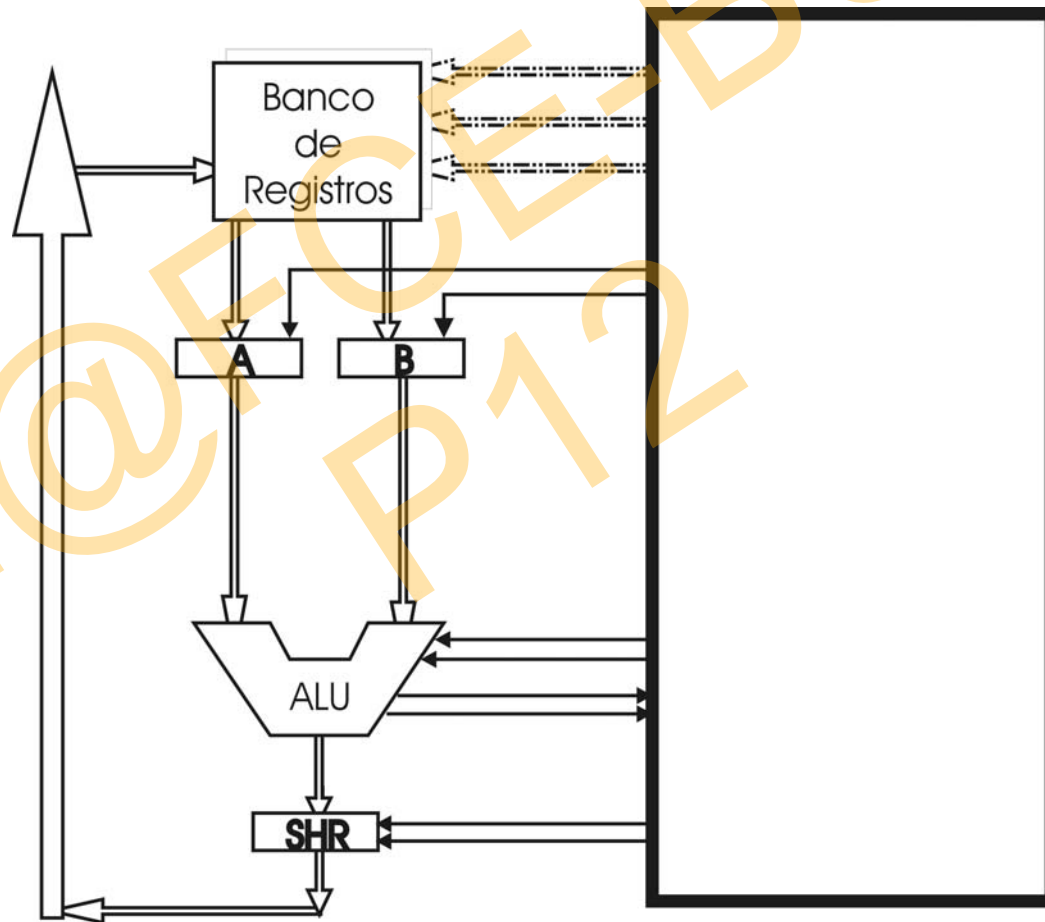
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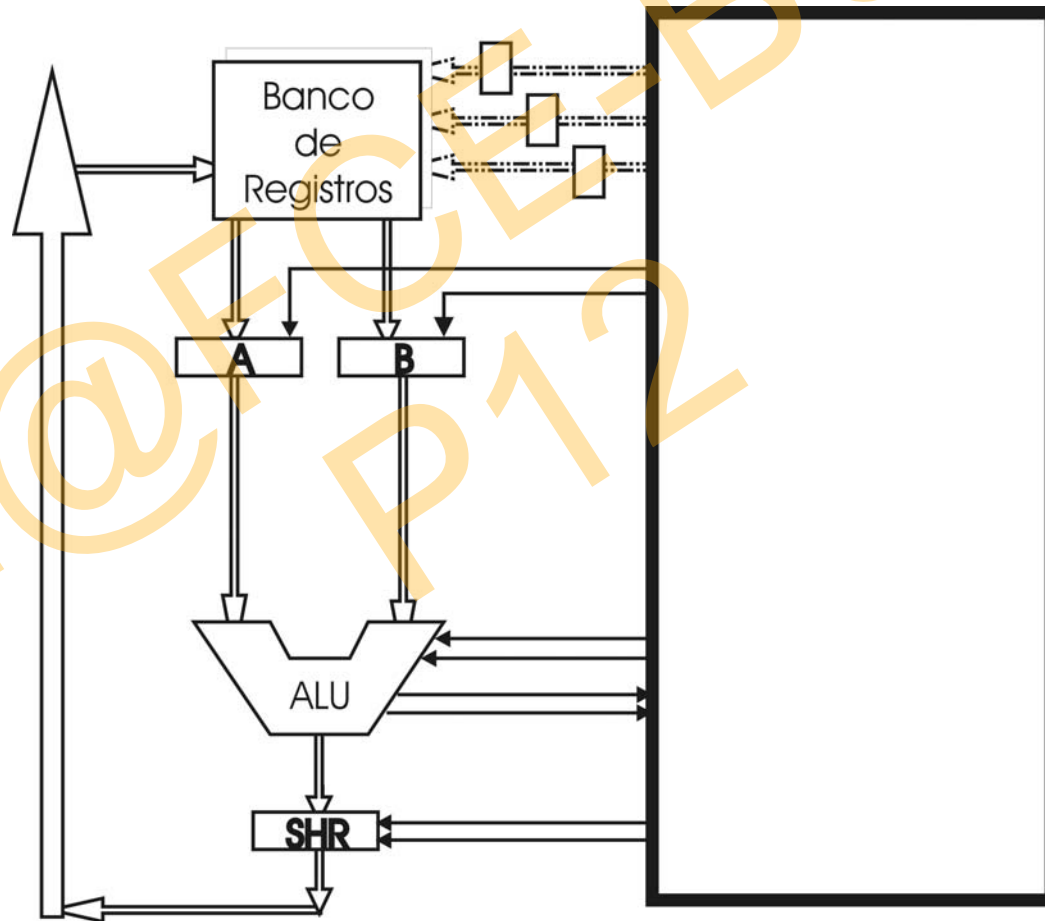
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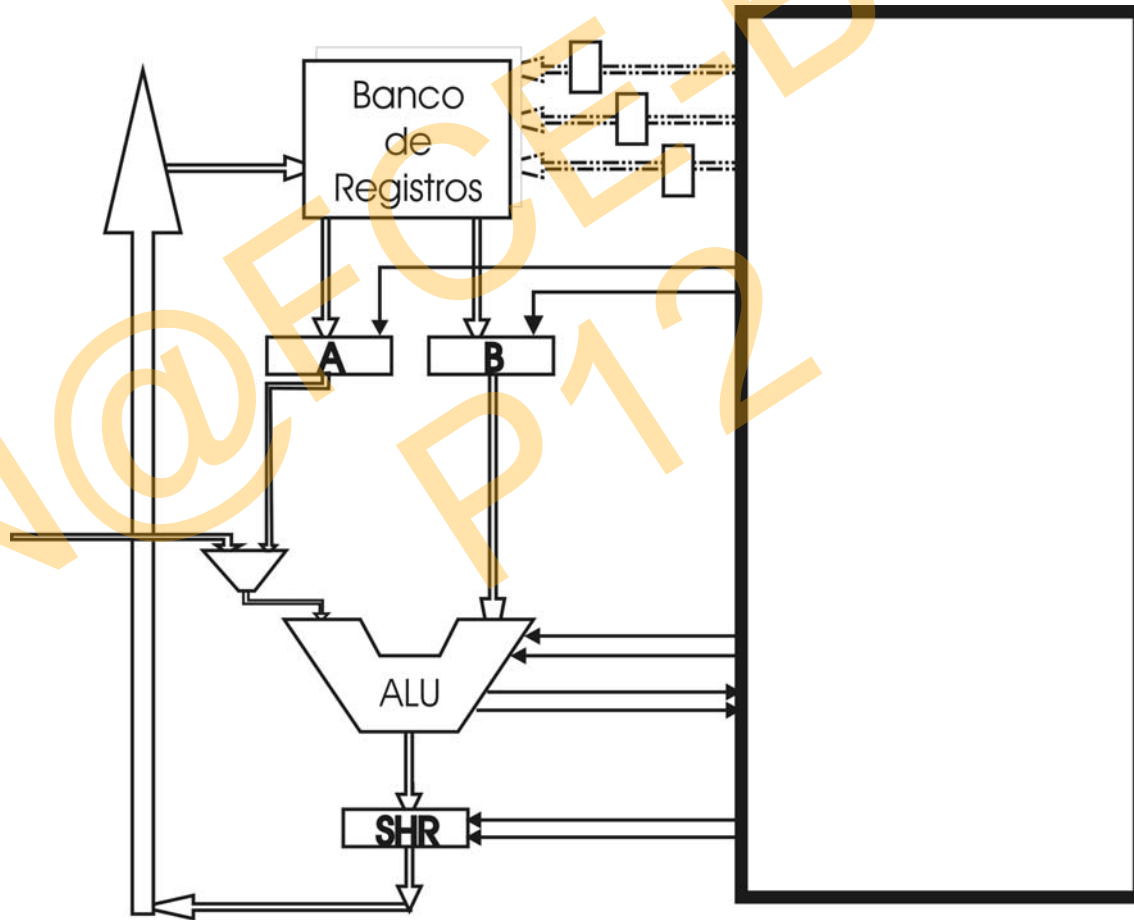
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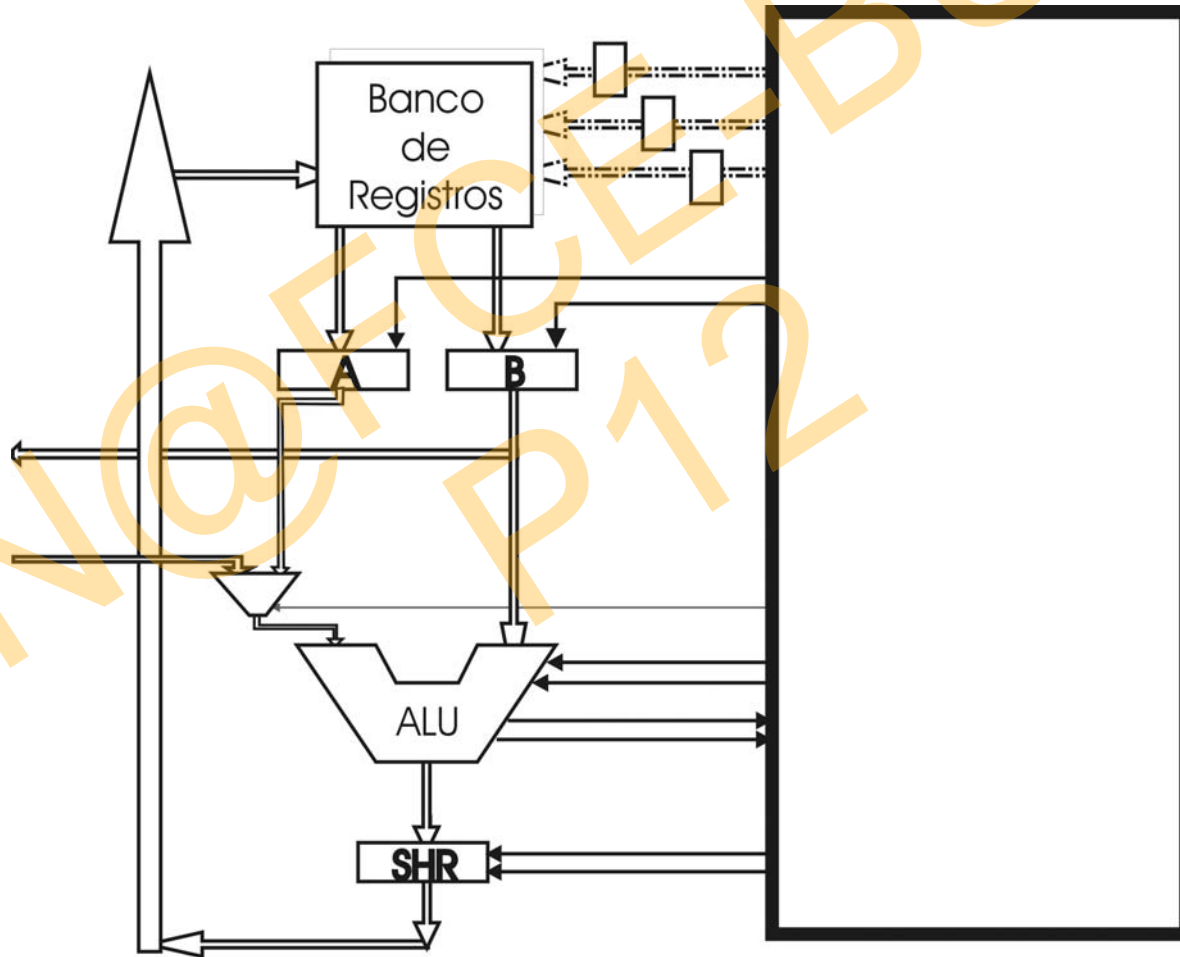
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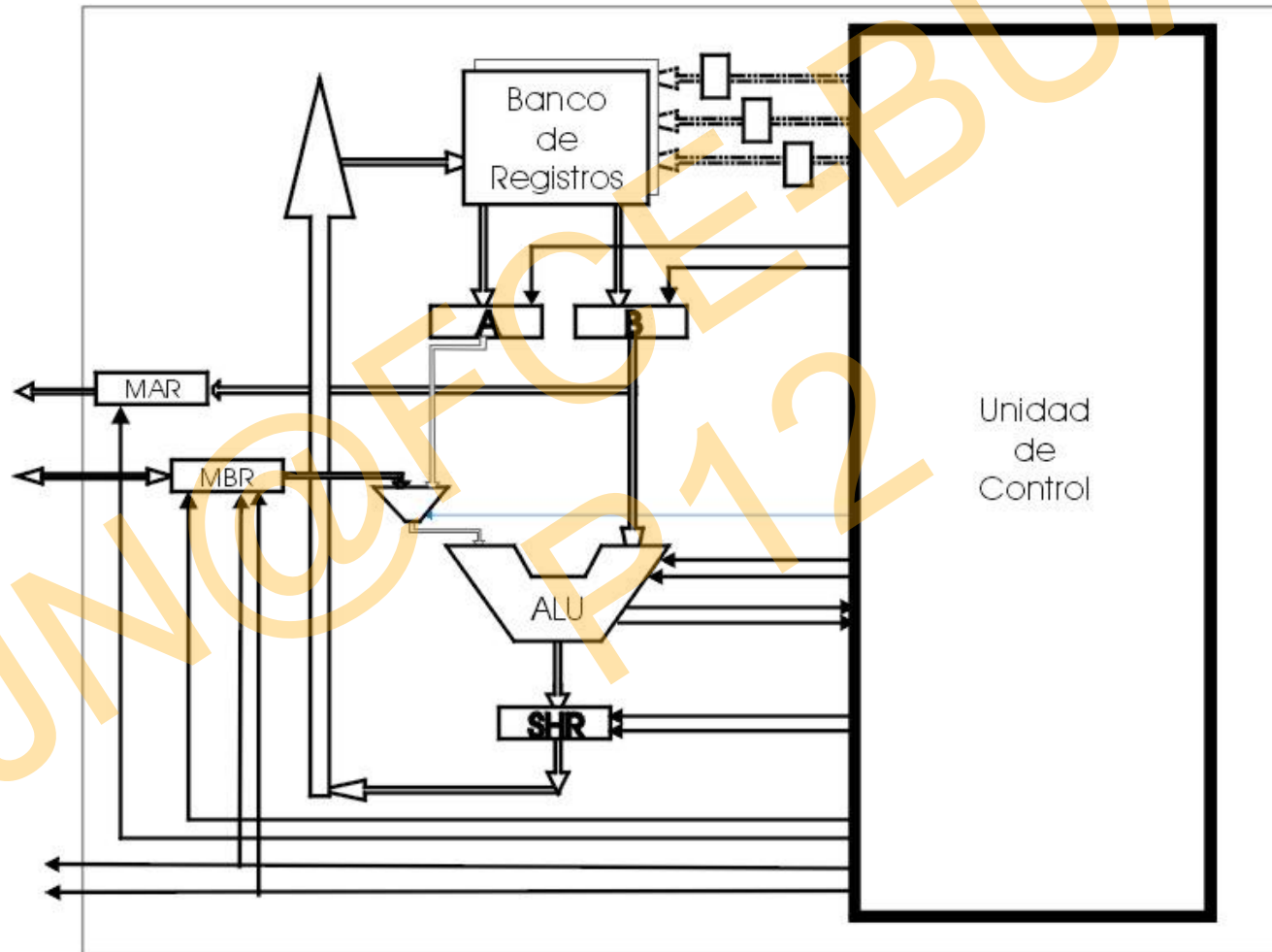
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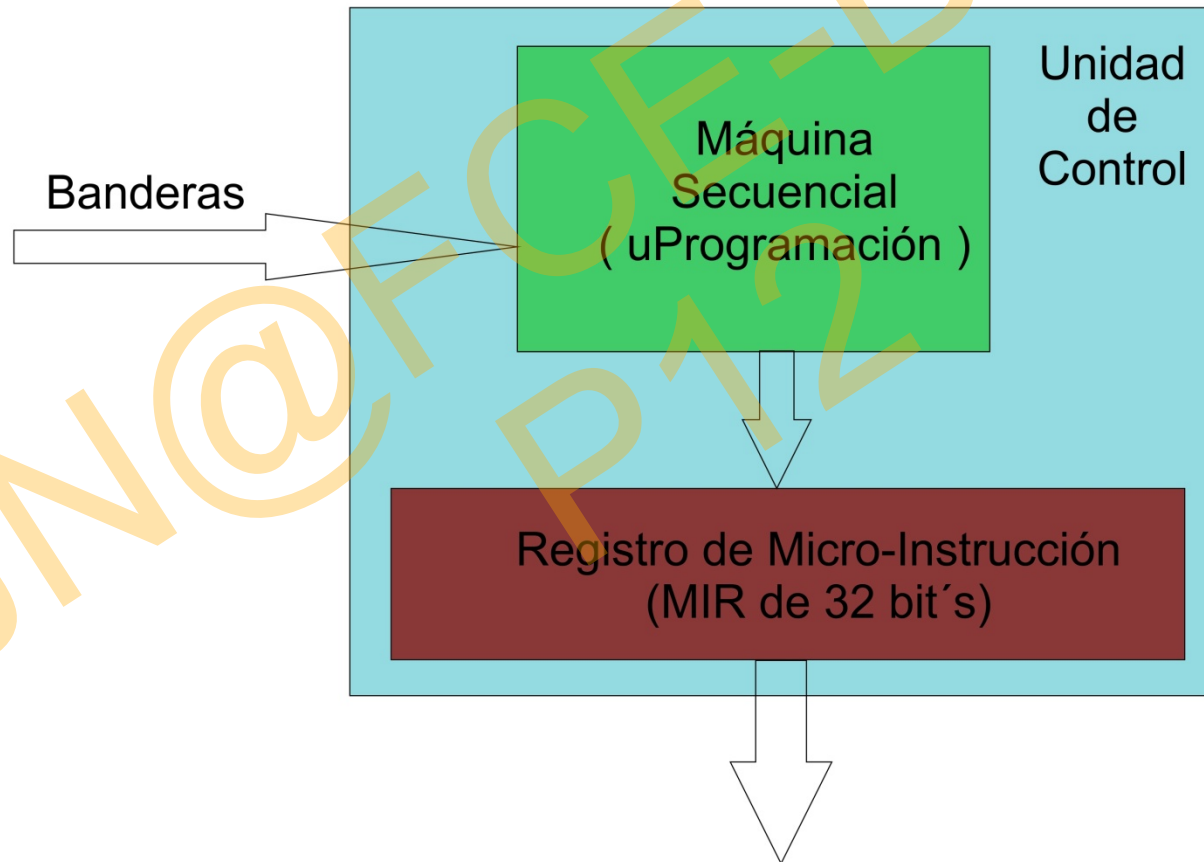
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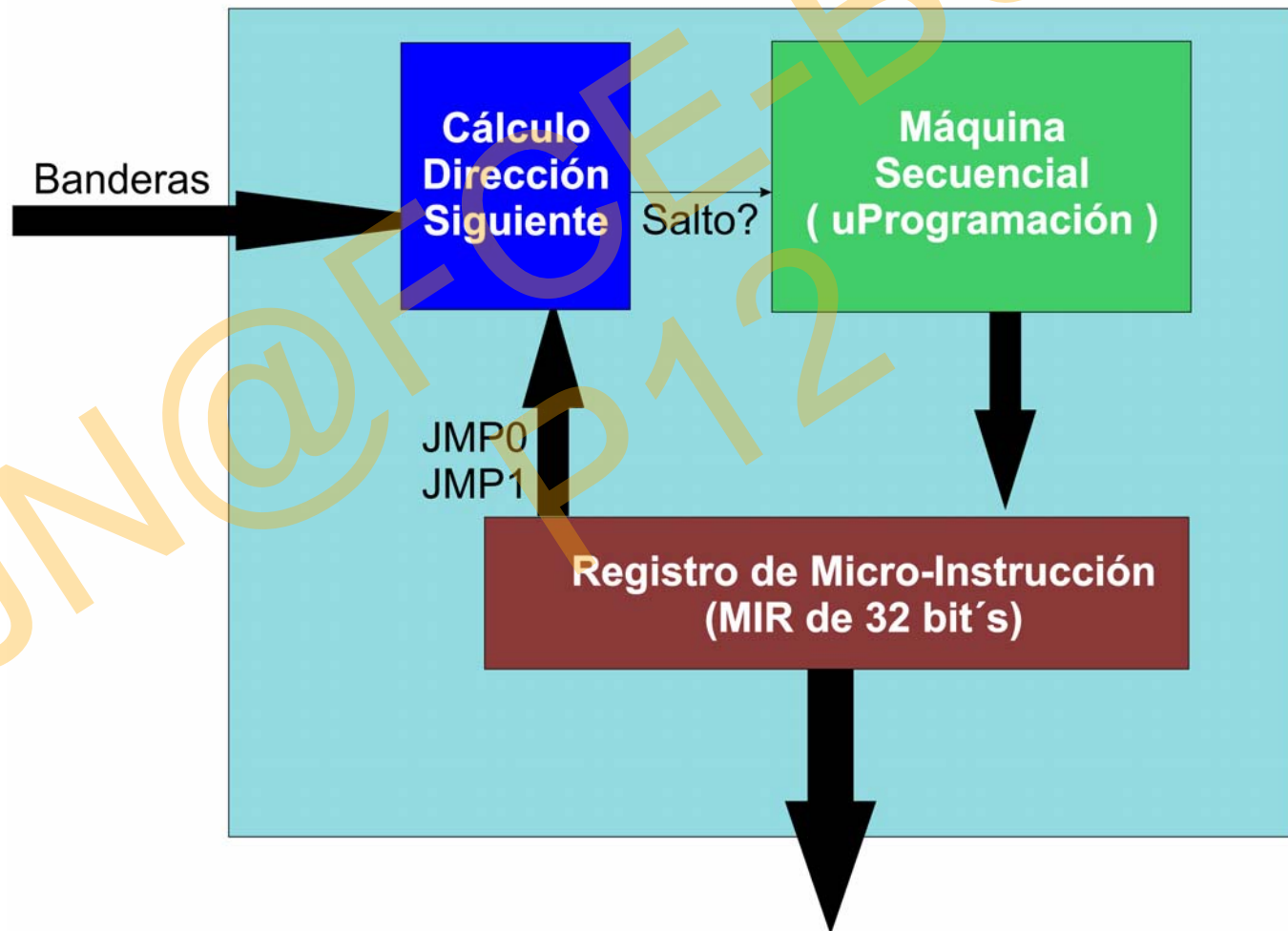
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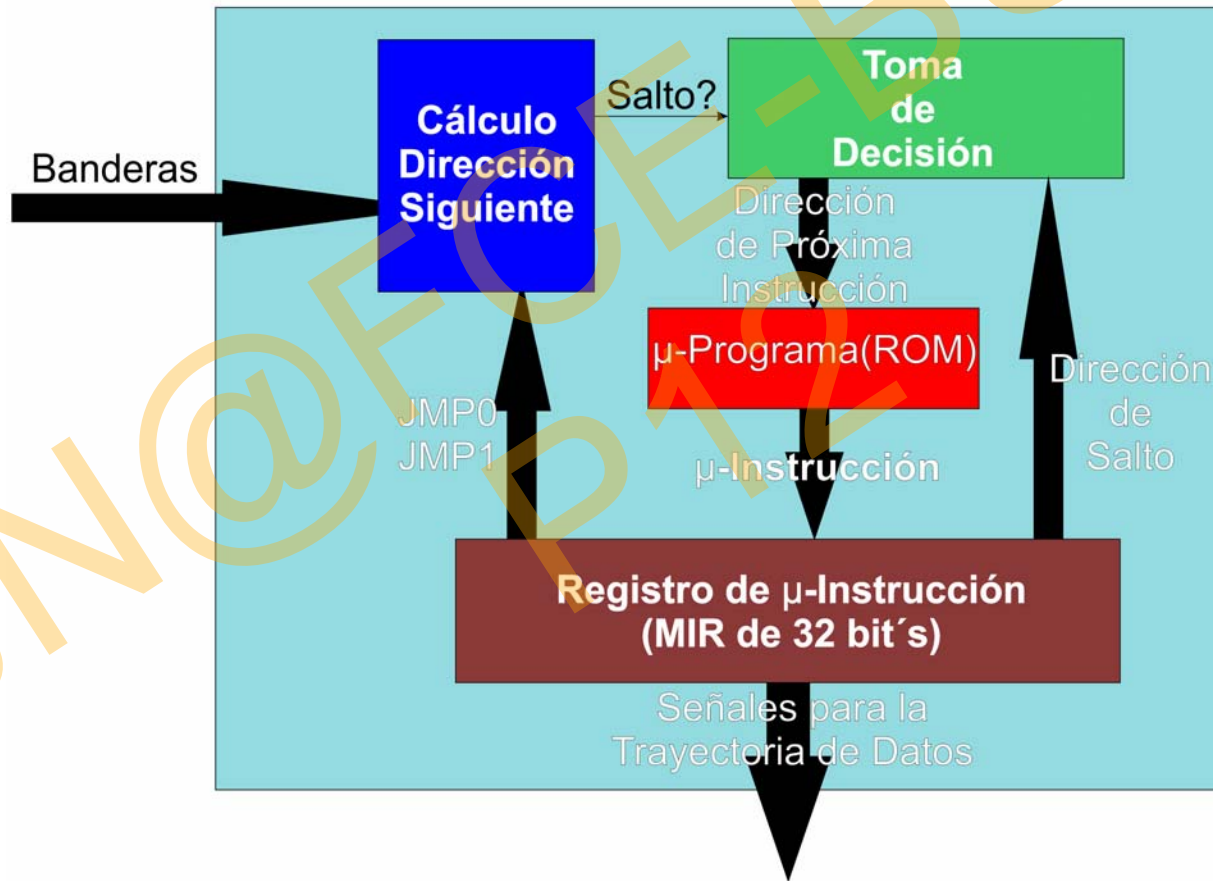


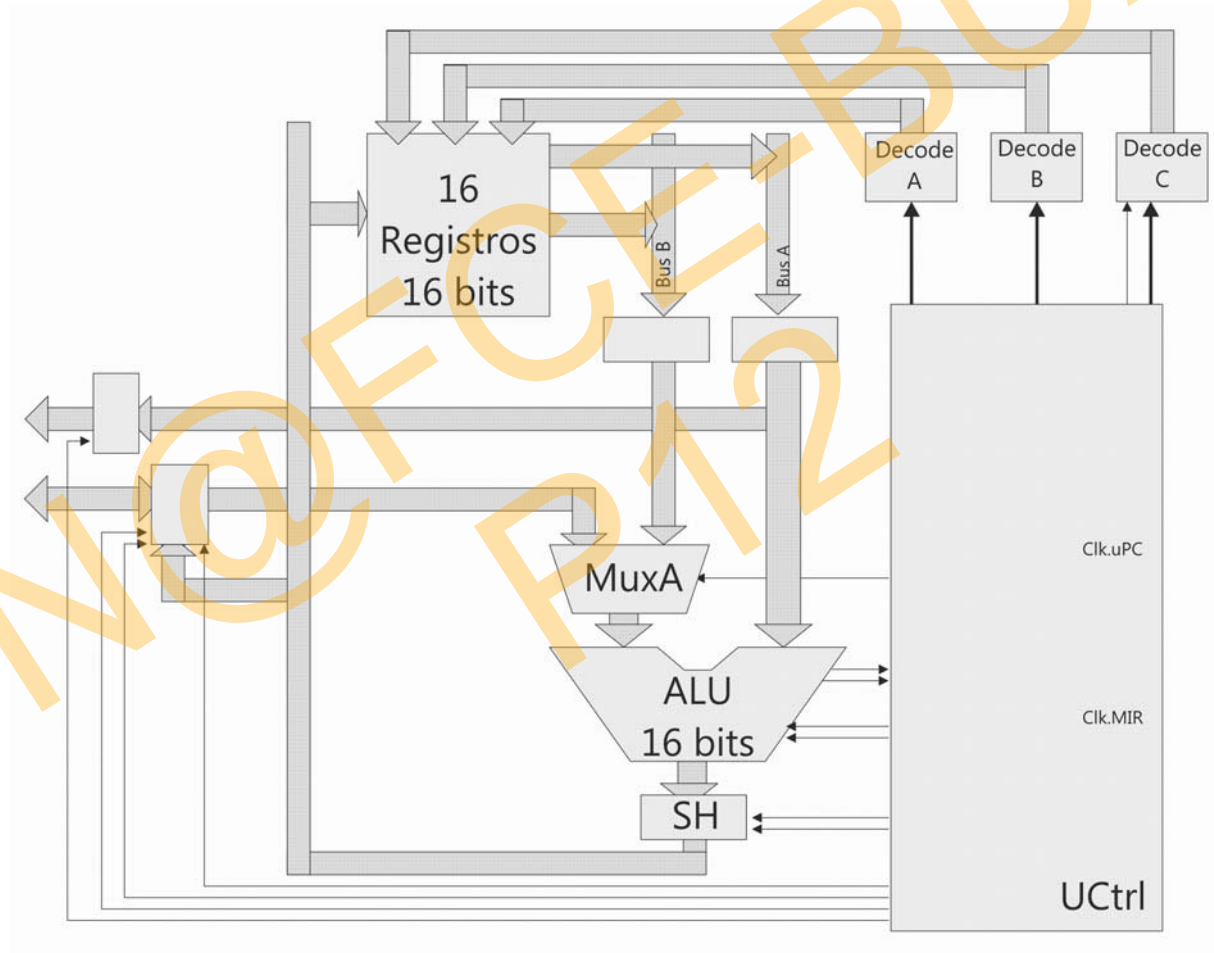
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Ciclo de μ -Instrucción

