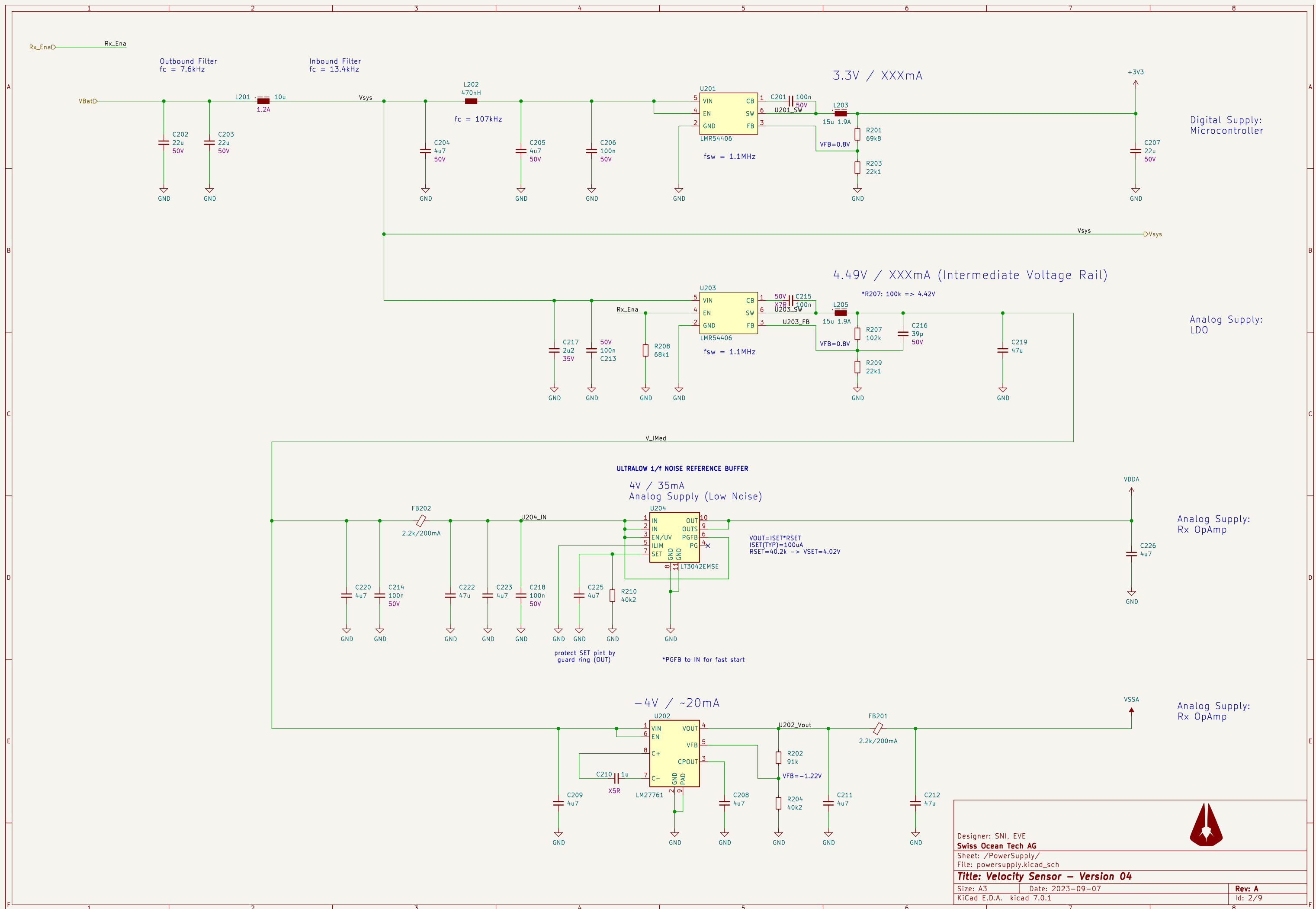
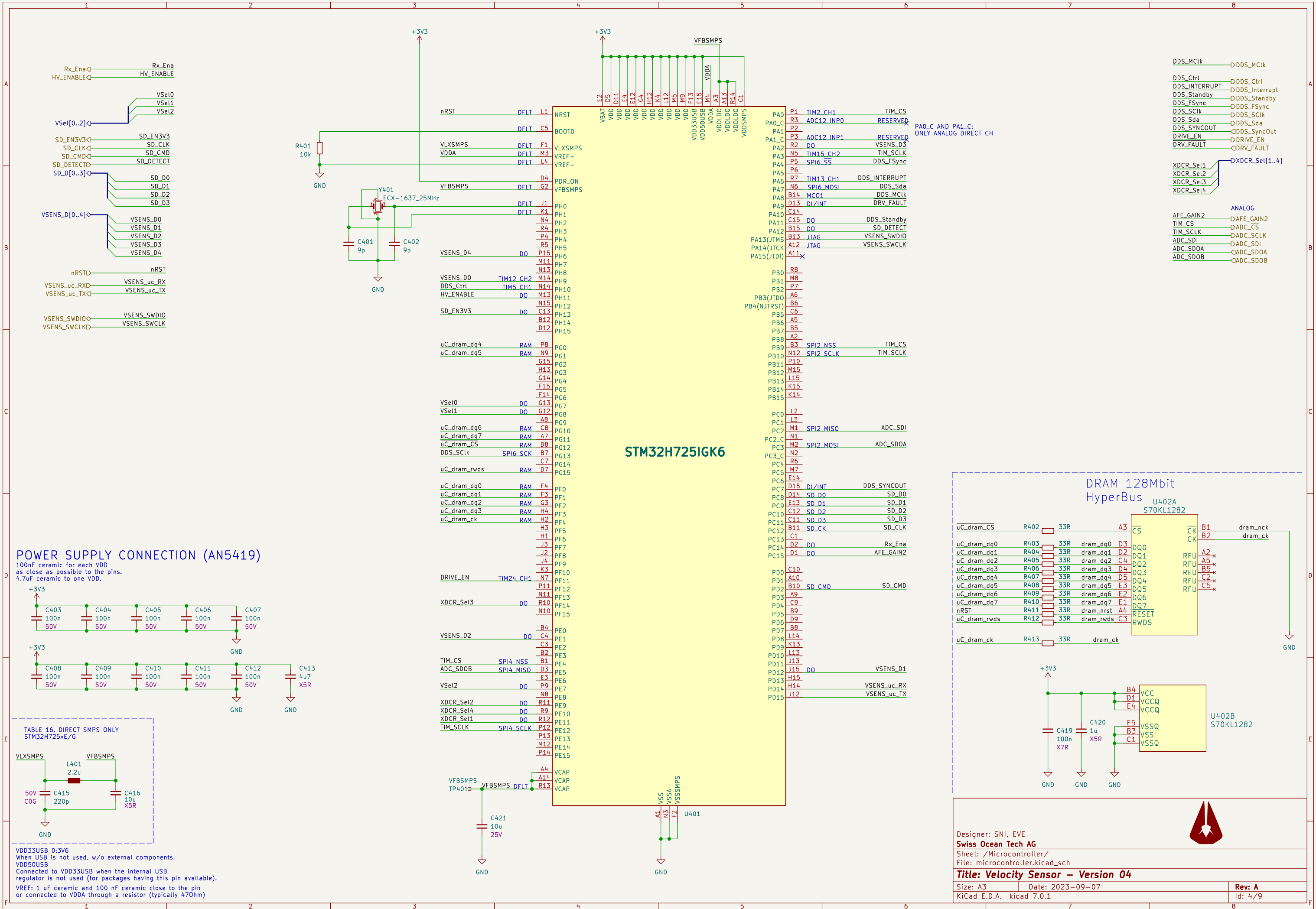
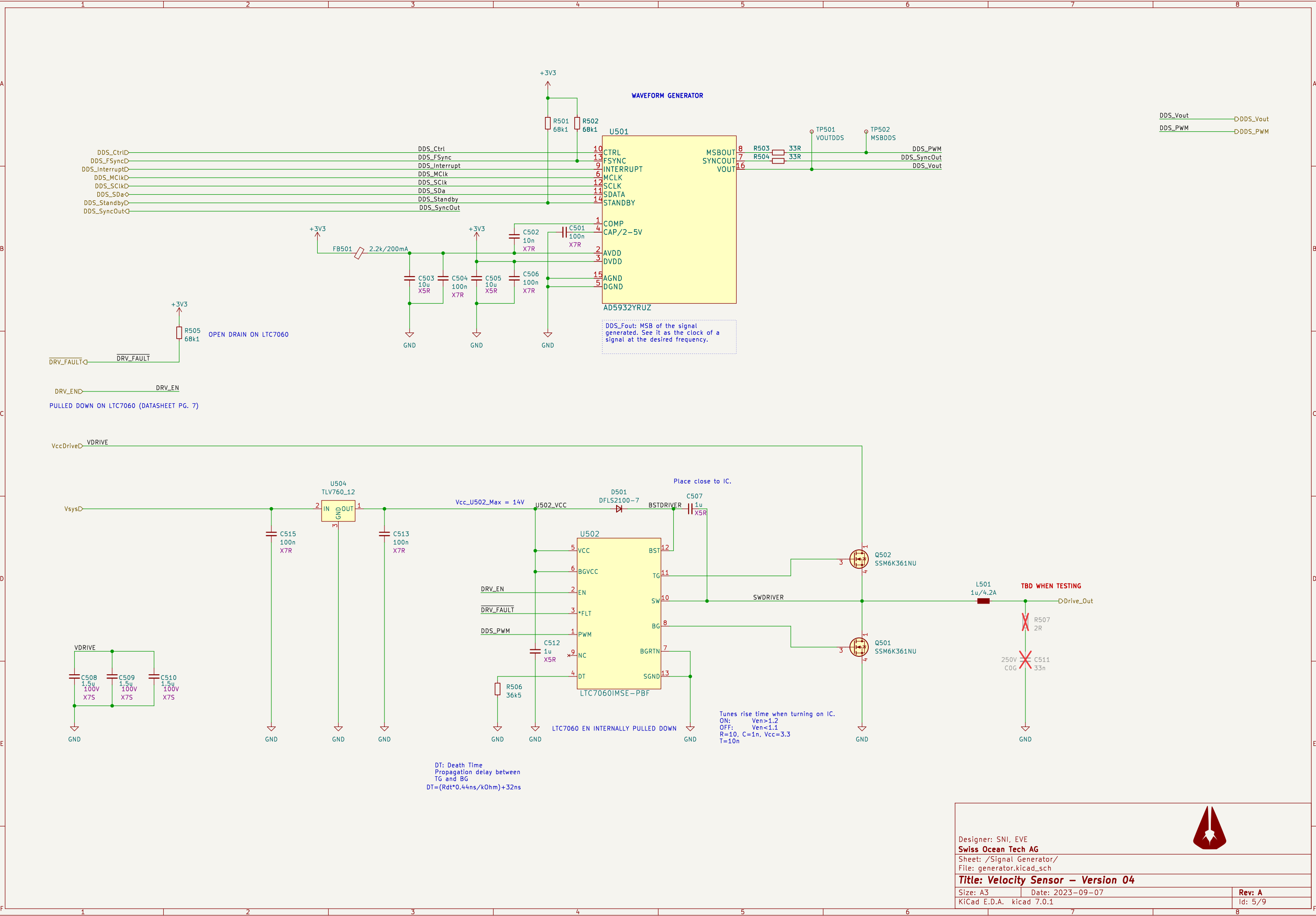
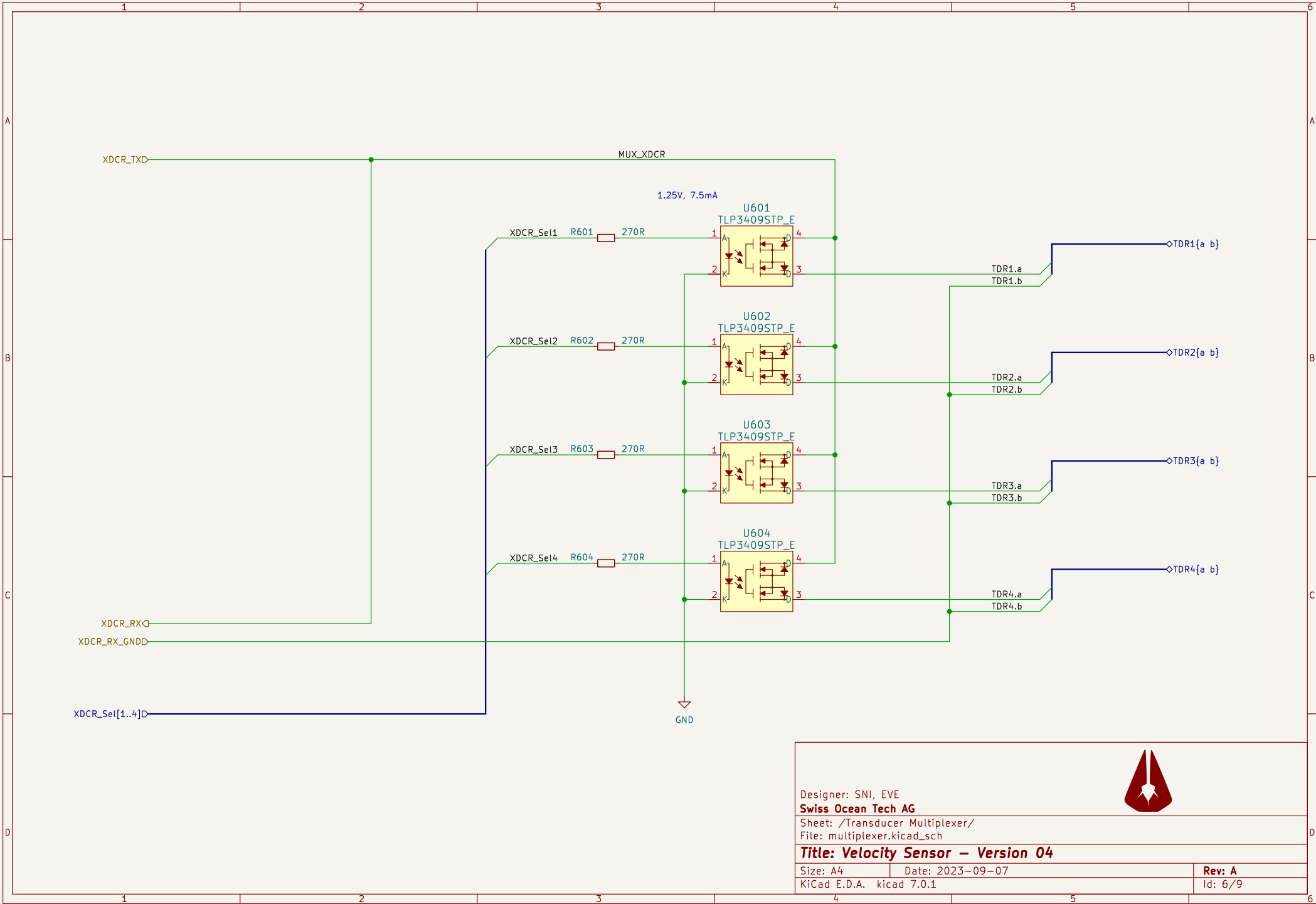


- FID101 FID102 FID105 FID106
- FID103 FID104 FID107 FID108

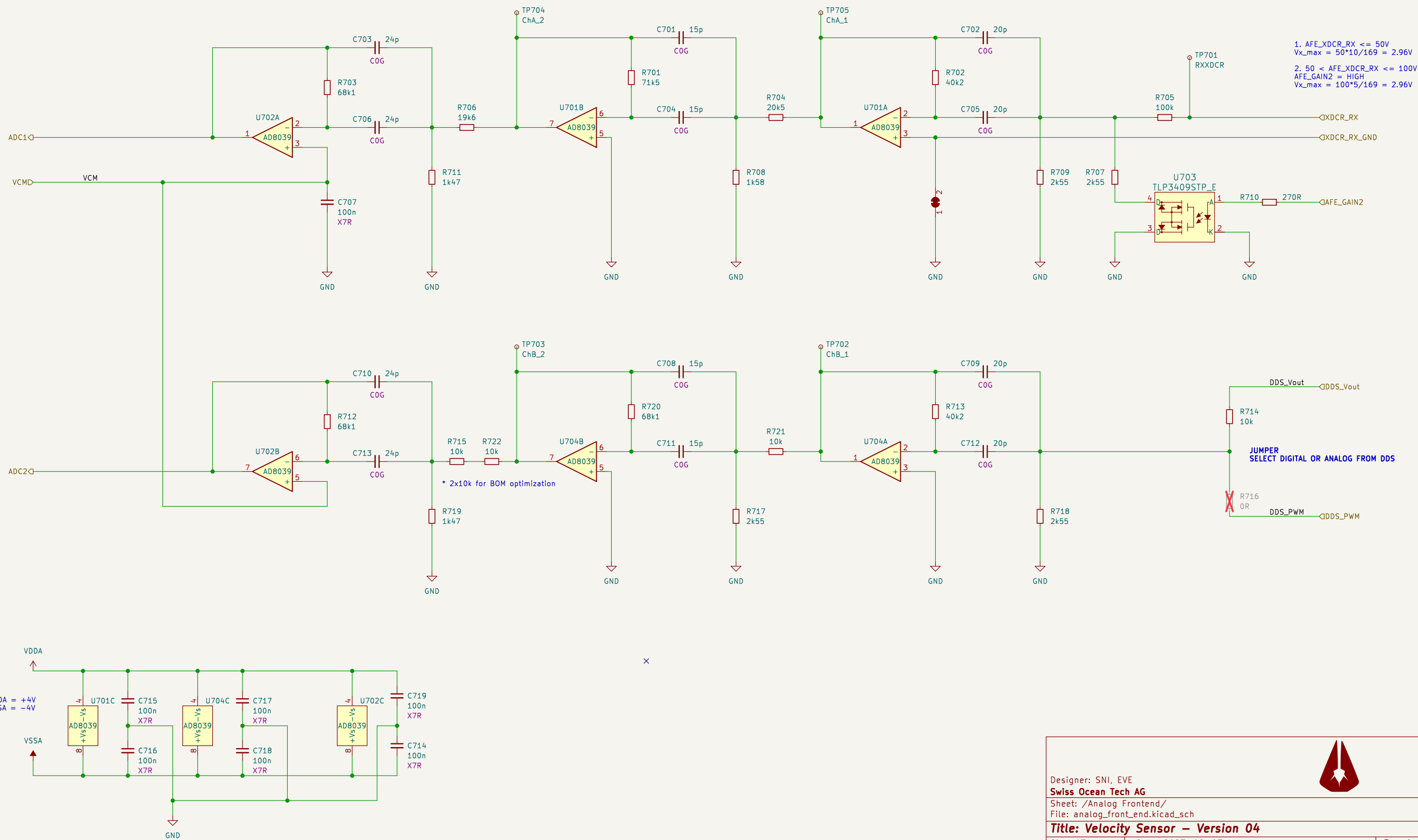




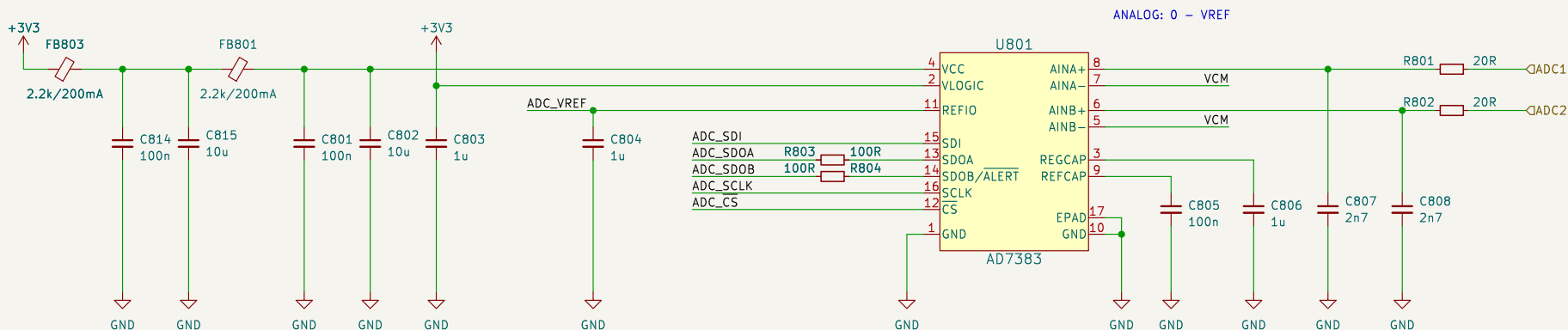




Analog Filter Path



ADC 2CH 16-bit



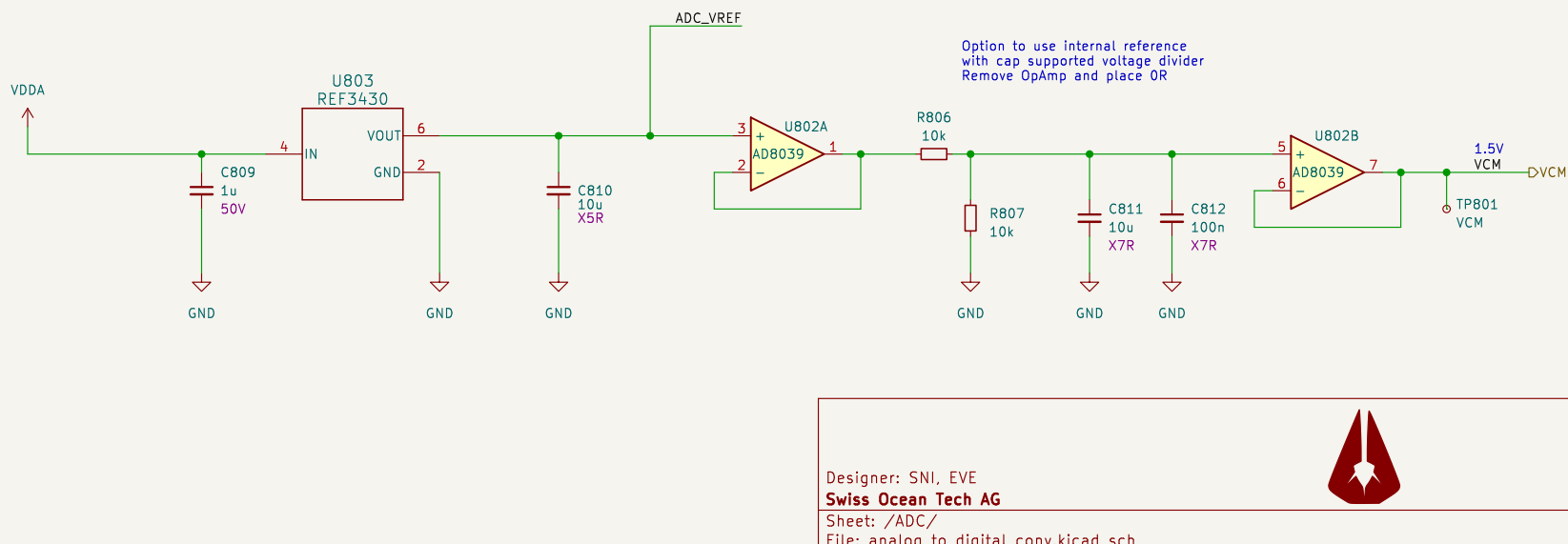
Place close to IC.

ADC_VREF IS 3V0
MAKE SURE THIS IS THE MAX VOLTAGE SUPPLIED TO AINA+ AND AINB+

OPAMPS POWERED WITH 5V0 TO AVOID DISTORTION

SEE DATASHEET, FIG.33

ADC_SDI0 ADC_SDI
ADC_SDOA0 ADC_SDOA
ADC_SDOB0 ADC_SDOB
ADC_SCLK0 ADC_SCLK
ADC_CS0 ADC_CS



Designer: SNI, EVE
Swiss Ocean Tech AG

Sheet: /ADC/
File: analog_to_digital_conv.kicad_sch

Title: Velocity Sensor – Version 04

Size: A4 Date: 2023-09-07

KiCad E.D.A. kicad 7.0.1

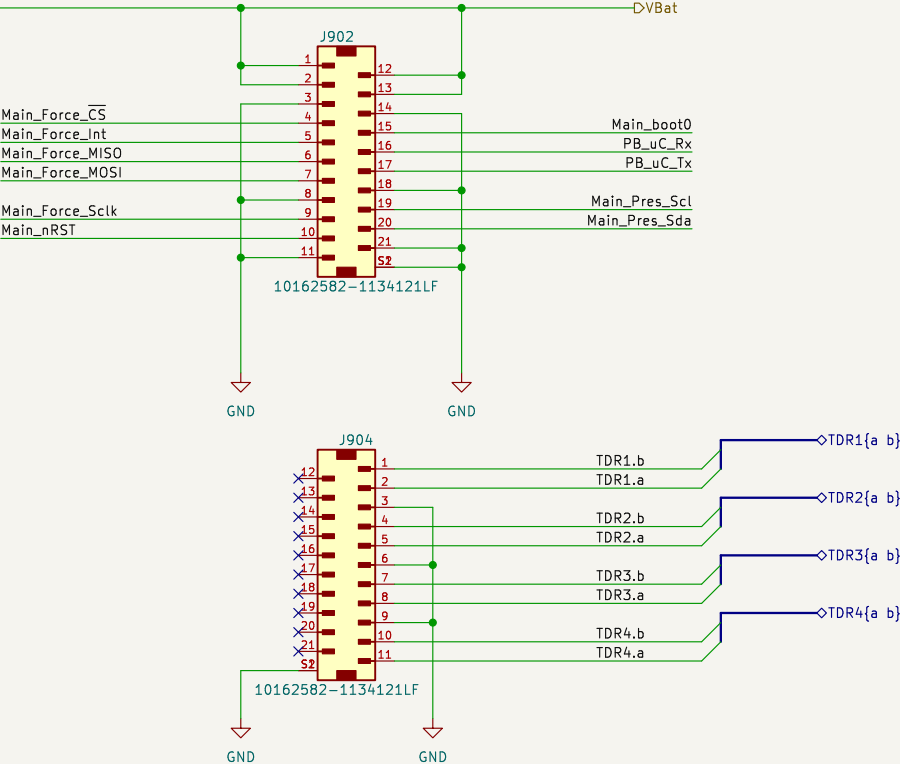
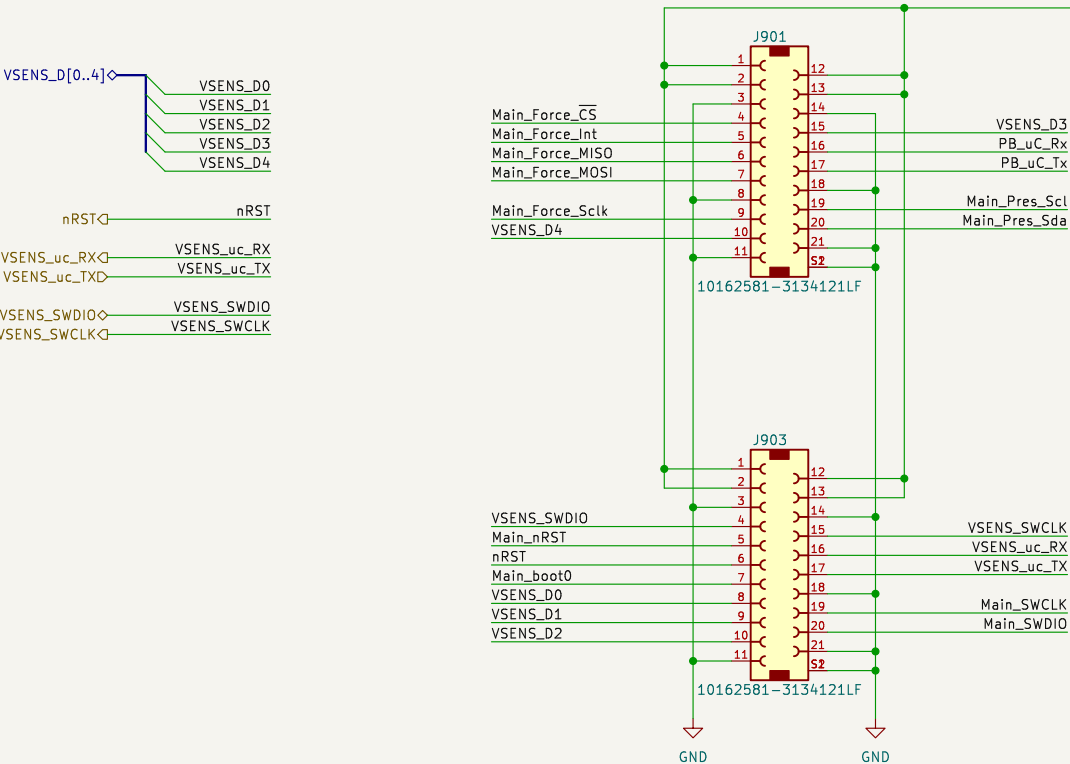
Rev: A

Id: 8/9



SOC Mainboard

To Peripheral Board



SD CARD INTERFACE

