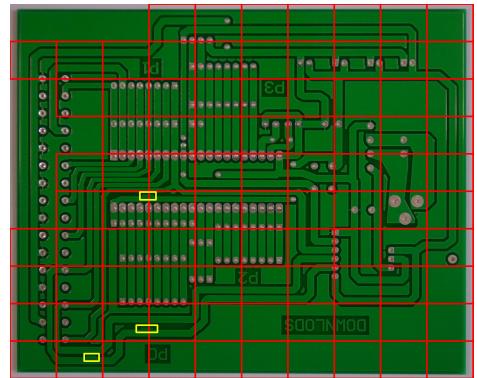
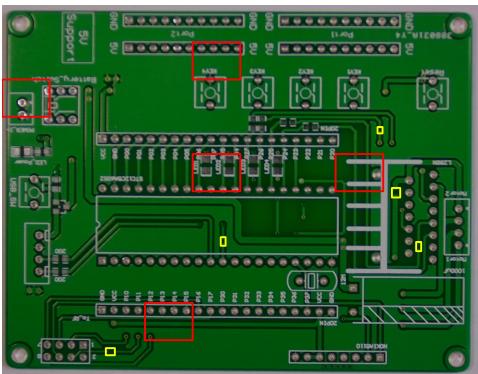


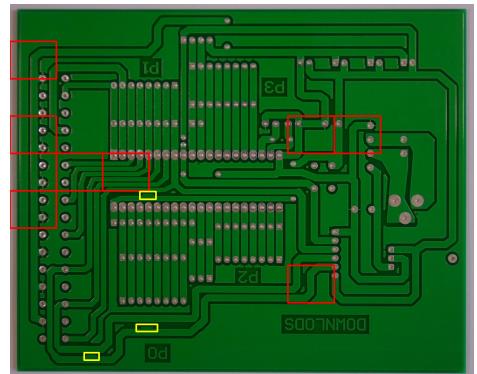
(a) Proposed Model: Open Circuit



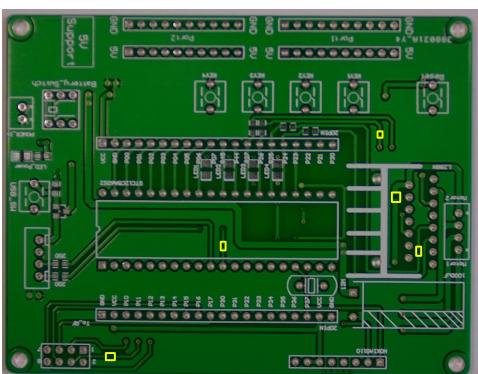
(b) Proposed Model: Spur



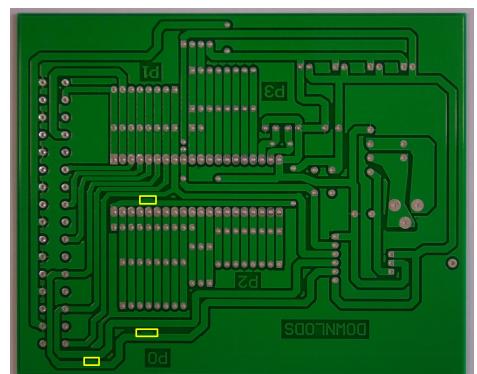
(c) EfficientNetV2B0: Open Circuit



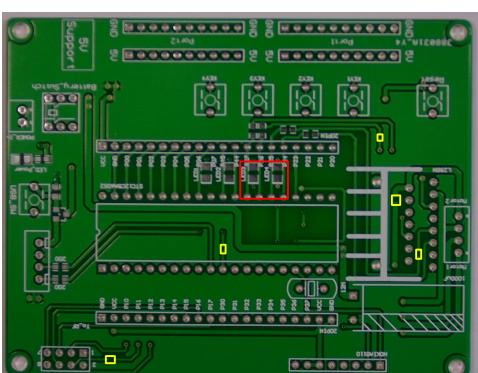
(d) EfficientNetV2B0: Spur



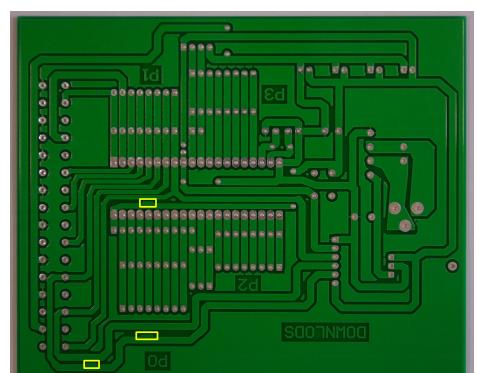
(e) MobileNetV2: Open Circuit



(f) MobileNetV2: Spur

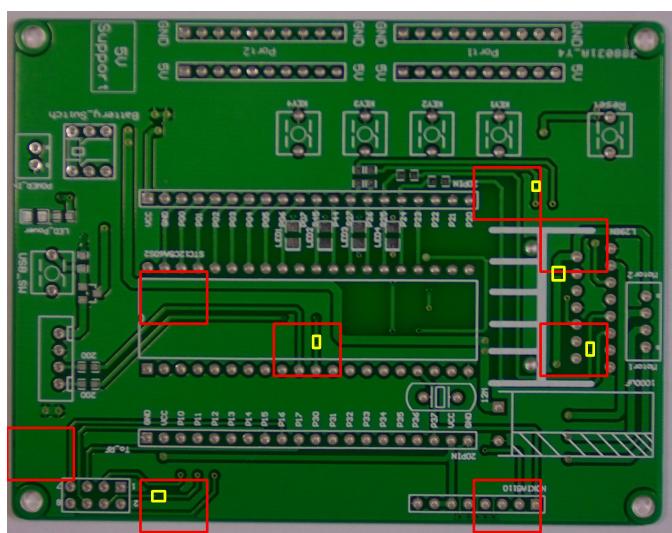


(g) MobileViT-XXS: Open Circuit

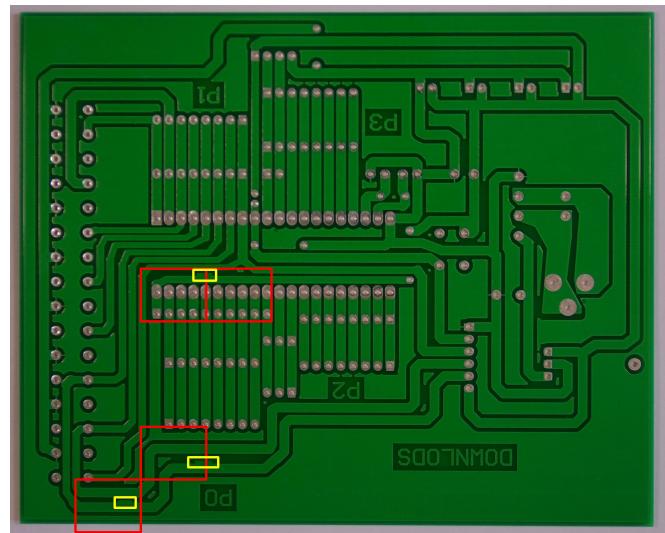


(h) MobileViT-XXS: Spur

Figure A.1: Result of defect detection on Open Circuit and Spur classes

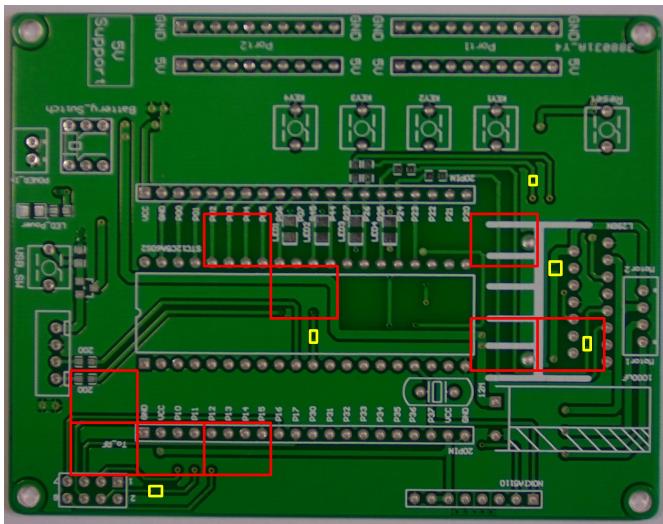


(a) Open Circuit

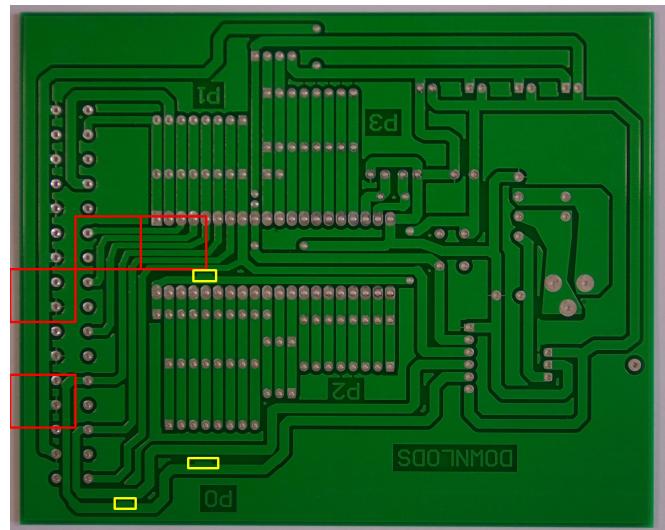


(b) Spur

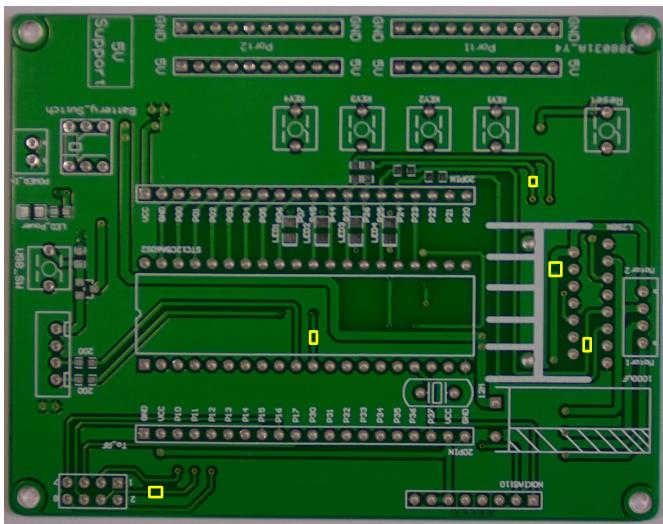
Figure A.2: Benchmark result with Xception (pre-trained on ImageNet) implemented on desktop



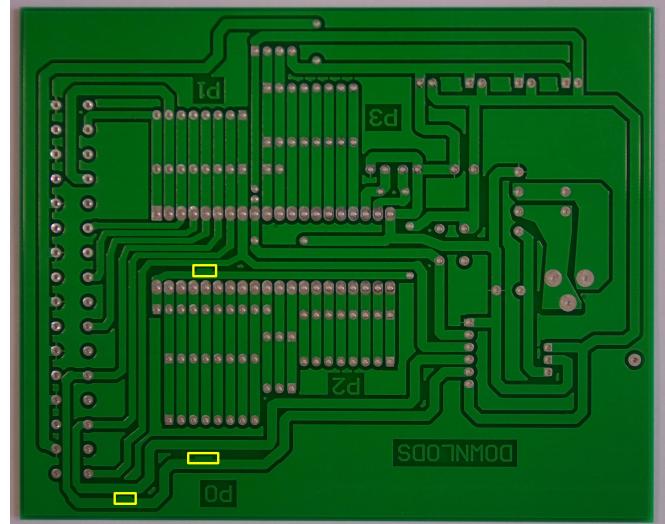
(a) MobileNetV2: Open Circuit



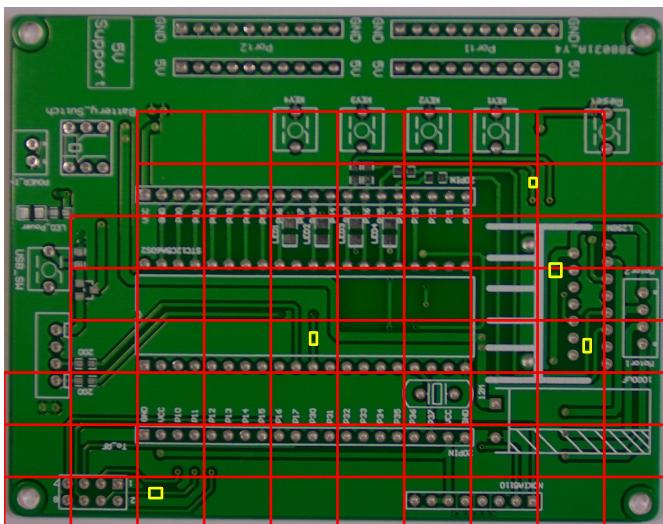
(b) MobileNetV2: Spur



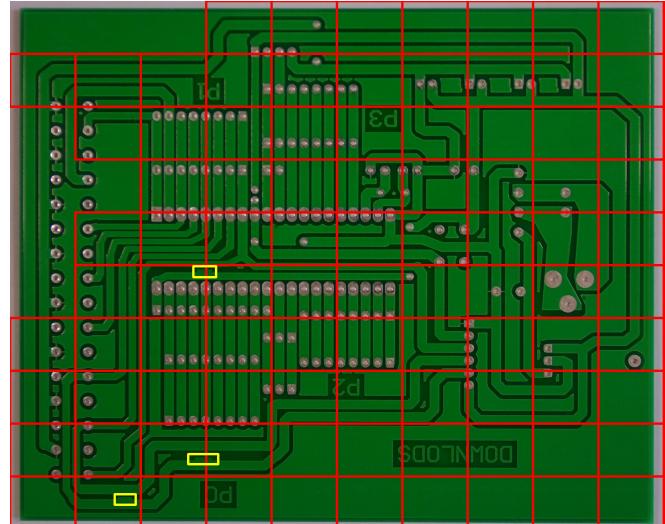
(c) EfficientNetV2B0: Open Circuit



(d) EfficientNetV2B0: Spur



(e) Proposed Model: Open Circuit



(f) Proposed Model: Spur

Figure A.3: Comparison of Pre-trained models vs Our Non pre-trained model