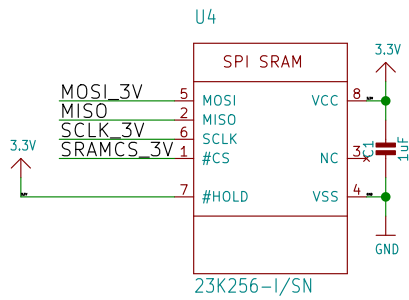
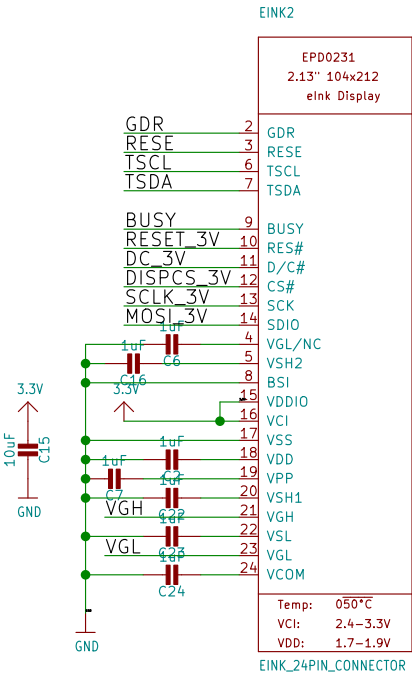
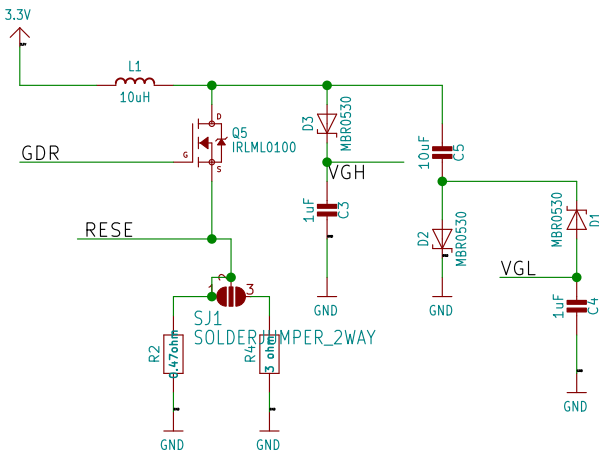
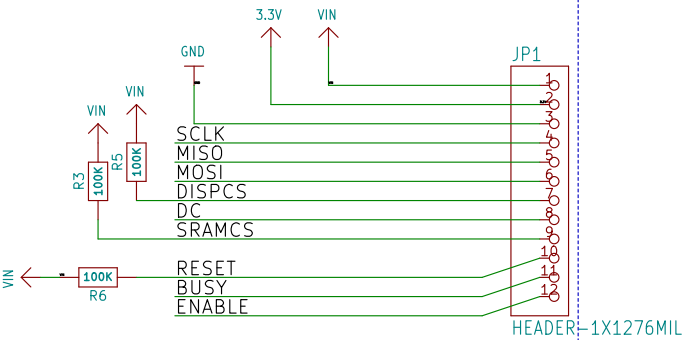


FRAMEBUFFER SRAM

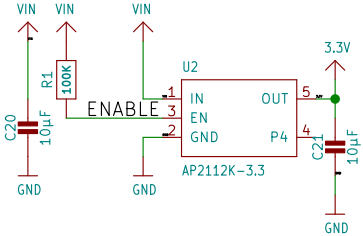
SPI SRAM can be used to store the screen data if the MCU doesn't have 6KB free SRAM



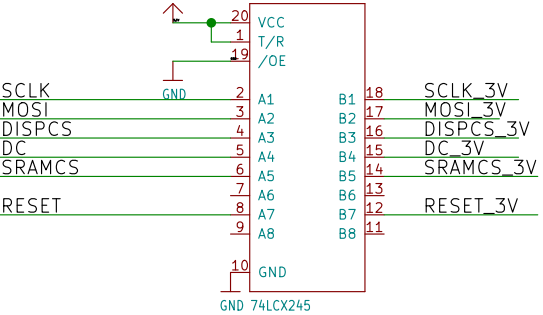
HEADER PINS



VOLTAGE REGULATOR



3/5V -> 3V LEVEL SHIFTING



ISSUE	>COMPANY		© >YEAR
DRAWN	TITLE		REV
>DRAWN	Pervasive Displays 2.0" Breakout		>REV
CHECKED	DATE		DRG №
>CHECKED	>LAST_DATE_TIME		>DRGNO
DATE	FILE: >DRAWING_NAME		PAGE: >SHEET
>DATE			