EDISON ENRIQUEZ C.

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SUMMARY

Electronics Engineer with a Master's degree and Ph.D. candidacy in Electronic Engineering, with a strong academic background and university-level teaching experience since 2014, specializing in the instruction of electronic design tools, electronic devices, digital systems, automatic control systems, and electrical/electronic circuits. Dedicated to the academic development of students by promoting practical, problem-solving-based learning. Proven ability to mentor groups of 10 to 15 undergraduate students in laboratory settings, fostering a deep understanding of technical concepts and the application of relevant technologies. Passionate about teaching, with strong communication skills and a proactive approach to educational innovation.

EDUCATION

Ph.D.(c), Electronic and Electrical Engineering

Graduating

Universidad del Valle, Cali, Co

School of Electrical and Electronic Engineering

Relevant coursework: Demand-side Management on Smart Grids , Local Energy Markets , Transactive Energy , Energy Internet , Reinforcement Learning , Smart Contracts and Blockchain , Tokenization and secure management and storage of digital assets , Automated Market Maker

M.S.C., Electronic Engineering

Graduating Dic 2018

Universidad del Valle, Cali, Co

School of Electrical and Electronic Engineering

Relevant coursework: Hardware Design Languages and Programmable Logic, Computer architecture, Dynamic partial reconfiguration in FPGAs, Web APP with Ruby on Rails and Redis

B.S.E., Electronic Engineering

Graduating Jun 2014

Universidad del Valle, Cali, Co

School of Electrical and Electronic Engineering

Relevant coursework: Hardware Design Languages and Programmable Logic, Image processing, Artificial Neural networks

TECHNICAL SKILLS

Data Analysis and Statistics: Python, Pandas, Numpy, SQL

Design and Modeling Tools: FUSION 360, LabVIEW, MATLAB, Microsoft Office, Latex **Programming:** Python, C/C++, Rust, Solidity, Java, HDL Lenguages (Verilog and VHDL)

 $\textbf{Certifications:} \ \ \textbf{Ethical Hacker (Sep 2023)} \ , \ \textbf{Junior Cybersecurity Analyst Career Path - CISCO (Sep 2023)} \ \ , \ \textbf{Google Constitutions:} \ \ \textbf{Constitutions:} \ \ \ \ \ \textbf{Constitutions:} \ \ \ \ \ \ \ \ \ \ \ \$

Cybersecurity Professional Certificate, Lectura y Escritura en el Aula Universitaria,

Languages: English (B1)

EXPERIENCE

Antonio Jose Camacho University institution, Cali, Co: Full time professor

Ago 2024 - Present

- Algorithm and Programming, C/C++, Arduino, Internet of Things (IoT)
- Cybersecurity, ISO 27001, NIST, ISO 31000, MITRE Framework, Risk Management, Ethical Hacking, Penetration Testing, Clean Code, Introduction to Cryptography,
- Micro-controller Systems, C++ Programming, Embedded Systems in C++, Design Petterns, Embedded Software Engineering, Assembler
- Member of the Committee for Designing a Network Technology and Cybersecurity Program
- Member of the Industrial Electronics Technology Accreditation Committee

Antonio Jose Camacho University institution, Cali, Co: Adjunct Professor (12 hours/week) Feb 2023 – Jul 2024

- · Java, Object-Oriented Programming (OOP), design patterns, Clean Code
- Algorithm and Programming, C/C++, RAII, Arduino, Internet of Things (IoT)
- Cybersecurity, ISO 27001, NIST, ISO 31000, MITRE Framework, Risk Management, Ethical Hacking, Penetration Testing, Clean Code, Introduction to Cryptography,
- Electronic Communications, RF Communication, Networking,
- Micro-controller Systems, C++ Programming, Embedded Systems in C++, Design Petterns, Embedded Software Engineering, Assembler

Universidad del Valle, Cali, Co: Adjunct Professor (7 hours/week)

Aug 2017 - Present

- Tutored 10-15 Undergraduate engineering students in design tools for electronic technology
- Tutored 10-15 Undergraduate engineering students in electronics devices

Universidad del Valle, Cali, Co: Tutor (6 hours/week)

Aug 2014 - May 2016

Tutored 10-15 Undergraduate engineering students in Digital Systems Laboratories, Laboratory of automatic control systems and Electronic/Electrical Circuits Laboratory

ACADEMIC PROJECTS

Development of a platform for the implementation of active learning didactics in electronic engineering based on the CDIO initiative Sep 2016 - Mar 2017

Research Assistant

 A remote experimentation laboratory was designed and implemented as a workspace within the framework of the CDIO initiative

Training module Based on FPGA with Embedded Logic Analyzer

Sep 2011 - Mar 2013

Research Assistant

- System configuration via USB developed in Java with libusb library from the server (Java, Libusb)
- Development of an embedded system based on FPGA and Microcontroller (Microchip Microcontroller , Xilinx FPGA , FreeRTOS)

OTHER WORK EXPERIENCE

Madrigral Industries, Pasto, CO: Development engineer (6 hours/week)

Jan 2019 - Jun 2019

- Development of the control system for the different automatic packaging machines based on PLC and arduino systems
- Replacement and adaptation of the control and communication system for an imported automatic packaging machine

Remington University Corporation, Cali, CO: Adjunct Instructor (6 hours/week)

Nov 2022 – Dic 2022

Cybersecurity Seminar - Pentesting

ACHIEVEMENTS

Scholarships (Univalle)

Aug 2014 - Dic 2016

Scholarship for Master and Doctorate studies

Academic incentives (Univalle)

Recognition that the Universidad del Valle Universidad del Valle gives each semester to the best averages

- First place in the seventh semester
- · Fourth place in the tenth semester