

The diagram shows a voltage divider circuit. A +5V supply is connected to a series combination of capacitors C4 through C10, which are all labeled as 100nF. The output of this network is connected to the VOUT pin of a component labeled U2 LD1117D33CTR. This component also has pins for GND, VIN, and another VOUT. The output of U2 is connected to a load consisting of a blue LED (LED1) in series with a resistor R12 (2.2K). The LED's anode is towards the positive side of the supply.

U5
STM32F103CBT6

1 VBAT
2 PC13-TAMPER-RTC
3 PC14-OSC32_IN
4 PC15-OSC32_OUT
5 PDO-OSC_IN
6 PD1-OSC_OUT
7 NRST
8 VSS_A
9 VDD_A
10 PA0_WKUP
11 PA1_USART2_RTS
12 PA2_USART2_TX
13 PA3_USART2_RX
14 PA4_SPI1_NSS
15 PA5_SPI1_SCK
16 PA6_SPI1_MISO
17 PA7_SPI1_MOSI
18 PB0_ADC12_IN8
19 PB1_ADC12_IN9
20 PB2_BOOT1
21 PB10I2C2_SCL
22 PB11I2C2_SDA
23 VSS_1
24 VDD_1

25 VDD_3
26 VSS_3
27 PB9TIM4_CH4
28 PB8TIM4_CH3
29 BOOT0
30 PB7I2C1_SD
31 PB6I2C1_SCL
32 PB5I2C1_SMB
33 PB4NJTRST
34 PB3JTDO
35 PA15JTDI
36 PA14JTCK
37 VDD_2
38 VSS_2
39 PA13JTMSW
40 PA12USART1_RT
41 PA11USART1_CT
42 PA10USART1_RX
43 PA9USART1_TX
44 PA8USART1_CK
45 PB15SPI2_MOSI
46 PB14SPI2_MISO
47 PB13SPI2_SCK
48 PB12SPI2_NSS

SWCLK
VCC
GND
SWDIO
PA12
PA11
Vid_Sel
SPI_DAT
SPI_CLK
SPI_SelA