| Addition and Multiplication | | |
|---------------------------------------------------------------------------|----------------------------------|--------------------|
| 1st 2nd and or 0 0 | | |
| 0 1 0 1 | | |
| 1 0 0 1 | | |
| | | |
| Complement | | Sign bit extension |
| 1's Complement of -8: 1111 0111 2's Complement of -8: 1111 1000 | | 011 3 |
| Managina manadiya ya i | changed | 0+0 2 |
| 1's Complement of 8: 0000 0111 2's Complement of 8: 0000 000 | ' | 000 |
| 1's Complement of -36: 1101 1011 | | 111 -1 |
| 2's Complement of -36: 101 1100 | | 101 -3 |
| 1's Complement of 36: 0010 0011 2's Complement of 36: 0010 000 | | |
| _ | | |
| the most significant bits to the next bit of least significant bit set re | verses turns out the negative of | IES volue. |
| 0100 + 0100 = 1000 overflow | | |
| 1 1 1 | | |
| [→ 010 7→ 01 × = 1000 | | |
| X 1011 4 multiplicand can be regarding. | | |
| [[1]] [] [] [] [] [] [] [] [] [] [] [] [] [| | |
| 11011101 | | |
| X + 1001 a multiplier with sign bit set 00000101 | | |
| 000000 11011 | | |
| 11011101 | | |
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