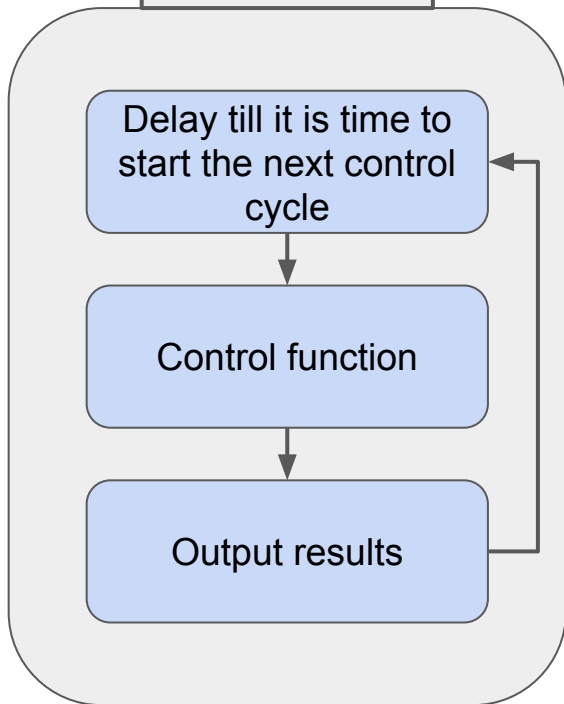


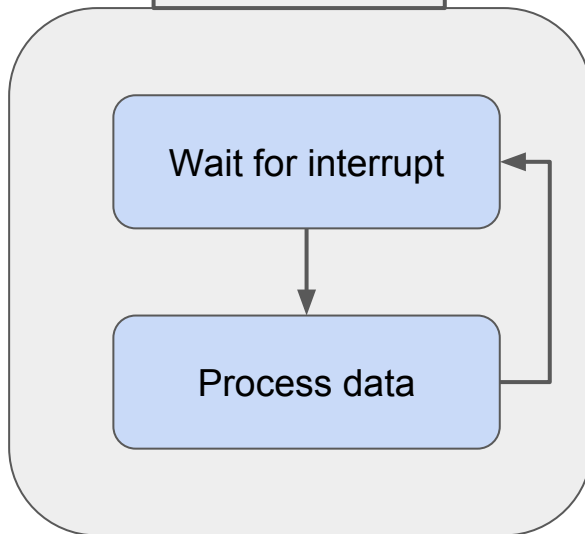
# Introduction to embedded programming on STM32

*RTOS. Interprocess communication (IPC)*

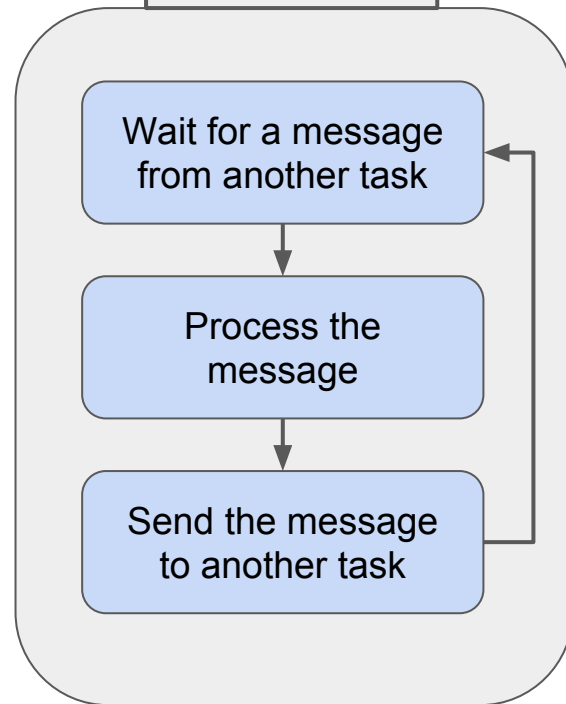
**Task #1**



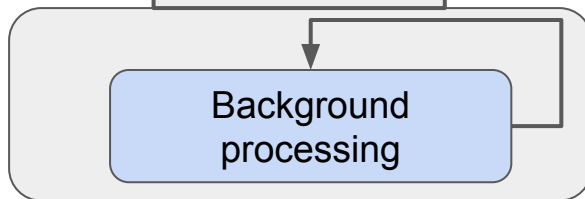
**Task #2**



**Task #3**



**Task #4**



# FreeRTOS. Interprocess communication

- Task Notifier
- Mutexes
- Queues

# FreeRTOS. Mutexes

- Multitasking systems are errors prone
- All running tasks might be preempted at anytime
- Resources might be left in inconsistent states

## Example #1

1. Task 1 is executed which in turn starts to write string “Hello world” to the LCD
2. Task 1 is preempted by Task 2 after outputting just the initial part of the string - “Hell”
3. Task2 writes “Skoltech” to the display and enters blocked state
4. Task1 continues from the point it was preempted and outputs the remaining part of the phrase - “o world”
5. Now, LCD shows “HellSkoltecho world”

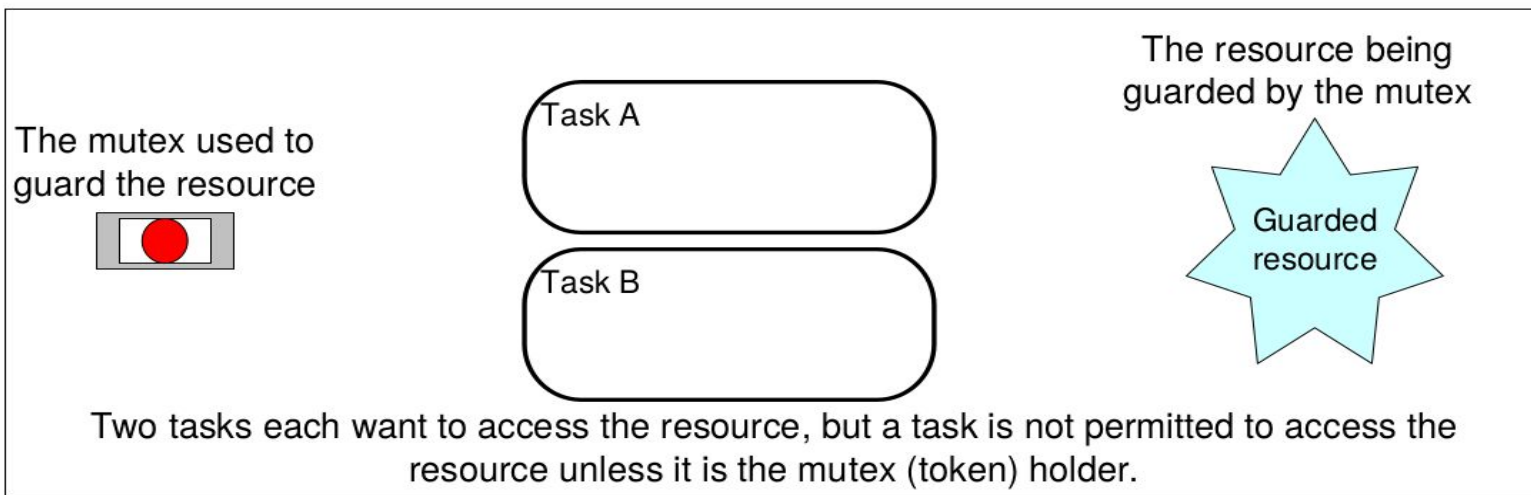
# FreeRTOS. Mutexes

**Example #2 (RMW operations)** - to modify output state of the whole port, we read current state (R), modify some of bits (M) and store result (W).

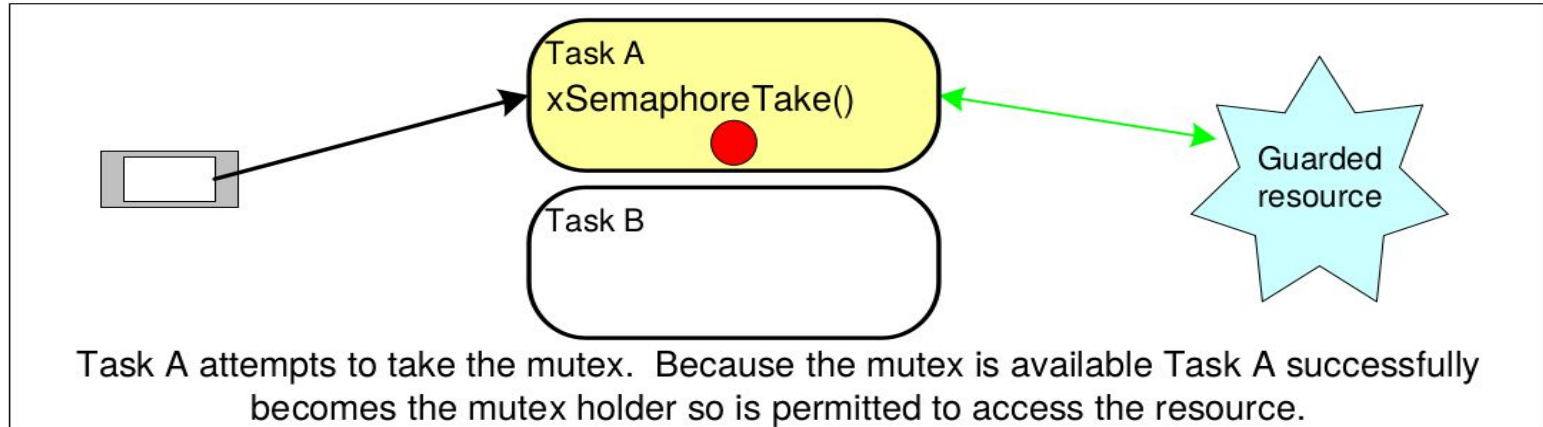
1. Task #1 loads the value of GPIOA->ODR into a reg
2. Task #1 is preempted by Task #2 before it completes the modify - write portions
3. Task #2 changes the value of GPIOA->ODR and goes to Blocked state
4. Task #1 modify the value it already holds in a reg and writes an outdated value to GPIOA->ODR

**Access is not atomic!**

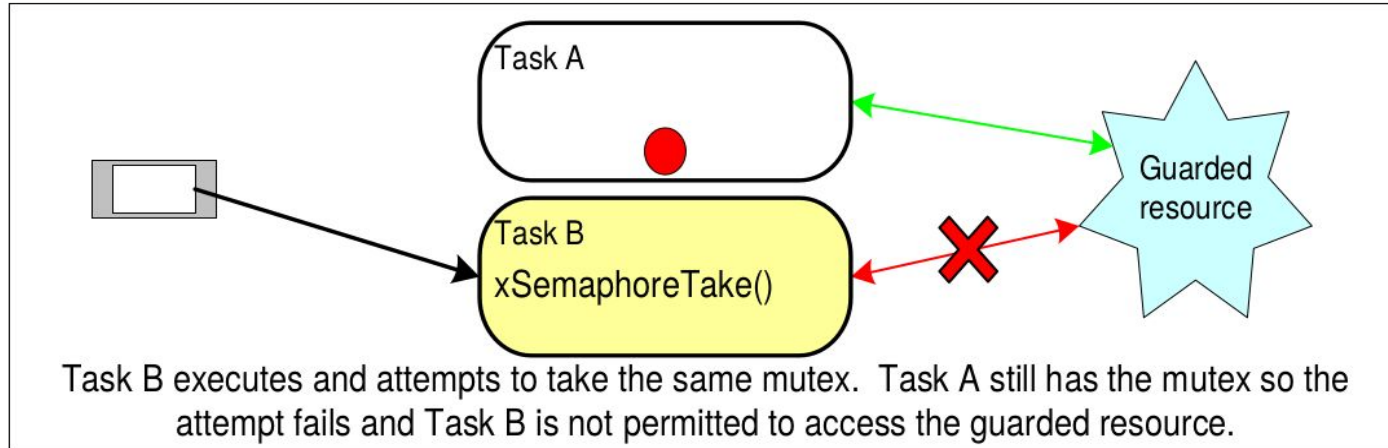
# FreeRTOS. Mutexes



# FreeRTOS. Mutexes

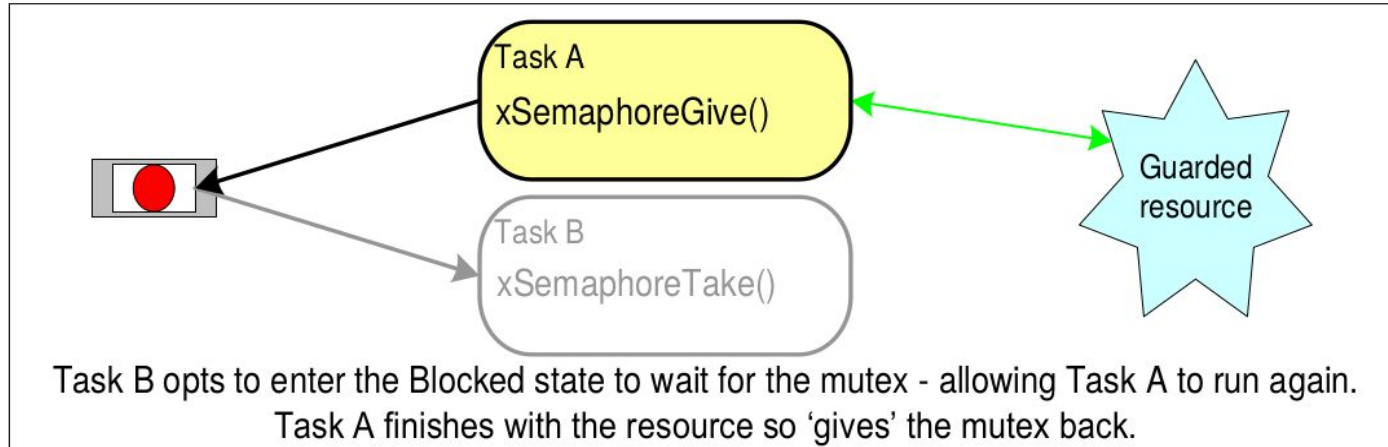


# FreeRTOS. Mutexes

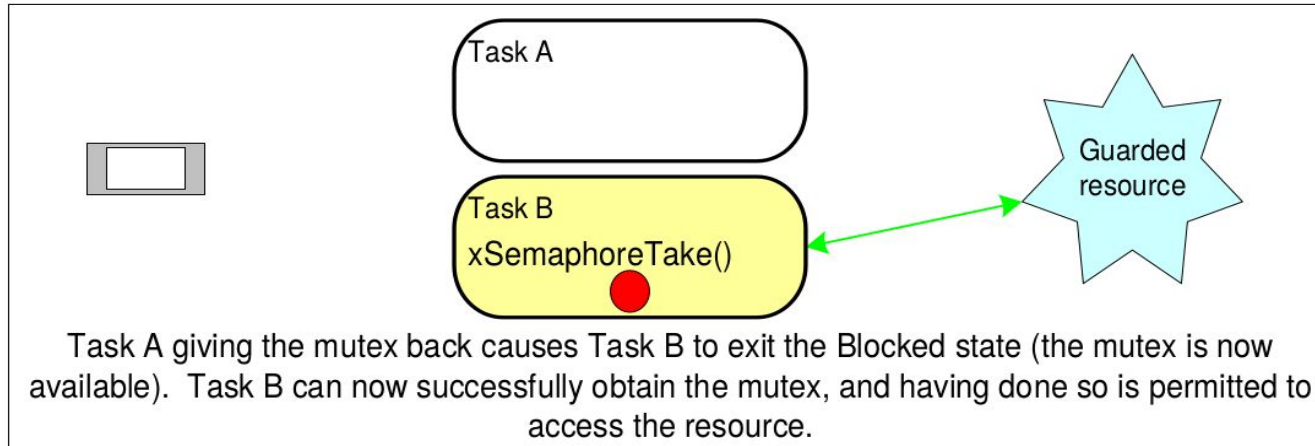




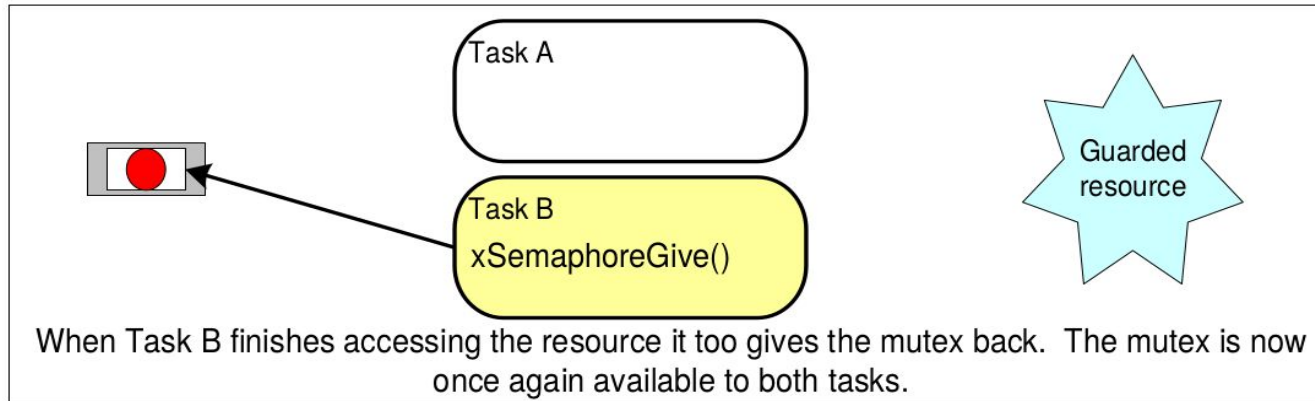
# FreeRTOS. Mutexes



# FreeRTOS. Mutexes



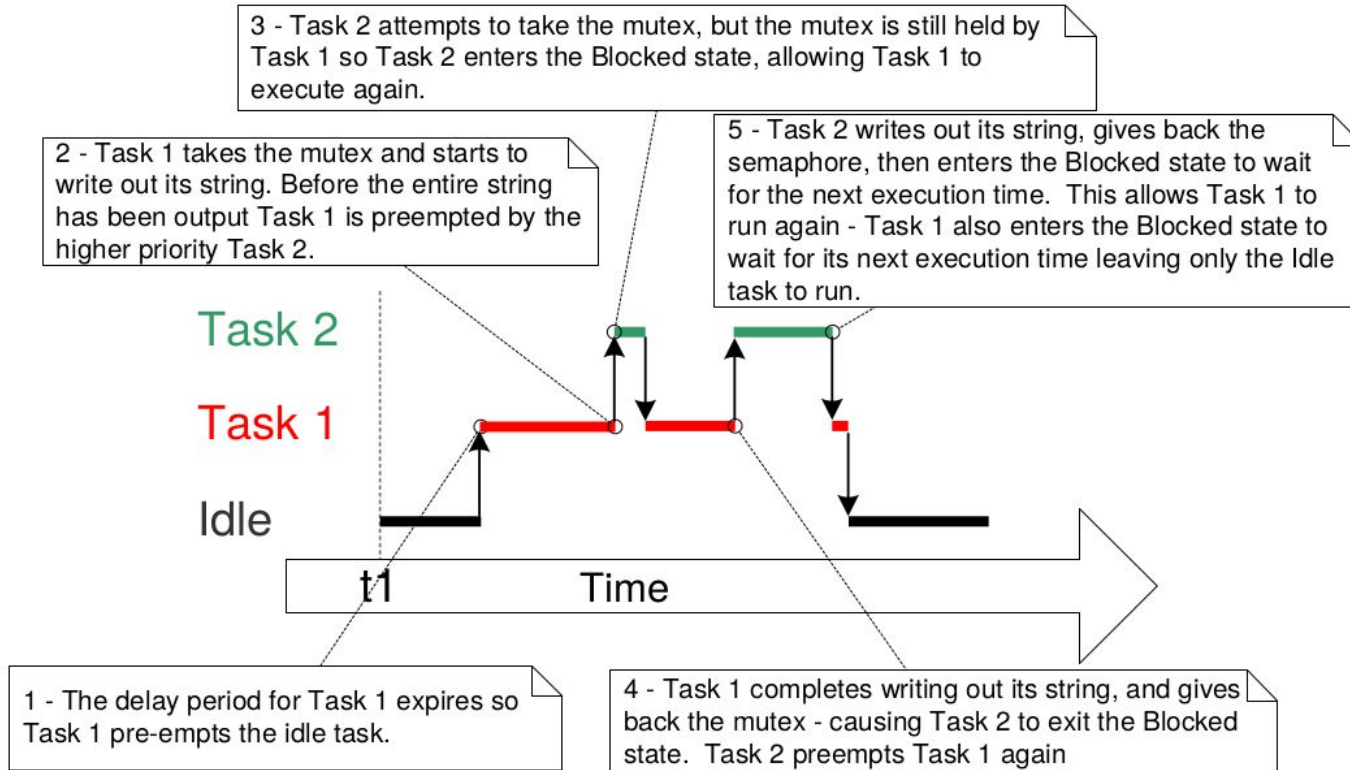
# FreeRTOS. Mutexes



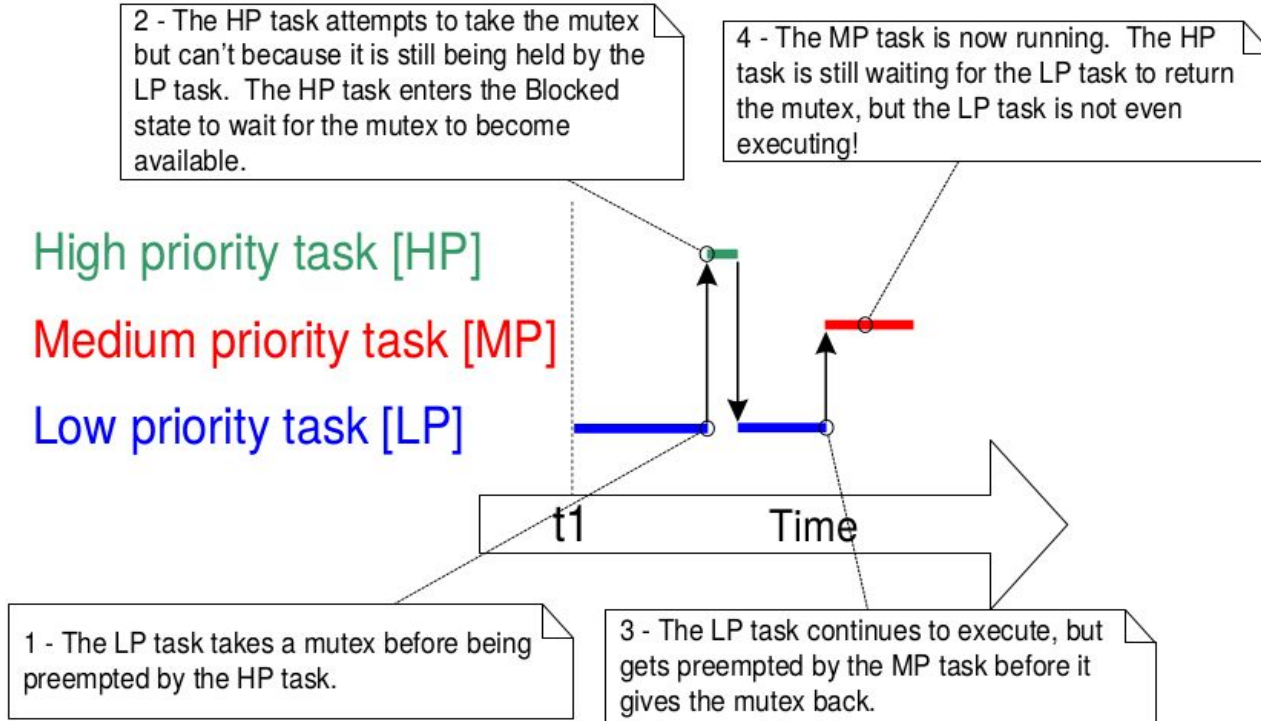
# FreeRTOS. Mutexes. API

- `SemaphoreHandle_t xSemaphoreCreateMutex(void)`
- `xSemaphoreGive(SemaphoreHandle_t xMutex)`
- `xSemaphoreTake(SemaphoreHandle_t xMutex, uint32_t delay)`

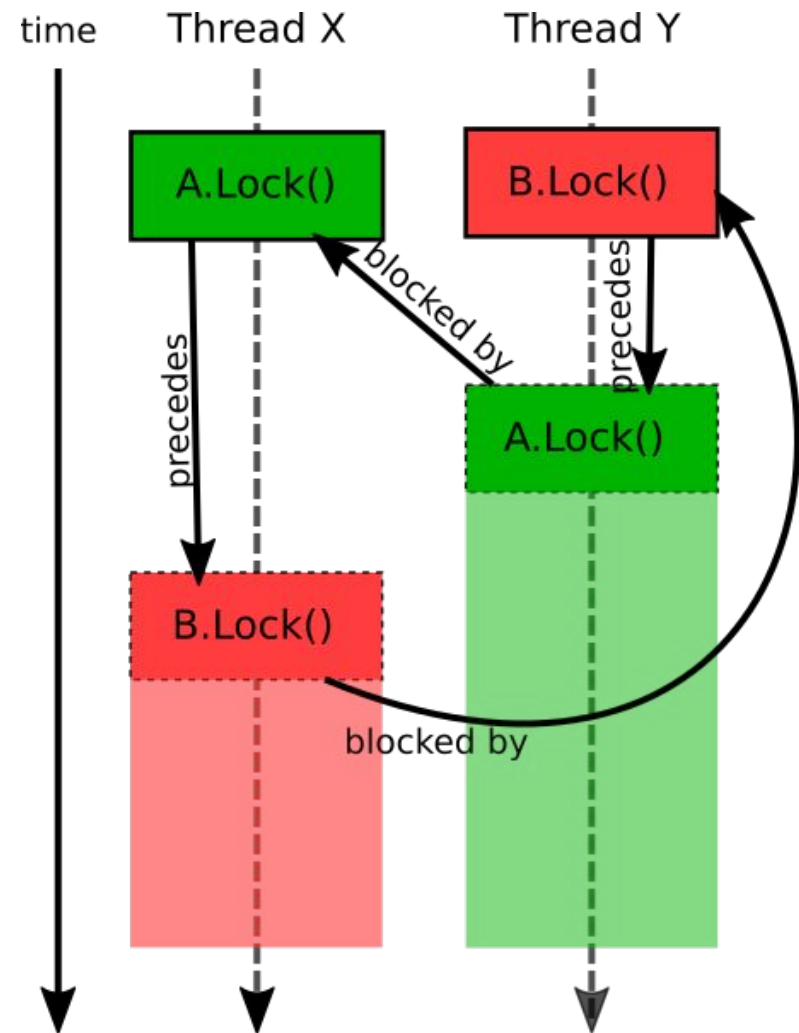
# FreeRTOS. Mutexes. Fixed example with LCD



# FreeRTOS. Mutexes. Priority Inversion



# FreeRTOS. Mutexes. Deadlock



# FreeRTOS. Task Notifier

- Allows to communicate with other tasks, or synchronize with ISRs, w/o the need for a separate communication object
- Lightweight compared to any other IPC
- Less use of RAM
- But it cannot be used in the following scenario:
  - Sending event to an ISR
  - More than one receiving task
  - Buffering several data items



# FreeRTOS. Task Notifier

```
void vTask1( void *pvParam )
{
    for( ;; )
    {
        /* Write function code
        here. */
        ....

        /* At some point vTask1
        sends an event to
        vTask2 using a direct to
        task notification.*/
        ASendFunction();
    }
}
```

This time there is no  
communication  
object in the middle

```
void vTask2( void *pvParam )
{
    for( ;; )
    {
        /* Write function code
        here. */
        ....

        /* At some point vTask2
        receives a direct
        notification from vTask1
        */
        AReceiveFunction();
    }
}
```



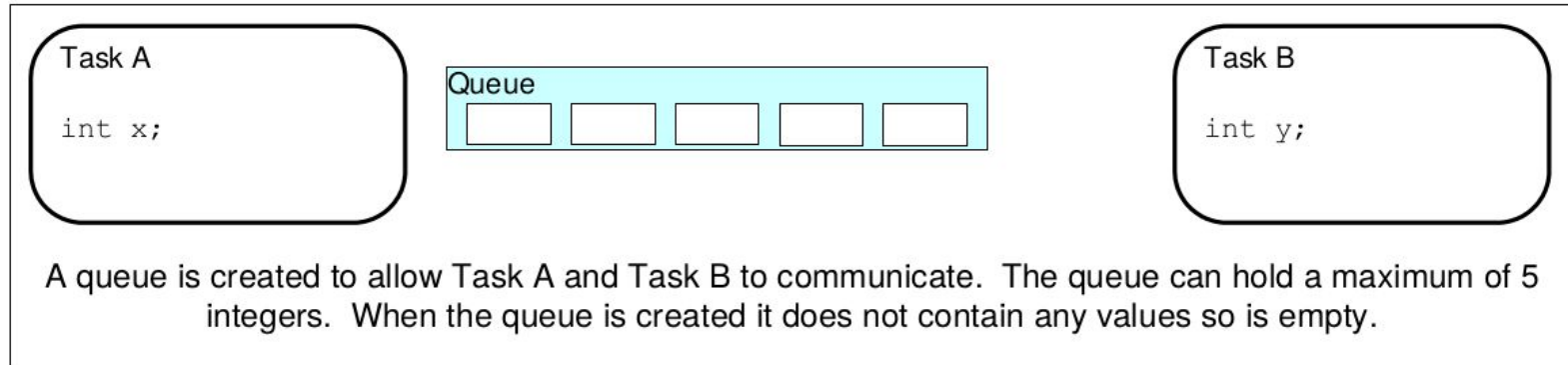
# FreeRTOS. Task Notifier. API

- `TaskHandle_t xTaskGetCurrentTaskHandle()`
- `xTaskNotifyGive(TaskHandle_t task_handler)`
- `ulTaskNotifyTake(BaseType_t xClearCountOnExit, TickType_t xTicksToWait)`
- `vTaskNotifyGiveFromISR(TaskHandle_t xTaskToNotify, BaseType_t *pxHigherPriorityTaskWoken)`

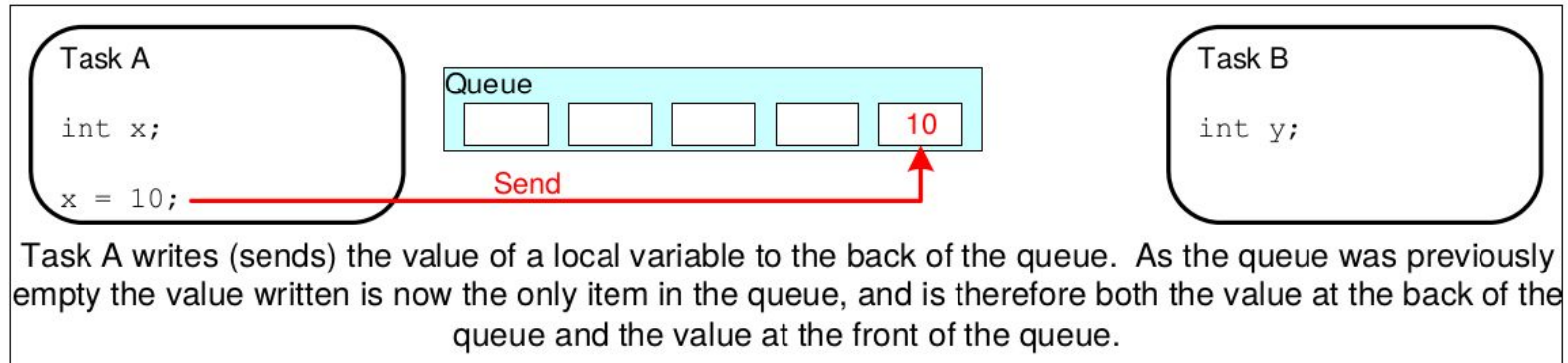
# FreeRTOS. Queue

- Hold a finite number of fixed size data items (max number is called length of queue)
- Queues are used as FIFO
- Can be task-to-task, task-to-interrupt, interrupt-to-task
- Can block the current task

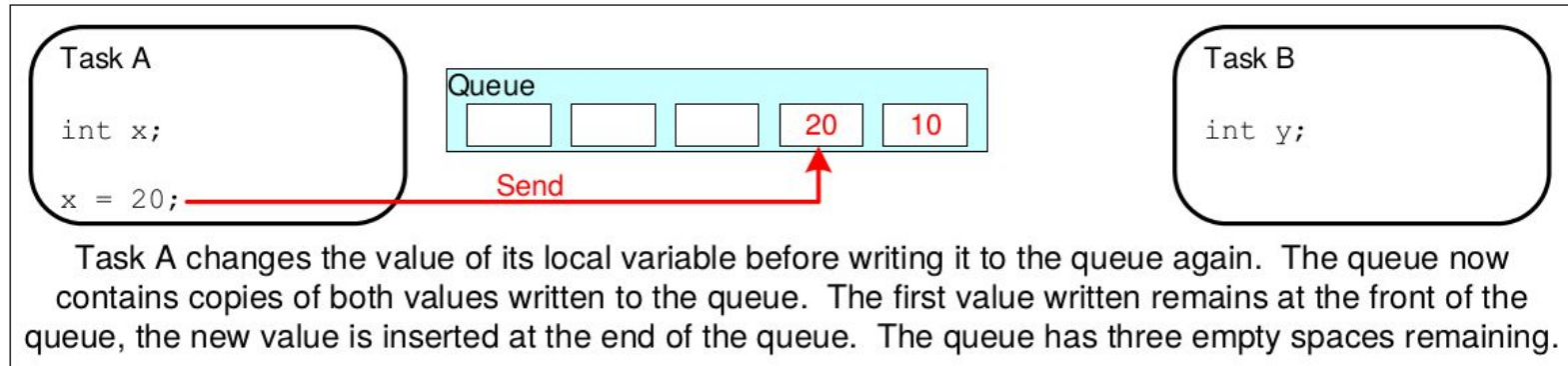
# FreeRTOS. Queues



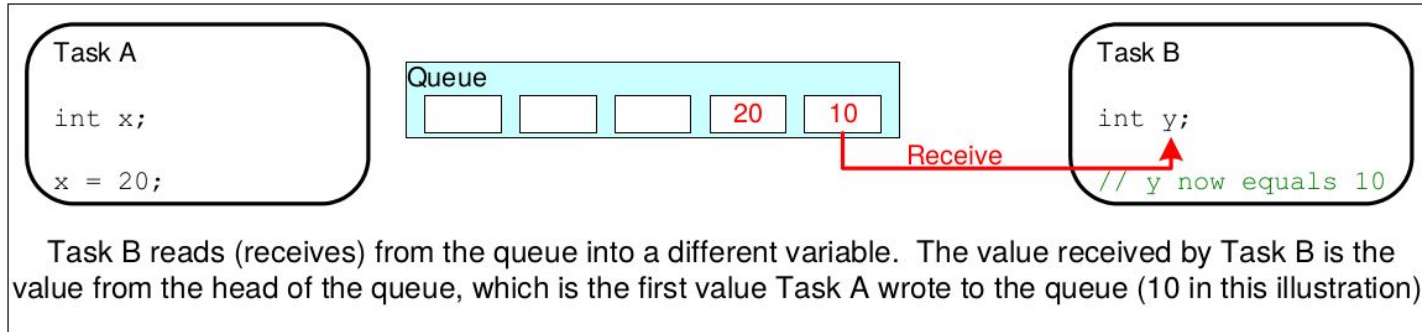
# FreeRTOS. Queues



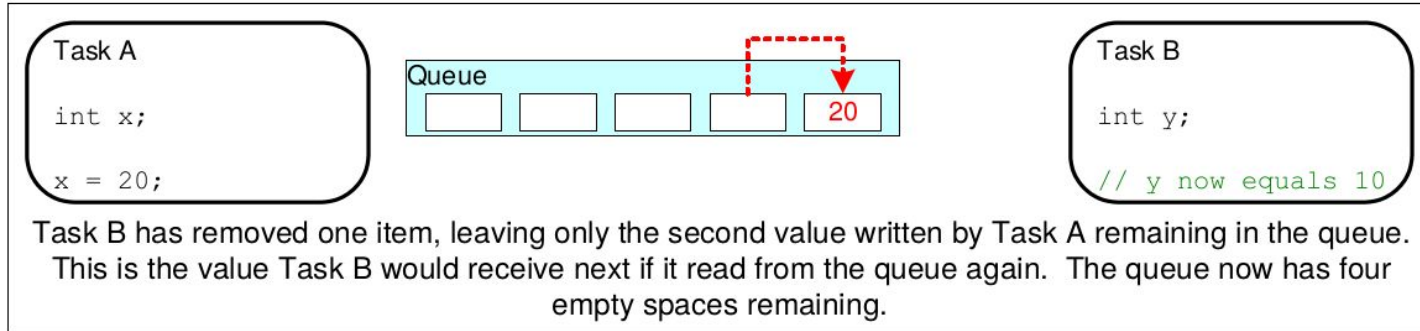
# FreeRTOS. Queues



# FreeRTOS. Queues



# FreeRTOS. Queues





# FreeRTOS. Queues. Copying policy

- **Queue by copy (FreeRTOS)**
- Queue by reference

# FreeRTOS. Queues. Features

- Can be accessed by multiple tasks
- Blocks on queue reads
- Might be blocked on write

# FreeRTOS. Queues. API

- `xQueueCreate()`
- `xQueueSendToBack()`
- `xQueueSendToBackFromISR()`
- `xQueueSendToFront()`
- `xQueueSendToFrontFromISR()`
- `xQueueReceive()` (received object is removed from queue)
- `xQueueReceiveFromISR()`

# More detailed doc

[https://www.freertos.org/Documentation/161204\\_Mastering\\_the\\_FreeRTOS\\_Real\\_Time\\_Kernel-A\\_Hands-On\\_Tutorial\\_Guide.pdf](https://www.freertos.org/Documentation/161204_Mastering_the_FreeRTOS_Real_Time_Kernel-A_Hands-On_Tutorial_Guide.pdf)