

# EE462 & EE464 Project: Design of a SM-PMSM Variable Frequency Drive with Matlab/Simulink

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The model that you will build and your results should be **completely on your own**.

## Definition

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Please see the motor ratings of the surface mount PM synchronous machine (SM-PMSM) below.

$$P_{nominal} = 400 \text{ kW}$$

$$n_{nominal} = 1500 \text{ rpm (rated)}$$

$$n_{max} = 2250 \text{ rpm}$$

$$\text{Pole number: } p = 4$$

$$\text{Flux linkage: } \lambda_{PM} = 0.5 \text{ Vs (Wb-t)}$$

$$L_d = L_q = 350 \text{ }\mu\text{H}$$

$$I_{nominal} = 1700 \text{ A (peak)}$$

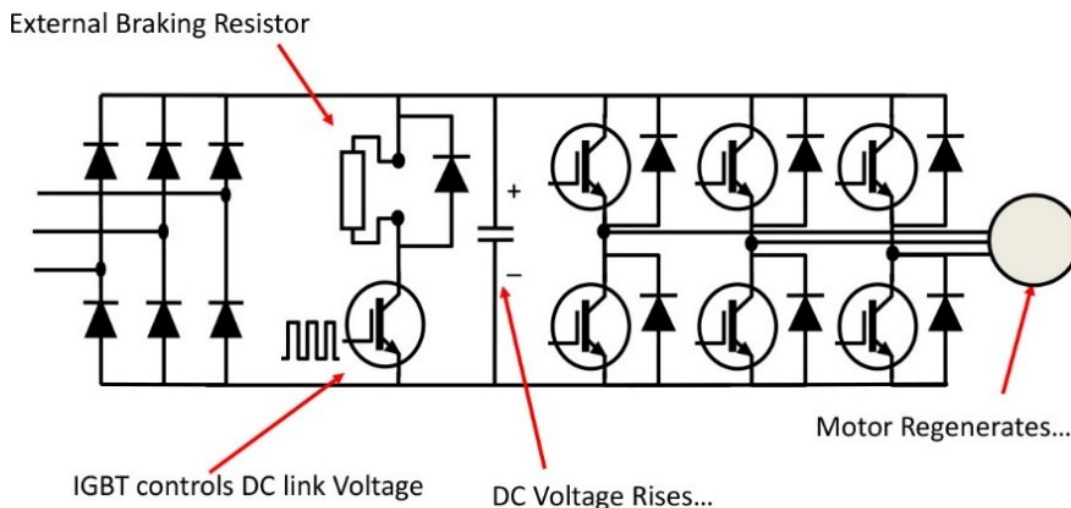
$$\text{Phase resistance } R_s = 16 \text{ m}\Omega$$

$$\text{Inertia of the rotor: } J_{rotor} = 10 \text{ kgm}^2$$

Ignore core losses and mechanical losses such as windage and friction losses.

The available supply is a three-phase AC source (50 Hz, 400V<sub>L-L</sub>) and the PM is a surface-mount motor.

Assume a 3-phase full-bridge diode rectifier is connected to the grid. The 3-phase motor drive inverter is connected to the diode rectifier as shown below.



## Part A: Pre-design Stage

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1. Calculate the rated torque the PMSM.
2. Calculate the maximum applied electrical frequency and choose a switching frequency for your inverter. You can refer to [1].
3. Design a reasonable DC-link filter for the rectifier block. Plot the DC-link voltage waveform by connecting a resistive load equivalent to motor at rated current. Calculate the DC-link voltage.

## Part B: Sinusoidal PWM

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Implement a motor drive using sinusoidal PWM (Sine-PWM), and implement a cascaded speed and current controller using  $i_d$ - $i_q$  parameters. You need to implement your OWN Clarke-Park transformation blocks and PWM generation block; however, you can use built-in models for the electrical machine. Also, include the machine inverse model in your control block diagram.

1. Assume a fan load with the given characteristics is connected to the shaft with no gear transmission (direct drive).

- Inertia of the load:  $J_{load} = 240 \text{ kg m}^2$
- $T_{load} = 0.09 \omega^2$

Simulate and plot the following waveforms during transition of 90% of the rated (nominal) speed to rated (nominal) speed:

- a. Plot speed vs. time
- b. Plot motor 3-phase line-to-line voltages and 3-phase line currents vs. time
- c. Plot torque vs. time
- d. Plot d-q currents
- e. What is the transition time under this condition?

Note: For a credit, make sure that the axes are visible with large fonts. Also, make sure that you pick a light color for the background of the plots, not black.

2. While the motor is running at rated speed with the fan load in previous question, assume the load is removed ( $T_L = 0 \text{ Nm}$ ) and the speed reference is kept constant. Show the performance of the drive by plotting relevant graphs.
3. While the motor is running at rated speed **at no-load**, assume the speed reference is reversed (i.e rotating at the opposite direction at rated speed). Comment on if this operation is feasible with the diode rectifier. Design a braking resistor system such that the DC-link voltage does not exceed 600 V.
  - a. Plot speed vs. time
  - b. Draw the 3-phase line currents during speed reversal and comment on the results.
  - c. Plot d-q currents
  - d. Plot DC-link voltage

4. While the motor is running at rated speed at half of the rated torque (assume a constant load torque and inertia of the load:  $J_{load} = 240 \text{ kg m}^2$ ), propose a method to run the motor at %150 of the rated speed without exceeding the rated currents. Calculate the required d-q currents at initial condition and at final condition. Apply these currents and obtain the following graphs.
  - a. Plot speed vs. time
  - b. Draw the 3-phase line currents during speed reversal and comment on the results.
  - c. Plot d-q currents

## Part C: Space Vector PWM (SV-PWM)

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1. Repeat Part B using a Space Vector PWM algorithm. You are free to use readily available Simulink blocks in this stage.
2. Plot the 3-phase reference voltage waveforms for the Sine-PWM and SV-PWM for rated operation and comment. You need to transform reference vector in SV-PWM to 3-phase reference frame.
3. Compare the FFT components of the line currents for Sine-PWM and SV-PWM. Comment on the frequency spectrum and amplitudes. Calculate and compare the THD of the line current at rated operating conditions.
4. Explain the main differences between Sine-PWM and SV-PWM. Which would you select for a high performance drive?

## Part D: About the project

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1. How much time did you spend to complete the project?

### NOTES:

- The aim of this project is not designing the most optimum or the most efficient motor drive system. Whenever you think the performance of the motor drive is not good, just comment on it and discuss how it can be improved.
- If you find unrealistic values (for example 10% efficiency), go back to your design. It's highly probable that you made a calculation mistake, but if it's really that low, then you need to modify your design.
- Comments are the most important section of your project report. Please explain both your models and results as detailed as possible.
- You will complete this project in pairs.

## HINTS:

- When constructing the models, go **step-by-step**. First build a few components, check if it is working as intended, if it works then add new components. Do not try to implement the whole model at once, expect it to work without any problems.
- Always be aware to use **correct units**.
- Understand the **requirements** before propositions.
- If you have any problems with your models, you can always [open an issue in GitHub](#) for online feedback, or e-mail the course assistant and lecturer.
- Do not forget to cite to any external sources you used.

## Project outputs

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You have to commit at least the following files until the deadline:

- The Simulink models and/or Matlab .m files
- A project report describing your models in detail (Your simulation results should be embedded in your report).
- Also, you are required to take a 10 min video that summaries your results and findings. You will upload your videos by the deadline.

## References

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- [1] R. Teichmann and S. Bernet, "A comparison of three-level converters versus two-level converters for low-voltage drives, traction, and utility applications," Industry Applications, IEEE Transactions on, vol. 41, no. 3, pp. 855 – 865, 2005.