

Node Name	Customize Column S...	Direction	Location	I/O Bank	VREF Group	Fitter Location	I/O Standard	Reserved	Current Strength	Slew Rate	Differential Pair	Strict Preservation
a[3]		Input	PIN_C12	7	B7_N0	PIN_C12	2.5 V		12mA (default)			
a[2]		Input	PIN_D12	7	B7_N0	PIN_D12	2.5 V		12mA (default)			
a[1]		Input	PIN_C11	7	B7_N0	PIN_C11	2.5 V		12mA (default)			
a[0]		Input	PIN_C10	7	B7_N0	PIN_C10	2.5 V		12mA (default)			
b[3]		Input	PIN_A14	7	B7_N0	PIN_A14	2.5 V		12mA (default)			
b[2]		Input	PIN_A13	7	B7_N0	PIN_A13	2.5 V		12mA (default)			
b[1]		Input	PIN_B12	7	B7_N0	PIN_B12	2.5 V		12mA (default)			
b[0]		Input	PIN_A12	7	B7_N0	PIN_A12	2.5 V		12mA (default)			
SSD0[7]		Output	PIN_D15	7	B7_N0	PIN_D15	2.5 V		12mA (default)	2 (default)		
SSD0[6]		Output	PIN_C17	7	B7_N0	PIN_C17	2.5 V		12mA (default)	2 (default)		
SSD0[5]		Output	PIN_D17	7	B7_N0	PIN_D17	2.5 V		12mA (default)	2 (default)		
SSD0[4]		Output	PIN_E16	7	B7_N0	PIN_E16	2.5 V		12mA (default)	2 (default)		
SSD0[3]		Output	PIN_C16	7	B7_N0	PIN_C16	2.5 V		12mA (default)	2 (default)		
SSD0[2]		Output	PIN_C15	7	B7_N0	PIN_C15	2.5 V		12mA (default)	2 (default)		
SSD0[1]		Output	PIN_E15	7	B7_N0	PIN_E15	2.5 V		12mA (default)	2 (default)		
SSD0[0]		Output	PIN_C14	7	B7_N0	PIN_C14	2.5 V		12mA (default)	2 (default)		
SSD1[7]		Output	PIN_A16	7	B7_N0	PIN_A16	2.5 V		12mA (default)	2 (default)		
SSD1[6]		Output	PIN_B17	7	B7_N0	PIN_B17	2.5 V		12mA (default)	2 (default)		
SSD1[5]		Output	PIN_A18	7	B7_N0	PIN_A18	2.5 V		12mA (default)	2 (default)		

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SSD1[4]		Output	PIN_A17	7	B7_N0	PIN_A17	2.5 V		12mA (default)	2 (default)		
SSD1[3]		Output	PIN_B16	7	B7_N0	PIN_B16	2.5 V		12mA (default)	2 (default)		
SSD1[2]		Output	PIN_E18	6	B6_N0	PIN_E18	2.5 V		12mA (default)	2 (default)		
SSD1[1]		Output	PIN_D18	6	B6_N0	PIN_D18	2.5 V		12mA (default)	2 (default)		
SSD1[0]		Output	PIN_C18	7	B7_N0	PIN_C18	2.5 V		12mA (default)	2 (default)		
<<new node>>												