

RESUME

LODAGALA SOWJANYA

Mobile: 6305100395

Email: sowjanya7575@gmail.com

Career objective

To work for an organization that provides me the opportunity to improve my skills and knowledge to growth along with the organization objective.

Profile Summary

- Highly motivated and leadership qualities
- Strong decision maker and hardworking
- Eager to learn new technologies and methodologies

Technical Skills

- Python programming language
- MS office, C

Achievements

- Certification in National Workshop on Analog and Digital Circuit Design using Cadence which was conducted by Gayatri Vidya Parishad Engineering College .
- Certification in National Workshop on Graphical System Design using LabVIEW which was conducted by Andhra University .
- Certification in the Seminar on Smart Sensor Systems conducted by IT Department Andhra University .

Academic Projects

Project 1:

Title: Noise Detection and Removal Filtering Techniques in Medical Images. Description: In this project, different medical images like MRI,Cancer,X-ray and brain are taken and standard deviation and mean are calculated after finding gaussian noise and to remove noise median

filtering technique is applied. After removing the noise standard deviation and mean are calculated. This experimental analysis will improve the medical images and helpful to analyse the symptoms of the patients.

Project 2:

Title: Simulation and Evaluation of High Performance Array Multiplier Using Modified Full Adder with Mach Zehnder Interferometer and Reversible Logic.

Description: the main objective of this project is to design and implementation of high performance array multiplier using modified full adder to optimize the circuit complexity and reduction of area and delay. To reduce the area and delay, different full adder techniques such as MZI full adder, reversible full adder are designed and implemented and among these MZI full adder is efficient and less delay. These functionalities are verified through simulation using Xilinx.

Education:

- M.Tech in stream of VLSI, ECE branch from Andhra University in the year 2017 with 81% .
- B.E, ECE from sankethika vidya parishad Engineering college affiliated with Andhra University in the year 2015 with 77.3%.
- Passed higher secondary from Board of Intermediate Education with 86% in the year 2011.
- Passed high school from Board of Secondary Education with 76% in the year 2009.