Mohammad Areeb

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EDUCATION

M. TECH | 2023 | Zakir Husain College of Engineering & Technology, AMU, Aligarh

Major: Electronic Circuits & System Design

CPI: 9.03

- Rank 2nd

B. TECH | 2021 | Zakir Husain College of Engineering & Technology, AMU, Aligarh

Major: Electronics Engineering

CPI: 8.15

DIPLOMA | 2017 | University Polytechnic, AMU, Aligarh

Major: Electronics Engineering

Score: 85.92%

- Rank 2nd

HIGH SCHOOL | 2014 | Our Lady of Fatima Higher Secondary School, Aligarh

CGPA: 7.8

SKILLS

- **HDL**: Verilog
- **HVL**: SystemVerilog
- **TB Methodology**: UVM
- **Programming Languages**: C++, Python
- **EDA Tools**: Mentor Graphics Questa, Xilinx Vivado
- **Knowledge**: Constraint Random Verification, ASIC Design Flow, System on Chip (SoC)
- **Application Tools**: MS Word, MS Powerpoint, MS Excel

PROJECTS

- **DECRYPTION OF AES-128 AND VERIFICATION OF ALGORITHM USING SYSTEM VERILOG AND UVM | September 2022 – Present**

- **DESIGN AND IMPLEMENTATION OF 128-bit ADVANCED ENCRYPTION STANDARD (AES) ALGORITHM | April 2022 – July 2022**

- **LANE DETECTION USING COMPUTER VISION | August 2020 – April 2021**

- **ALEXA BASED SMART HOME MONITORING | January 2020 – May 2020**

I hereby declare that the information given here is correct to the best of my knowledge.