**GUDEM MOUNIKA**

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**Career Objective:**

To apply my knowledge and 23 months of my experience in PCB designing to develop efficient and effective systems using my skills in CAM 350, Cadence Allegro V17.4, and Valor in meeting company’s needs.

**Professional Experience:**

● Having experience of 1 year 10 months in PCB design.

**Summary Professional Qualifications:**

* High Speed Digital and mixed signal PCB design.
* Knowledge in library creation
* PCB design techniques for EMI/EMC Compliance.
* Knowledge on DFM/DFA.
* Gerber generation and validation.
* Manufacturing checks in CAM350, Valor

**Technical Skills:**

**PCB Design:** Cadence Allegro 17.4

**Basic DFM checks**: CAM350, Valor

**Professional Experience Details:**

**Company**: Micron technology operations India.

**Duration**: May 2021 to March 2023.

**Designation**: Associate Engineer PCB design

**Projects:**

1. Project #1: Interposer board.

Tool used: Cadence Allegro 17.4

No of Layers: 6

Activities involved:

* Modification of Footprints
* PCB Layout and Routing length matching.
* Gerber Generation

Interfaces Present: FBGA, USB

1. Project #2: LPDDR5 Power measurement Interposer.

Boards: 2 Boards with different pin count

Tool used: Cadence Allegro 17.4

No of Layers: 8

Activities involved:

* PCB Layout and Routing length matching
* Gerber Generation.

1. Project #3: Name: Bring UP Boards

Boards: 2 Bub Boards with different FBGA pin

Tool used: Cadence Allegro 17.4

No of Layers: 10

Activities involved:

* PCB Layout and Routing
* Gerber Generation

Interfaces Present: EDGE connector with 262 pin & FBGA

1. Project #4: Name: Solderability test board.

Tool used: Cadence allegro 17.4.

No of Layers: 4

Activities involved:

* PCB Layout
* Gerber Generation

1. Project #5: Name: SOEDIMM

Tool used: Cadence allegro 17.4.

No of Layers: 10

Activities involved:

* PCB Layout and Routing
* Gerber Generation

Interfaces Present: EDGE connector & DDR5.PMIC HUB

**Professional Strengths:**

● Quick learner

● Passion to learn new things.

● Good team worker

● Flexible at work

● Hardworking

**Academic Projects:**

**Mini Project:**

Project Title: Implementation of CRYPTOGRAPHY using PC with RF Platform: Embedded systems

Project Description**:**

This prototype project is developed for the security purpose means by using this the information can be sent in a secure manner which is more important and official in an unreadable manner to others. It is mostly used in the army, defense, medical and in many industries.

**Major project:**

Project Title: Implementation of PIC In RCI

Platform: VHDL

Project Description:

The main theme of this project is that PIC acts as an overall system manager. During the project, we write the code for all the internal registers of the PIC so that it can accomplish its task of prioritizing the various interrupts and thereby increasing the efficiency of the processor. It is a hardware designed chip.

**Academic qualification:**

**Graduation** in Electronics & Communication in JBREC, JNTU Hyderabad. Percentage achieved: 77%

Year -2008 to 2012

**Declaration:**

I hereby declare that the particulars mentioned above are true to the best of my knowledge.

Place: Hyderabad

Date:

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