

**Vidiyala Bhavya Sai**

**Email :** **vidiyala.bhavya@gmail.com**

**Mobile :** **8712204358,7680921905**

**Career Objective:**

To obtain a creative and challenging position in an organization that gives me an opportunity for self improvement and leadership, while contributing to the symbolic growth of the organization with my technical, innovative and logical skills.

**Profile Summary:**

* An enthusiastic fresher with highly motivated and leadership skills having bachelors of engineering degree in Electronics and Communication Engineering.
* Expert in implementation of each step of project.
* Eager to learn new technologies and methodologies.
* Always willing to innovate the new things which can improve the existing technology.

**Personality Traits:**

* Highly motivated and eager to learn new things.
* Strong motivational and leadership skills.
* Ability to produce best result in pressure situation.
* Excellent communication skills in written and verbal both.
* Ability to work as individual as well as in group.

**Academic Qualifications:**

* Masters of Technology in VLSI from Jagruti Institute of Engg & Tech (persuing).
* Bachelor of Technology in ECE from SPR College of Enng & Tech with 66% in year 2013.
* Intermediate from Narayana Jr College with 65% in year 2009.
* SSC from Dilsukhnagar Public School with 86% in year 2007.

**Technical Qualifications:**

* MS Office.
* Computer Languages – C, Core Java, VHDL.
* Operating System – Windows XP, 7.

**Academic Project:**

Project Title : Built-In-Self Detection/Correctoin Architecture for Motion

Estimation Computing Arrays.

Software’s used : VHDL

Operating System : Windows 7

Role : developer

Team size : 3

Role undertaken : Team Leader

Summary -

This project proposes BISDC architecture for self-detection and self-correction of errors of PEs in an MECA. Based on the error detection correction concepts of bi residue codes. Performance evaluation reveals that the proposed BISDC architecture effectively achieves self-detection and self-correction capabilities with minimal area.

**Other Project:**

* 2 days project and presentation on BIO CHIPS new technology.
* Presentation on Lighting Control using LDR for agriculture use.

**Achievements:**

* Participation in college fest dance competition.
* Organizer of college technical & cultural fest.
* District 2nd award as Young Genius in Vishwabharati Foundation Examination.

**Certifications:**

* Seminar Presentation on Lighting Control using LDR in Vijay Krishna Institute of Engg & Tech.
* Paper Presentation on Lighting Contrl Using LDR in Aurora College of Engg & Tech, Bhongiri.

**Intrests:**

* Listining music.
* Participating in dance competitions.
* Travelling.
* Internetsurfing.
* Learning new things.

**Personal Details:**

Date of Birth : 25th October,1991.

Languages : Telugu, Hindi, English.

Gender : Female.

Address : 6-3-927/B, NGO’s Colony, Vanasthalipuram, Hyd-500070

**Declaration:**

I here by declare that all the information given above is correct and I will do best of my knowledge.

**Place:** Hyderabad

**V.Bhavya Sai**