**NANDHINI A**

19/10, K.R.K Complex,

Valluva nagar south,

Velayuthampalayam,

Karur (DT).

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🖂: [nandhiniashok95@gmail.com](mailto:nandhiniashok95@gmail.com)

**OBJECTIVE**

Seeking a challenging career, this provides an opportunity for developing application using evolving technologies and exposure to learn new exiting skills.

**EDUCATIONAL QUALIFICATION**

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **Course** | **Name of Institution** | **Board / University** | **Year of Completion** | **Per**  **centage** |
| B.E., (ECE) | SNS College of Engineering, Coimbatore. | Anna University | 2016 | 7.01% |
| HSC | Kongu Matric Higher Secondary School Velur. | State Board | 2012 | 84.5% |
| SSLC | TNPL Matric Higher Secondary School, Kagithapuram. | State Board | 2010 | 82.6% |

**Soft Skills**

* Tools and Technologies : Xilinx, Modelsim.
* Operating Systems : Windows xp /07/8/8.1
* Adobe Photoshop.
* Computer Hardware and Networking.

**Area of Interest**

* Digital Electronics, Computer Networks, Wireless Communication

**Project**

**Title :** Error Detection and Correction using Concatenation of Hamming and Trellis coded modulation.

**Tools used :** Xilinx, FPGA kit

**Description:** In this project using Decimal Matrix Code, it will detect and correct more number of errors, reducing redundant bits in the memory and implementing that code using Field Programmable Gate Array(FPGA).

**Hobbies**

* Browsing Internet
* Designing
* Troubleshooting PCs

**In-Plant Training**

* Five days In-plant Training at **Bharat Sanchar Nigam Limited Exchange** held at Hosur.
* One day Industrial visit at **Bharat Sanjar Nigam Limited,** Coimbatore.

**Presentations**

* Presented a National Level Conference organized by “**KARPAGAM COLLEGE OF ENGINEERING**, Coimbatore. Titled on FPGA Implementation of Error Correction Technique Based on DMC.
* Presented a Project National Level Technical Symposium organized by **TEJAA SHAKTHI INSTITUTE OF TECHNOLOGY FOR WOMENS COLLEGE**, Coimbatore (**Selected for Second prize)**

**Training Attended**

* Attended six days Hands on training Program **on “Embedded system using Microchip PIC Microcontroller”** From 16-12-2013 to 21-12-2013 conducted by Falcon Square Technology, Coimbatore.

**Workshop Attended**

* Attended the Workshop on “VLSI Design and Testing” in **18th International Symposium** held on 16th July 2014 at PSG College of Technology

**Personal Details**

**DOB :** 22nd May 1995

**Father’s Name :** K.Asokan

**Mother’s Name :** A.Kavitha

**Languages Known :** Tamil, English.

**Declaration:**

I hereby declare that the above mentioned details are true to my knowledge. I assure you, that I am selected for the job, I will work to the sincere satisfaction of my superior officers.

Place: Coimbatore **(A.NANDHINI)**