**SUKANYA SUSAN JUJJAVARAPU**

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**OBJECTIVE:**

Looking for a career which will enable me to accept challenges, provide my analytical & professional skills and which creates excitement in learning and improving my skills according to present situations.

**Professional Qualification**

* **~ 3 years of experience in International Voice Process**
* **~ 1 year of experience as Associate Engineer**

**EXPERIENCE:**

**04/2019- 25/01/2022**

I hold around ~3 years of Experience at Amazon wherein I had to work on tax related issues for North American Customers, further I have supported several cross skills such as I worked on fraud accounts and unknown charges, and I have worked as a subject matter expert for new hires. Further, I have worked on German Translator tool- Rosetta. I have received Employee of the Month award and a Reliable player award which shows that I am committed to the work that is assigned to me.

**05/2022- Till Date**

I hold an experience of around ~1 year in Tech Mahindra wherein I have worked as a Design Verification Associate Engineer.

* Good understanding of the ASIC design flow
* Good experience in writing Test benches using System Verilog and UVM
* Very good knowledge in verification methodologies
* Experience in using industry standard EDA tools for the front-end design and verification

**EDUCATION QUALIFICATION:**

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **S.No** | **Name of College** | **Name of Board** | **Year of passing** | **% Secured** |
| 1 | Lakireddy Balireddy College of Engineering | JNTU-Kakinada  (ECE Stream) | 2018 | 7.24 CGPA |
| 2 | Sri Chaitanya Junior College | BIE | 2014 | 70% |
| 3 | Bhashyam High School | SSC | 2012 | 8.7 GPA |

**TECHNICAL SKILLS:**

* Languages : Basic in C , Python, SQL.
* ▪ Package : MS Office 2007/2010.
* ▪ Basic in Verilog VHDL design

**CO-CURRICULAR**

* Presented paper on in Technozian-16 conducted at National Institute Of Technology (NIIT) Warangal.
* Participated in workshop at LAKIREDDY BALIREDDY COLLEGE OF ENGINEERING on PCB
* designing
* Participated in the workshop on “Internet of things” conducted by Geeks lab Technologies Pvt. Ltd
* and IEEE student Branch held on 29 th and 30 th August, 2016.
* Participated in a CIRCUITRIX, ELECTROMANIA, QUIZ event of AFOSEC’16 organized by V.R
* Siddhartha Engineering college.

**ACADEMIC PROJECTS:**

* **MAIN PROJECT- Irrigation System Using Aurdino And IOT**. The project aims to aid the farmers regulating their farms as they may not be able to keep an eye on the farm and moisten round the clock, so this renders such feasibility from their own residences. This proposed system also provides ease to their daily life. This is made possible by using the sensor technology, IoT, Cloud Memory and GSM Module as a subsidiary in case the farmers fail to connect the servers

**DECLARATION:**

I hereby declare that the above-furnished information is true to the best of my knowledge.

**Place:**

**Date: (SUKANYA SUSAN J)**