**Vasavi Bussetty** Emai[l: bvasavi](mailto:karthickj1802@gmail.com)95@gmail.com

Senior Software Engineer Mobile: +91 9701760818

OBJECTIVE

To make use of my knowledge and technical skills in an efficient way which would contribute to achieving organizations goals faster and help in further development of the organizations.

**JOB PROFILE**

* Senior Software Engineer at VVDN Technologies, Chennai with total experience of 5.7 Years.
* **UEFI/BIOS firmware** Development for Celestica Projects which includes AMI Bios, Intel CPU with various Server Platforms.
* Roles involve Feature Enhancement, Debugging, Bug Fixing, Feature development, Documentation and Unit Testing.

**WORK EXPERIENCE**

## Experience 1 :

**Designation :** BIOS/UEFI Firmware Engineer

**Company Name :** UST Global Technologies India Pvt. Ltd

**Duration :** 28th February 2018 to 21st November 2020

**Domain :** BIOS/UEFI Pre/Post Silicon Validation(Manual & Automation)

## Experience 2 :

**Designation :** Senior Engineer

**Company Name :** Tech Mahendra Cerium Systems Pvt. Ltd

**Duration :** 23rd November 2020 11th June 2021

**Domain :** BIOS/UEFIPre/Post Silicon Validation(Manual)

## Experience 3 :

**Designation :** Senior Engineer

**Company Name :** Sasken Technologies Pvt. Ltd

**Duration :** 14th June 2021 to 12th August 2022

**Domain :** BIOS/UEFI Pre Silicon Validation(Manual & Automation)

## Experience 4 :

**Designation :** Senior Software Engineer

**Company Name :** VVDN Technologies Pvt. Ltd

**Duration :** 17th August 2022 to Present

**Domain :** BIOS/UEFI firmware development

**RESPONSIBILITIES**

* + BIOS Code Porting and development
  + Feature implementations and Testing
  + Debugging and Bug Fixing

**SOFTWARE PROFICIENCY**

**Operating system :** Windows, Linux

**Languages :** C **Standards/Specifications :** SMBIOS, IPMI, ACPI, BMC, I2c, SPI

**Interfaces :** Visual eBIOS, Visual Studio, Simics Software

**Flash Tools & Methodologies :** DediProg, AFU & CFU, BMC Web UI

**Other Tools :** R/W Utility, Putty, Teraterm, Git, Git Bash

**Debugging Tools :** ITP, Verbose Trace, Windbg, Movidebugger

**Bug Tracker Software :** Jira, HSD tool, Phoenix tool

**PROJECT EXPERIENCE**

# Project Experience 1 [2018 February – 2020 November]

• Bios/UEFI Firmware validation in post-silicon using windows and linux OS with diﬀerent platforms

• Automation testing on Bios/UEFI ﬁrmware Pre/Post silicon using

Scripts (python) with Frameworks I2C, SPI, CCMT

• Security features TPM, DTPM, TXT, Capsules, ACPI, SMBIOS, Boot guard, Bios guard, Setting Bitlockers, Memory Modules

• Triaging and debugging the issues using Teraterm tool and HSD tool

# Project Experience 2 [2020 November – 2021 June]

• Baseboard Management Controller (BMC), IPMI

• Power-on Execution with Server boards

• Installation of windows and Linux OS in server as well as desktop platforms

• Diﬀerent OS installation methods BMC,PXE,CD/DVD,USB

• Writing test procedures for test modules

• ACPI, SMBIOS, RAS, memory Modules, GSA tests, CPU

• Debugging the issues using Teraterm tool, Windbg tools

# Project Experience 3 [2021 June - 2022 August]

• Validating the Driver in Pre-Silicon using Linux Environment

• Drivers in Bios with diﬀerent kind of platforms and checking the compatibility with diﬀerent OS • Driver features with Automation Scripts using Python

• Triaging & Debugging the issues using with Windbg Tool, Movidebugger tool, Verbosetrace tool

• Worked on New and diﬀerent applications as per client requirements

• Bug tracking and test procedures using Phoenix tool

# Project Experience 4 [2022 August- Present]

• Bios Development for the CelesticaProjects with Intel Hewittlake platforms(Greystone)

• Project and source maintenance – Module updates, Porting the code and Feature

Enhancement.

• Bug Fixing – Debugging the issues reported by testing team.

• Validation - Smoke Test, Features Testing

• Bug tracking Using Jira and Test procedures

**EDUCATION QUALIFICATION**

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| --- | --- | --- | --- |
| **Examination** | **Year of Passing** | **College/University** | **Marks Obtained** |
| B.E – Electronics and Instrumentation Engineering | 2017 | Bapatla Enginnering College, Bapatla | 76% |
| 12th | 2013 | Sri Chaitanya College, Guntur | 82% |
| 10th | 2011 | Yadava High School, Guntur | 85% |

**PERSONAL DETAILS**

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| --- | --- |
| Gender | Female |
| DOB | 15-06-1996 |
| Marital Status | Single |
| Languages Known | English, Telugu & Tamil |
| Current Location | Chennai |