

OS HW #3

7.8

Because acquiring a semaphore may put the process to sleep while it is waiting for the semaphore to become available. Spinlocks are to only be held for short durations and a process that is sleeping may hold the spinlock for too long a period.

8.20

- (a) Increase Available: This could safely be changed without problems
- (b) Decrease Available: This could have an effect on the system and in reduce the possibility of deadlock as the safety of the system assumed there were a certain number of available resources.
- (c) Increase Max for one process: This could have an effect on the system and Introduce the possibility of deadlock.
- (d) Decrease the number of process: This could be allowed assuming that The system doesn't enter an unsafe state.
- (f) Decrease the number of process: This could safely be changed without problems.

8.27

• 8.27

	Allocation				Max				Need			
	A	B	C	D	A	B	C	D	A	B	C	D
P0	1	2	0	2	4	3	1	6	3	1	1	4
P1	0	1	1	2	2	4	2	4	2	3	1	2
P2	1	2	4	0	3	6	5	1	2	4	1	1
P3	1	2	0	1	2	6	2	3	1	4	2	2
P4	1	0	0	1	3	1	1	2	2	1	1	1

(a) Available (2, 2, 2, 3)

$$P4 \ (2, 1, 1, 1) \leq (2, 2, 2, 3) \Rightarrow (2, 2, 2, 3) + (1, 0, 0, 1) = (3, 2, 2, 3)$$

P0, P1, P2, P3, not satisfied, lead to unsafe.

(b) Available (4, 4, 1, 1)

$$P4 \ (2, 1, 1, 1) \leq (4, 4, 1, 1) \Rightarrow (4, 4, 1, 1) + (1, 0, 0, 1) = (5, 4, 1, 2)$$

$$P1 \ (2, 3, 1, 2) \leq (5, 4, 1, 2) \Rightarrow (5, 4, 1, 2) + (0, 1, 1, 2) = (5, 5, 2, 4)$$

$$P2 \ (2, 4, 1, 1) \leq (5, 5, 2, 4) \Rightarrow (5, 5, 2, 4) + (1, 2, 4, 0) = (6, 7, 6, 4)$$

$$P3 \ (1, 4, 2, 2) \leq (6, 7, 6, 4) \Rightarrow (6, 7, 6, 4) + (1, 2, 0, 1) = (7, 9, 6, 5)$$

$$P0 \ (3, 1, 1, 4) \leq (7, 9, 6, 5) \Rightarrow (7, 9, 6, 5) + (1, 2, 0, 2) = (8, 11, 6, 7)$$

P1, P2, P3, P4, P0 satisfied, lead to safe.

8.30

8.30

```
semaphore ok-to-cross = 1 ;  
do {  
    wait (ok-to-cross) ;  
    Critical Sec  
    signal (ok-to-cross) ;  
}
```

9.15

(a) External Fragmentation:

Contiguous Allocation with fixed-sized partition: **do not suffer** from external fragmentation.

Contiguous Allocation with variable-sized partition: **suffer** from external fragmentation.

Pure segmentation: **suffer** from external fragmentation.

Paging: **not suffer** from external fragmentation.

(b) Internal Fragmentation:

Contiguous Allocation with fixed-sized partition: **suffer** from external fragmentation.

Contiguous Allocation with variable-sized partition: **do not suffer** from external Fragmentation.

Pure segmentation: **do not suffer** from Internal fragmentation.

Paging: **suffer** from Internal fragmentation.

(C) Ability to share code across process.

Contiguous Allocation with fixed-sized partition: **no support** for code sharing across processes.

Contiguous Allocation with variable-sized partition: **no support** for code sharing across processes.

Pure segmentation: **support** for code sharing across processes. However, must be careful to make sure that processes do not mix code and data in the same segment.

Paging: **support** for code sharing across processes. However, must be careful to make sure that processes do not mix code and data in the same page.

9.24

a. Conventional, single-level page table:

In a conventional page table, each page entry corresponds to a page in memory.

Number of entries = Number of pages = Physical memory size / Page size

Number of entries = $(1 * 1024 * 1024 \text{ KB}) / (8 * 1024 \text{ bytes})$

Number of entries = 128, meaning there are 128 entries in the conventional, single-level page table.

b. Inverted page table:

In an inverted page table, each entry corresponds to a frame in memory.

Number of entries = Number of frames = Physical memory size / Page size

Number of entries = $(1 * 1024 * 1024 \text{ KB}) / (8 * 1024 \text{ bytes})$

Number of entries = 128, meaning there are also 128 entries in the inverted page table.

So, both a conventional, single-level page table and an inverted page table in this scenario would have 128 entries each.