RISC-V® SW Overlay Aug 25th 2021

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Agenda

- 1. Standard ported to FOSSi
- 2. LLVM changes are ready for review steps for llvm Phabricator
- 3. Next GNU patches
- 4. Round table q&a



Standard ported to FOSSi

- The Overlay standard was ported to <u>FOSSi</u>.
- The Standard is frozen as RC until toolchain submission is complete.
- RISCV repo
 - We keep the source code for the engine in this repo until toolchain submission is complete
 - Need to decide if the FW engine should be part of the toolchain libs or not
 - We keep the HLD and LLD in the repo
 - We still need the T&R to review and approve our changes for RISCV
 - Last milestone: should we move all to FOSSi



Next - GNU patches

- Binutils patches
 - o Being prepared for review



Round table

• Open issues



