

```

7  module Homework1 where
8
9  import Prelude hiding (Num)
10
11  {------ 1A -----}
12
13  data Cmd = Pen Mode
14           | MoveTo Pos Pos
15           | Def Name Pars Cmd
16           | Call Name Vals
17           | Seq [Cmd]
18           deriving Show
19
20  data Mode = Up | Down
21
22  data Pos = M Num | N Name
23
24  type Pars = [Name]
25
26  type Vals = [Num]
27
28  -- Terminal Symbol types
29  type Num = Int
30  type Name = String
31
32  -- Show implementations
33  instance Show Mode where
34      show Up    = "up"
35      show Down  = "down"
36
37  instance Show Pos where
38      show (M a) = show a
39
40  {------ 1B -----}
41  -- Vector macro
42  -- Assume the pen is down...
43  -- def vector (x1, y1, x2, y2)
44  --     pen up
45  --     moveto (x1, y1)
46  --     pen down
47  --     moveto (x2, y2)
48  --     pen up
49
50  . . . " . . . "

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50 vector = let "vector"
51         ["x1","y1","x2","y2"]
52         (Seq [Pen Up,
53              MoveTo (N "x1") (N "y1"),
54              Pen Down,
55              MoveTo (N "x2") (N "y2"),
56              Pen Up])
57
58 {----- 1C -----}
59 steps :: Int -> Cmd
60 steps a | a <= 1 = Seq [Call "vector" [0, 0, 0, 1],
61                        Call "vector" [0, 1, 1, 1]]
62 steps a          = Seq [steps (a-1),
63                        Seq [Call "vector" [a-1, a-1, a-1, a],
64                            Call "vector" [a-1, a, a, a]]]
65
66
67 {----- 2A -----}
68 data GateFn = And | Or | Xor | Not
69             deriving Show
70
71 data Pair = Pair Int Int
72
73 data Gates = Gate Int GateFn Gates | GNone
74
75 data Links = Link Pair Pair Links | LNone
76
77 data Circuit = Circuit Gates Links
78
79 {----- 2B -----}
80
81 halfadder = Circuit (Gate 1 Xor
82                    (Gate 2 And
83                     GNone))
84                    (Link (Pair 1 1) (Pair 2 1)
85                     (Link (Pair 1 2) (Pair 2 2)
86                      LNone))
87
88 {----- 2C -----}
89
90 printPair :: Pair -> String
91 printPair (Pair x y) = show x ++ "." ++ show y
92
93 printGates :: Gates -> String
94 printGates GNone = ""

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94 printGates GNone = ""
95 printGates (Gate i gfnc gates) = show i ++ ":" ++ show gfnc ++ ";\n" ++
96     printGates gates
97
98 printLinks :: Links -> String
99 printLinks LNone = ""
100 printLinks (Link pair1 pair2 links) = "from " ++ printPair pair1 ++ " to " ++
101     printPair pair2 ++ ";\n" ++ printLinks links
102
103 prettyPrint :: Circuit -> String
104 prettyPrint (Circuit gates links) = printGates gates ++ printLinks links
105
106
107 {----- Exercise 3 -----}
108
109 -- Original Syntax:
110 data Expr = I Int
111           | Plus Expr Expr
112           | Times Expr Expr
113           | Neg Expr
114
115 -- Alternative Syntax:
116 data Op = Add | Multiply | Negate
117         deriving Show
118
119 data Exp = Num Int
120          | Apply Op [Exp]
121          deriving Show
122
123 {----- 3A -----}
124
125 theExpression = Apply Multiply [ Apply Negate [Apply Add [Num 4, Num 3]], Num 7]
126
127 {----- 3B -----}
128
129 The alternative syntax can apply an operator to an arbitrary list of
130 expressions, whereas the original syntax can only add two expressions,
131 multiply two expressions, or negate one expression. Thus, although the
132 alternative syntax is more flexible for the currently defined operators, it
133 would be more difficult to implement division in a way that makes sense.
134
135 {----- 3C -----}
136
137 translate :: Expr -> Exp
138 translate (I i) = (Num i)

```

```
138 translate (I x) = (num x)
139 translate (Plus x y) = Apply Add [(translate x), (translate y)]
140 translate (Times x y) = Apply Multiply [translate x, translate y]
141 translate (Neg x) = Apply Negate [translate x]
142
```
