

LAB 7 PRELAB

1. Storing the contents of registers R25:R24 into Timer/Counter1's 16-bit register, TCNT1

sti TCNT1H, R25

sti TCNT1L, R24

2. Loading the contents of Timer/Counter1's 16-bit register, TCNT1, into registers R25:R24

ldi R25, TCNT1H

ldi R24, TCNT1L

3. $\text{Delay}_{\text{Normal}} = ((\text{MAX} - \text{value}) * \text{prescale}) / 16\text{MHz}$

$$(255 - 128) / (16) = 7.932 * 10^{-6} \text{ seconds for the TOV0 flag to become set}$$