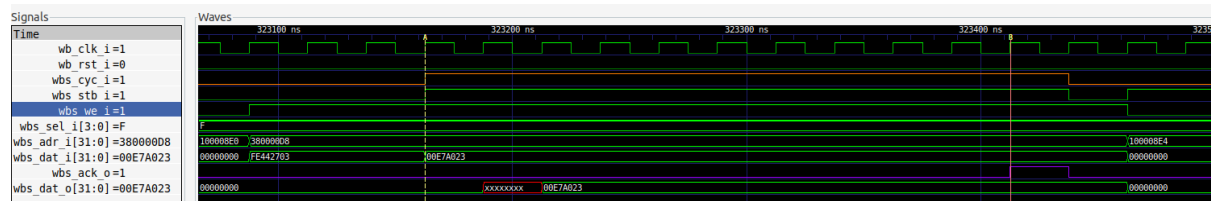
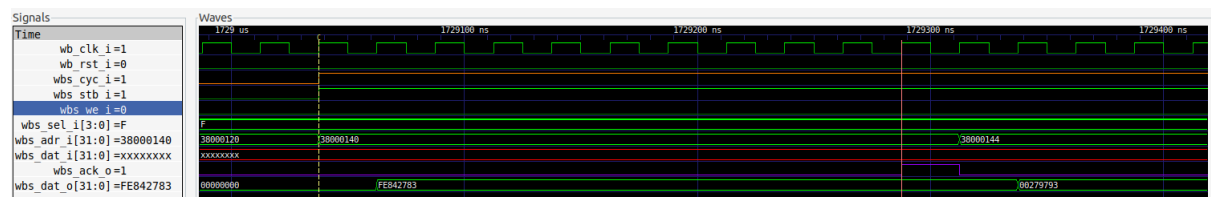


Lab4-1 Report

Write 10 cycle delay



Read 10 cycle delay



Testbench Passed

```
ubuntu@ubuntu2004: ~/Documents/Lab4/Lab4-1/lab-exmem_fir/testbench/counter_la_fir
ubuntu@ubuntu2004:~/Documents/Lab4/Lab4-1/lab-exmem_fir/testbench/counter_la_fir$ source run_clean
ubuntu@ubuntu2004:~/Documents/Lab4/Lab4-1/lab-exmem_fir/testbench/counter_la_fir$ source run_sim
Reading counter_la_fir.hex
counter_la_fir.hex loaded into memory
Memory 5 bytes = 0x6f 0x00 0x00 0x0b 0x13
VCD info: dumpfile counter_la_fir.vcd opened for output.
LA Test 1 started
LA Test 2 passed
ubuntu@ubuntu2004:~/Documents/Lab4/Lab4-1/lab-exmem_fir/testbench/counter_la_fir$
```