make debug

spike -d --log-commits fib

(spike)

core 0: 0x0000000000001000 (0x00000297) auipc t0, 0x0

core 0: 3 0x0000000000001000 (0x00000297) x5 0x0000000000001000

(spike)

core 0: 0x0000000000001004 (0x02028593) addi a1, t0, 32

core 0: 3 0x0000000000001004 (0x02028593) x11 0x0000000000001020

(spike)

core 0: 0x0000000000001008 (0xf1402573) csrr a0, mhartid

core 0: 3 0x0000000000001008 (0xf1402573) x10 0x0000000000000000

(spike)

core 0: 0x000000000000100c (0x0182b283) ld t0, 24(t0)

core 0: 3 0x000000000000100c (0x0182b283) x5 0x0000000080000000 mem 0x0000000000001018

(spike)

core 0: 0x0000000000001010 (0x00028067) jr t0

core 0: 3 0x0000000000001010 (0x00028067)

(spike)

core 0: >>>> $xrv64i2p1\_m2p0\_a2p1\_f2p2\_d2p2\_c2p0\_zicsr2p0\_zifencei2p0\_zmmul1p0\_zaamo1p0\_zalrsc1p0

core 0: 0x0000000080000000 (0x00004301) c.li t1, 0

core 0: 3 0x0000000080000000 (0x4301) x6 0x0000000000000000

(spike)

core 0: 0x0000000080000002 (0x000043a1) c.li t2, 8

core 0: 3 0x0000000080000002 (0x43a1) x7 0x0000000000000008

(spike)

core 0: 0x0000000080000004 (0x00004e01) c.li t3, 0

core 0: 3 0x0000000080000004 (0x4e01) x28 0x0000000000000000

(spike)

core 0: 0x0000000080000006 (0x00004e85) c.li t4, 1

core 0: 3 0x0000000080000006 (0x4e85) x29 0x0000000000000001

(spike) \

Unknown command \

(spike)

core 0: 0x0000000080000008 (0x01ce8f33) add t5, t4, t3

core 0: 3 0x0000000080000008 (0x01ce8f33) x30 0x0000000000000001

(spike)

core 0: >>>> loop

core 0: 0x000000008000000c (0x00000305) c.addi t1, 1

core 0: 3 0x000000008000000c (0x0305) x6 0x0000000000000001

(spike)

core 0: 0x000000008000000e (0x00008e76) c.mv t3, t4

core 0: 3 0x000000008000000e (0x8e76) x28 0x0000000000000001

(spike)

core 0: 0x0000000080000010 (0x00008efa) c.mv t4, t5

core 0: 3 0x0000000080000010 (0x8efa) x29 0x0000000000000001

(spike)

core 0: 0x0000000080000012 (0x01de0f33) add t5, t3, t4

core 0: 3 0x0000000080000012 (0x01de0f33) x30 0x0000000000000002

(spike)

core 0: 0x0000000080000016 (0xfe734be3) blt t1, t2, pc - 10

core 0: 3 0x0000000080000016 (0xfe734be3)

(spike)

core 0: >>>> loop

core 0: 0x000000008000000c (0x00000305) c.addi t1, 1

core 0: 3 0x000000008000000c (0x0305) x6 0x0000000000000002

(spike)

core 0: 0x000000008000000e (0x00008e76) c.mv t3, t4

core 0: 3 0x000000008000000e (0x8e76) x28 0x0000000000000001

(spike)

core 0: 0x0000000080000010 (0x00008efa) c.mv t4, t5

core 0: 3 0x0000000080000010 (0x8efa) x29 0x0000000000000002

(spike)

core 0: 0x0000000080000012 (0x01de0f33) add t5, t3, t4

core 0: 3 0x0000000080000012 (0x01de0f33) x30 0x0000000000000003

(spike)

core 0: 0x0000000080000016 (0xfe734be3) blt t1, t2, pc - 10

core 0: 3 0x0000000080000016 (0xfe734be3)

(spike)

core 0: >>>> loop

core 0: 0x000000008000000c (0x00000305) c.addi t1, 1

core 0: 3 0x000000008000000c (0x0305) x6 0x0000000000000003

(spike)

core 0: 0x000000008000000e (0x00008e76) c.mv t3, t4

core 0: 3 0x000000008000000e (0x8e76) x28 0x0000000000000002

(spike)

core 0: 0x0000000080000010 (0x00008efa) c.mv t4, t5

core 0: 3 0x0000000080000010 (0x8efa) x29 0x0000000000000003

(spike)

core 0: 0x0000000080000012 (0x01de0f33) add t5, t3, t4

core 0: 3 0x0000000080000012 (0x01de0f33) x30 0x0000000000000005

(spike)

core 0: 0x0000000080000016 (0xfe734be3) blt t1, t2, pc - 10

core 0: 3 0x0000000080000016 (0xfe734be3)

(spike)

core 0: >>>> loop

core 0: 0x000000008000000c (0x00000305) c.addi t1, 1

core 0: 3 0x000000008000000c (0x0305) x6 0x0000000000000004

(spike)

core 0: 0x000000008000000e (0x00008e76) c.mv t3, t4

core 0: 3 0x000000008000000e (0x8e76) x28 0x0000000000000003

(spike)

core 0: 0x0000000080000010 (0x00008efa) c.mv t4, t5

core 0: 3 0x0000000080000010 (0x8efa) x29 0x0000000000000005

(spike)

core 0: 0x0000000080000012 (0x01de0f33) add t5, t3, t4

core 0: 3 0x0000000080000012 (0x01de0f33) x30 0x0000000000000008

(spike)

core 0: 0x0000000080000016 (0xfe734be3) blt t1, t2, pc - 10

core 0: 3 0x0000000080000016 (0xfe734be3)

(spike)

core 0: >>>> loop

core 0: 0x000000008000000c (0x00000305) c.addi t1, 1

core 0: 3 0x000000008000000c (0x0305) x6 0x0000000000000005

(spike)

core 0: 0x000000008000000e (0x00008e76) c.mv t3, t4

core 0: 3 0x000000008000000e (0x8e76) x28 0x0000000000000005

(spike)

core 0: 0x0000000080000010 (0x00008efa) c.mv t4, t5

core 0: 3 0x0000000080000010 (0x8efa) x29 0x0000000000000008

(spike)

core 0: 0x0000000080000012 (0x01de0f33) add t5, t3, t4

core 0: 3 0x0000000080000012 (0x01de0f33) x30 0x000000000000000d

(spike)

core 0: 0x0000000080000016 (0xfe734be3) blt t1, t2, pc - 10

core 0: 3 0x0000000080000016 (0xfe734be3)

(spike)

core 0: >>>> loop

core 0: 0x000000008000000c (0x00000305) c.addi t1, 1

core 0: 3 0x000000008000000c (0x0305) x6 0x0000000000000006

(spike)

core 0: 0x000000008000000e (0x00008e76) c.mv t3, t4

core 0: 3 0x000000008000000e (0x8e76) x28 0x0000000000000008

(spike)

core 0: 0x0000000080000010 (0x00008efa) c.mv t4, t5

core 0: 3 0x0000000080000010 (0x8efa) x29 0x000000000000000d

(spike)

core 0: 0x0000000080000012 (0x01de0f33) add t5, t3, t4

core 0: 3 0x0000000080000012 (0x01de0f33) x30 0x0000000000000015

(spike)

core 0: 0x0000000080000016 (0xfe734be3) blt t1, t2, pc - 10

core 0: 3 0x0000000080000016 (0xfe734be3)

(spike)

core 0: >>>> loop

core 0: 0x000000008000000c (0x00000305) c.addi t1, 1

core 0: 3 0x000000008000000c (0x0305) x6 0x0000000000000007

(spike)

core 0: 0x000000008000000e (0x00008e76) c.mv t3, t4

core 0: 3 0x000000008000000e (0x8e76) x28 0x000000000000000d

(spike)

core 0: 0x0000000080000010 (0x00008efa) c.mv t4, t5

core 0: 3 0x0000000080000010 (0x8efa) x29 0x0000000000000015

(spike)

core 0: 0x0000000080000012 (0x01de0f33) add t5, t3, t4

core 0: 3 0x0000000080000012 (0x01de0f33) x30 0x0000000000000022

(spike)

core 0: 0x0000000080000016 (0xfe734be3) blt t1, t2, pc - 10

core 0: 3 0x0000000080000016 (0xfe734be3)

(spike)

core 0: >>>> loop

core 0: 0x000000008000000c (0x00000305) c.addi t1, 1

core 0: 3 0x000000008000000c (0x0305) x6 0x0000000000000008

(spike)

core 0: 0x000000008000000e (0x00008e76) c.mv t3, t4

core 0: 3 0x000000008000000e (0x8e76) x28 0x0000000000000015

(spike)

core 0: 0x0000000080000010 (0x00008efa) c.mv t4, t5

core 0: 3 0x0000000080000010 (0x8efa) x29 0x0000000000000022

(spike)

core 0: 0x0000000080000012 (0x01de0f33) add t5, t3, t4

core 0: 3 0x0000000080000012 (0x01de0f33) x30 0x0000000000000037

(spike)

core 0: 0x0000000080000016 (0xfe734be3) blt t1, t2, pc - 10

core 0: 3 0x0000000080000016 (0xfe734be3)

(spike)

core 0: 0x000000008000001a (0x00004285) c.li t0, 1

core 0: 3 0x000000008000001a (0x4285) x5 0x0000000000000001

(spike)

core 0: 0x000000008000001c (0x00000317) auipc t1, 0x0

core 0: 3 0x000000008000001c (0x00000317) x6 0x000000008000001c

(spike)

core 0: 0x0000000080000020 (0x01430313) addi t1, t1, 20

core 0: 3 0x0000000080000020 (0x01430313) x6 0x0000000080000030

(spike)

core 0: 0x0000000080000024 (0x00533023) sd t0, 0(t1)

core 0: 3 0x0000000080000024 (0x00533023) mem 0x0000000080000030 0x0000000000000001