

EE2016 Microprocessor Theory & Lab, Fall 2020

Midsemester Exam, Part B

1. Write and execute an AVR assembly language program to generate a set of Fibonacci numbers. Using the fibonacci series generated, display '1' for odd number and display '2' for even number (consider 0 as even) in memory as shown in table. Assume count(n =50, define inside program) is an input along with the first two fibonacci series numbers.

Display Fibonacci series generated:

SRAM location	Values	Remarks	Input/Output
0051	0	Fibonacci numbers	Output
0052	1	Fibonacci numbers	Output
0052	1	Fibonacci numbers	Output
0053	2	Fibonacci numbers	Output

Display '1' and '2' based on series generated:

SRAM location	Values	Remarks	Input/Output
0070	2	Display 0 for '0'	Output
0071	1	Display 1 for '1'	Output
0072	1	Display 1 for '1'	Output
0073	2	Display 2 for '2'	Output