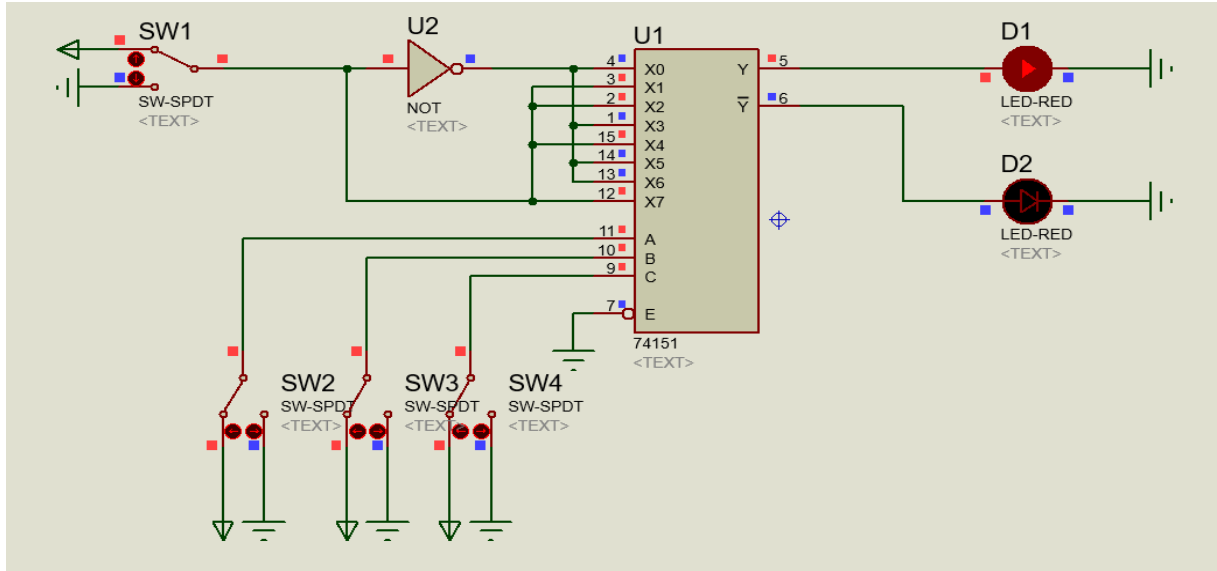


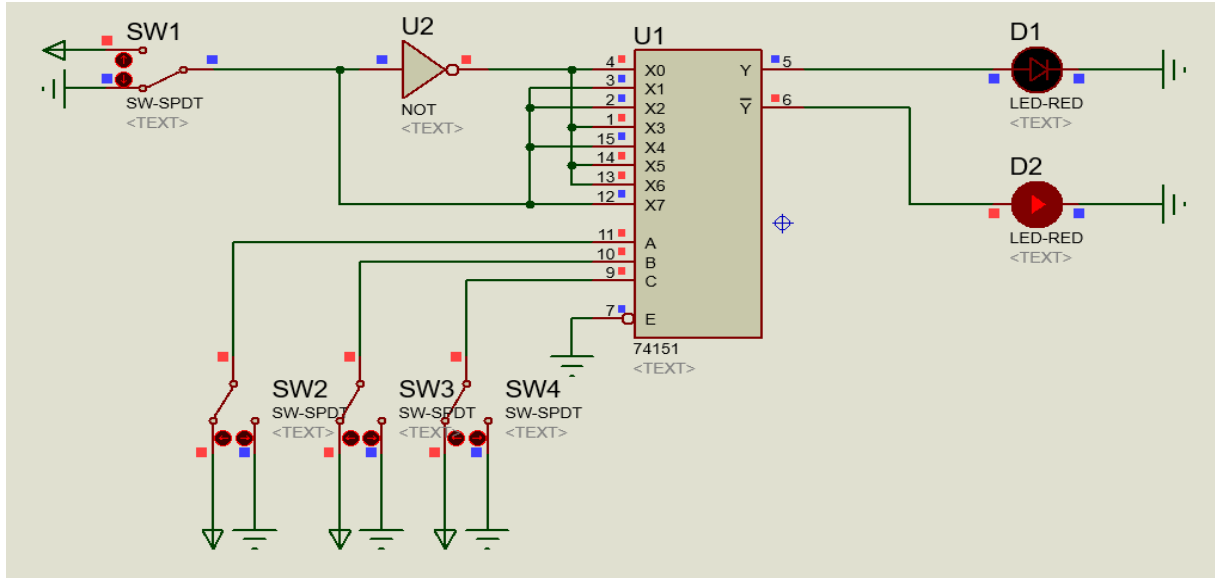
SAYISAL TASARIM LAB. 3.DENEY PRELAB

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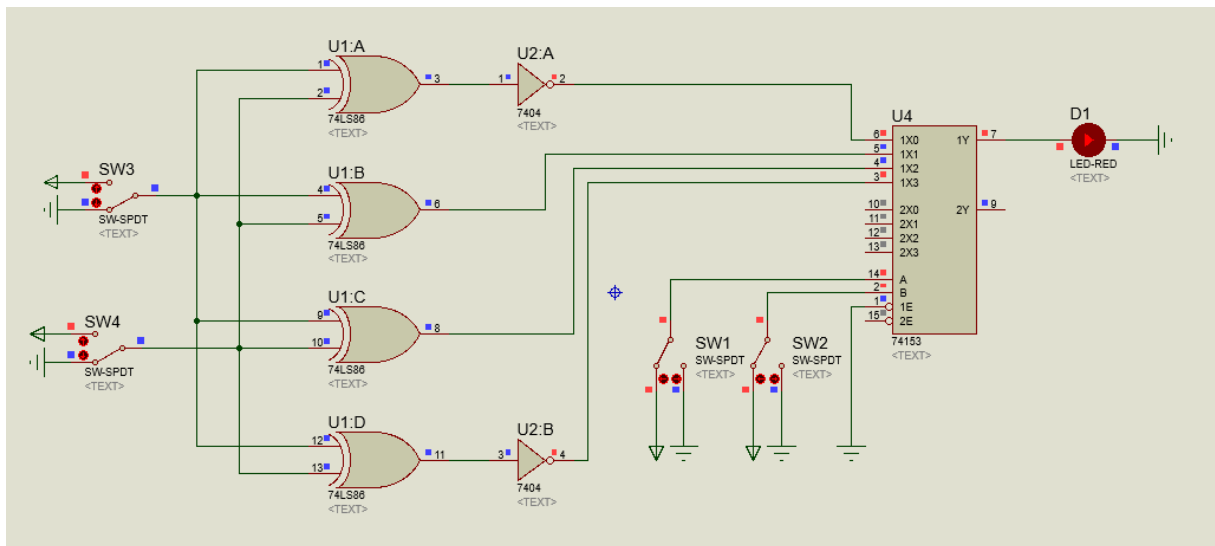
1.SORU

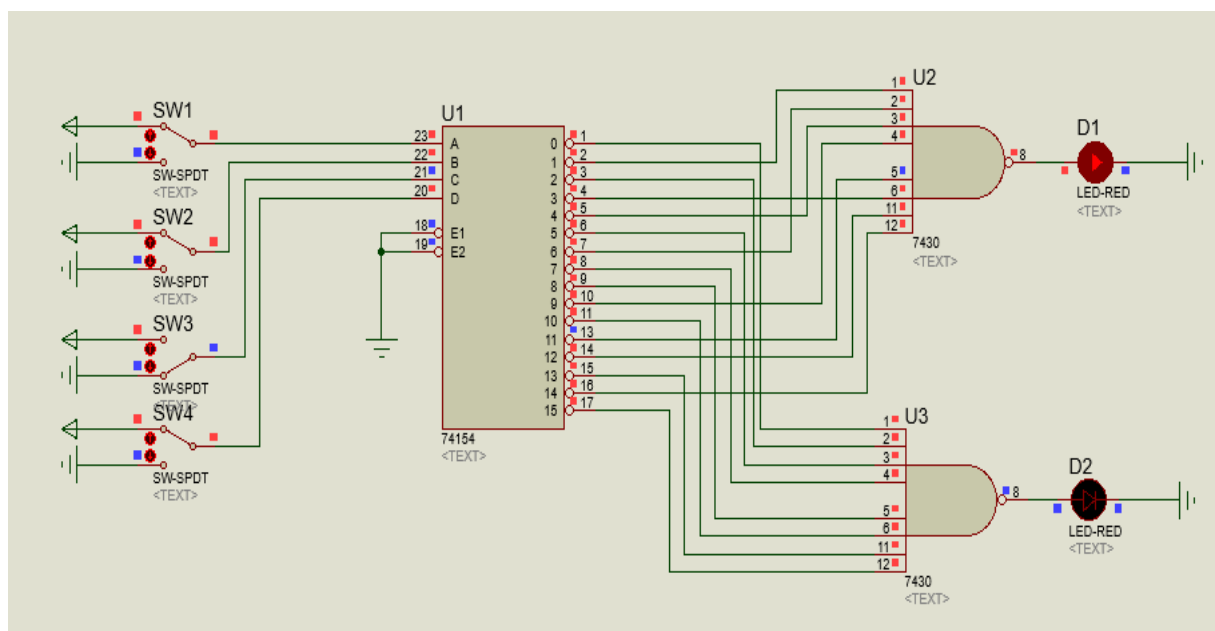


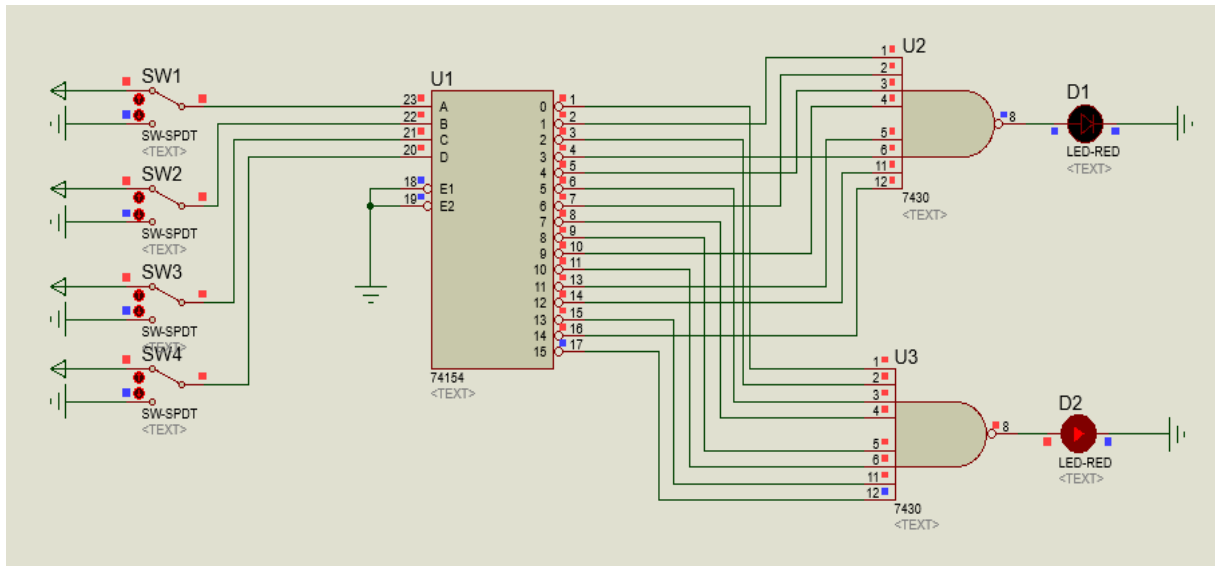
Inputs	Parity Bit
A B C D	P
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0 0 0 1	0
0 0 1 0	0
0 0 1 1	1
0 1 0 0	0
0 1 0 1	1
0 1 1 0	1
0 1 1 1	0
1 0 0 0	0
1 0 0 1	1
1 0 1 0	1
1 0 1 1	0
1 1 0 0	1
1 1 0 1	0
1 1 1 0	0
1 1 1 1	1



2.SORU







4.SORU

