#### **EE315 - Electronics Laboratory**

### Experiment - 5

# MOSFET Characteristics and Small Signal Amplifier

## **Preliminary Work**

1. The inverter in Figure 1 utilizes an N-channel enhancement-mode MOSFET with the parameters  $K_n = 1 \text{ mA/V}^2$  and  $V_{TN} = 2 \text{ V}$ . Find the inverter output at  $v_{out}$  when  $v_{in} = 3 \text{ V}$  and  $v_{in} = 4 \text{ V}$ . Plot the  $v_{in}$ - $v_{out}$  transfer characteristics of the inverter.

 $V_{DD}=10V$   $R_{D}$   $i_{D}\sqrt{510\Omega}$   $V_{IN}=2.0 V$   $K_{n}=1 \text{ mA/V}^{2}$ Figure 1

Equations for the N-channel MOSFET are given below.

$$i_{\rm D} = {\rm K_n}(v_{\rm GS} - {\rm V_{TN}})^2$$
 (in SAT region:  $v_{\rm DS} \ge v_{\rm GS} - {\rm V_{TN}}$ )  
 $i_{\rm D} = {\rm K_n}[2(v_{\rm GS} - {\rm V_{TN}})v_{\rm DS} - v_{\rm DS}^2]$  (in triode=non-SAT region:  $v_{\rm DS} < v_{\rm GS} - {\rm V_{TN}}$ )

2. Consider the MOSFET amplifier in Figure 2 with the parameters  $K_n$  = 1 mA/V² and  $V_{TN}$  = 2 V. Determine the resistor values to obtain roughly  $I_D$  = 1 mA and to set  $V_D$  approximately midway between  $V_{DD}$  and  $V_G$ .

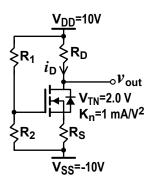
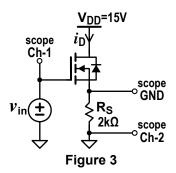


Figure 2

### **Procedure**

- **1.a)** Build the inverter circuit given in Figure 1 using  $R_D = 510 \Omega$  and  $V_{DD} = 10 V$ . Initially set  $v_{in} = 0 V$ .
- **1.b)** Increase  $v_{in}$  gradually until the point  $v_{out}$  becomes constant. Record the  $v_{in}$  and  $v_{out}$  values to identify significant changes in the inverter response.
- **2.a)** Build the circuit in Figure **3**. Apply an input voltage varying between **0 V** and **10 V** setting  $v_{in} = 5V + 5V \sin(2000 \pi t)$ .
- **2.b)** Connect the oscilloscope probes as shown in Figure 3. Observe and record the  $i_D$  versus  $v_{GS}$  plot using the oscilloscope in X-Y mode. Note that, polarities of  $v_{GS}$  and  $i_D$  are reversed on the oscilloscope screen.



**3.a)** Build the circuit in Figure **4** using the resistor values you calculated in the preliminary work. Set  $v_{in}$  frequency to **10 kHz** and amplitude to **10 mV**. You may need a higher amplitude to obtain significant results when you begin the measurements.

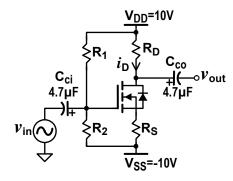


Figure 4

- **3.b)** Display input and output signals on the oscilloscope and observe the phase shift. Measure the output voltage, phase shift and calculate the voltage gain.
- **3.c)** Connect a **100 k\Omega** resistor between the input voltage source and the coupling capacitor,  $C_{ci}$ . Measure the voltage gain and use the results from step **3.b** to deduce the amplifier input resistance,  $R_{in}$ .
- **3.d)** Measure the output resistance,  $R_0$ , of the amplifier.