Inverter Timing Report

PRE LAYOUT SIMULATION

Signal	Rise Time ps	Fall Time ps	tpHL ps	tpLH ps	Propagation delay ps
IN	10	10			
OUT	7.68	6.4327	8	8	8

POST LAYOUT SIMULATION

Signal	Rise Time ps	Fall Time ps	tpHL ps	tpLH ps	Propagation delay ps
IN	10	10			
OUT	16.64	13.17	14	16	15