

JK Flip Flop Timing Report

PRE LAYOUT SIMULATION

Signal	Rise Time ps	Fall Time ps	tpHL ps	tpLH ps	Propagation delay ps
J	10	10			
K	10	10			
CLK	10	10			
Q	39.93	53.35	134.27	85.93	110.1
Q*	41.1	54.45	128.35	84.55	106.45

POST LAYOUT SIMULATION

Signal	Rise Time ps	Fall Time ps	tpHL ps	tpLH ps	Propagation delay ps
J	10	10			
K	10	10			
CLK	10	10			
Q	117.35	157.453	292.1	196.59	244.345
Q*	110.8	149.67	322.7	172.2	247.45