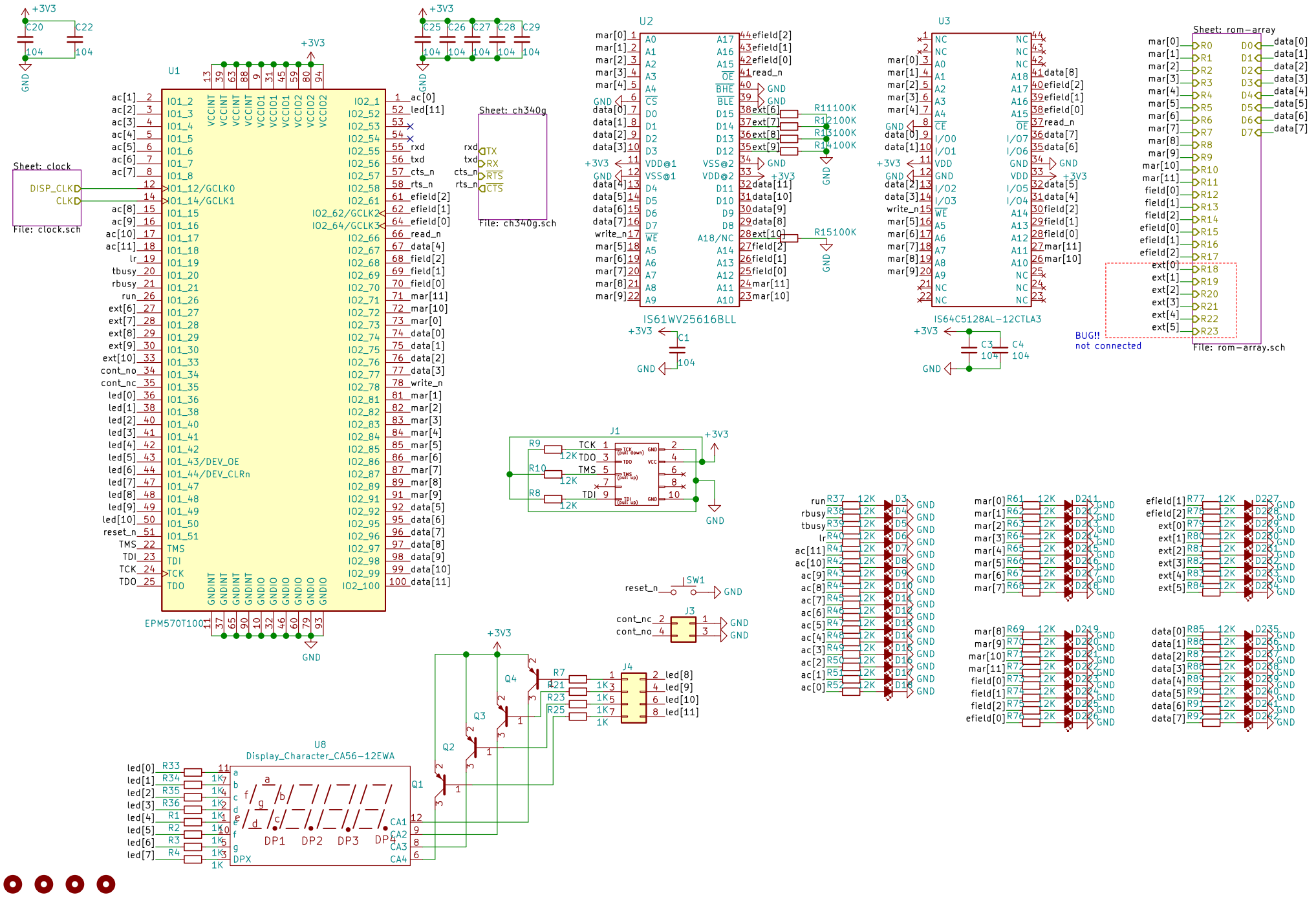
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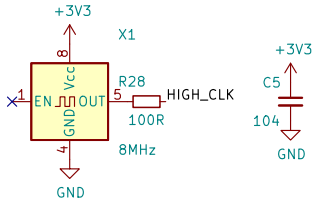
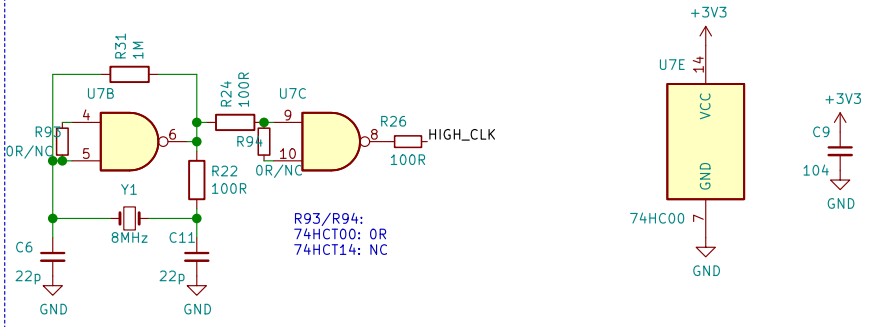
EPM570T100
Sheet: clock
File: clock.sch
Sheet: ch340g
File: ch340g.sch
U2
IS61WV25616BLL
U3
IS64C5128AL-12CTLA3
Sheet: rom-array
File: rom-array.sch
BUG!!
not connected
J1
SW1
J3
J4
U8
Display_Character_CA56-12EWA
Q1
Q2
Q3
Q4

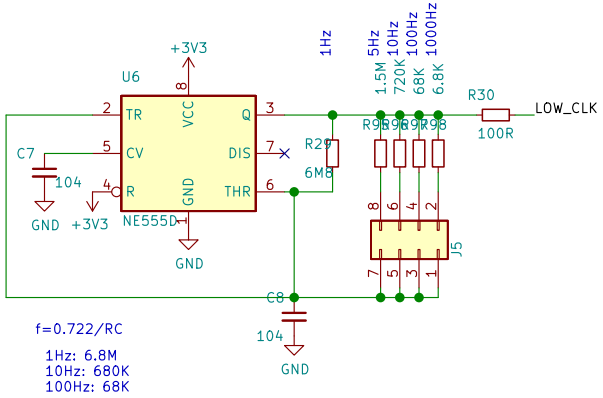
OPTION 1 – clock with OSC



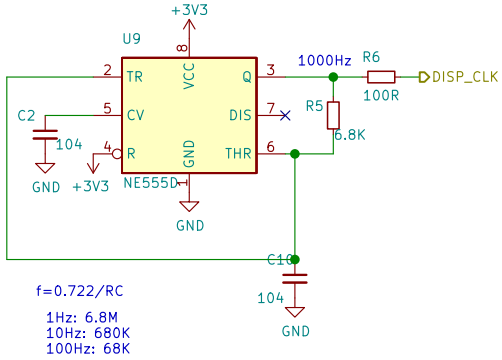
OPTION 3 – clock with crystal and NAND gates



OPTION 2 – clock with NE555



DISP_CLK



https://www.electronics-tutorials.ws/waveforms/555-circuits-part-1.html

http://qrp-popcorn.blogspot.com/2016/01/inverter-crystal-oscillator.html

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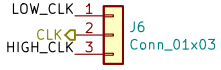

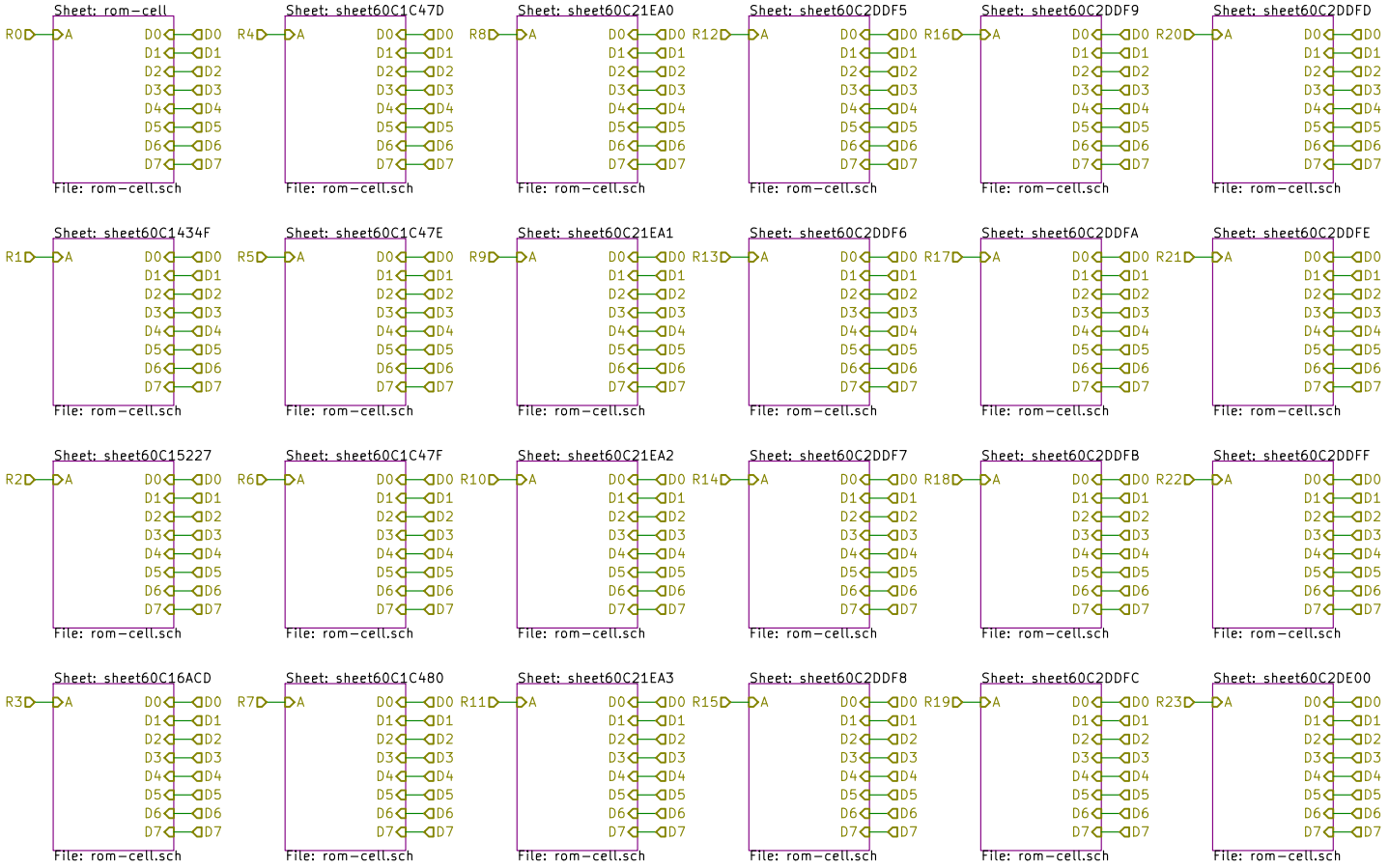

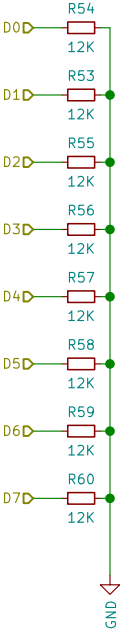

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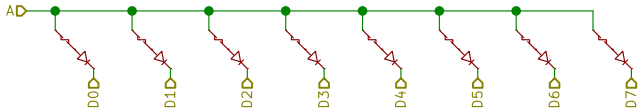
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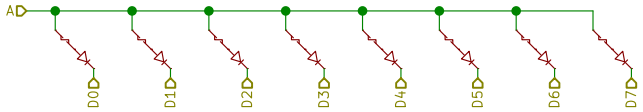
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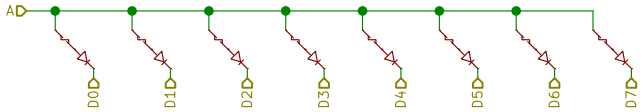
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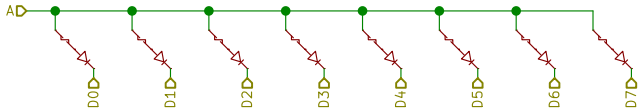
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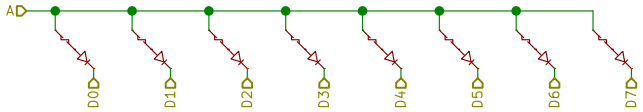
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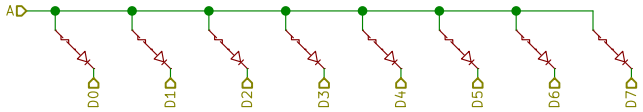
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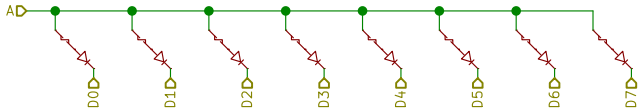
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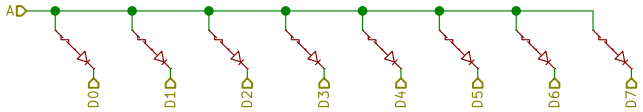
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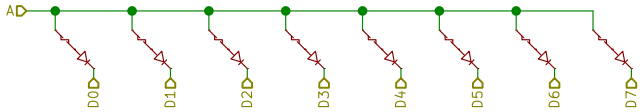
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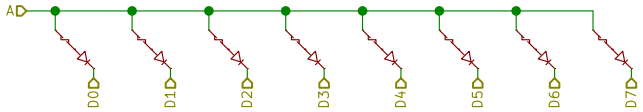
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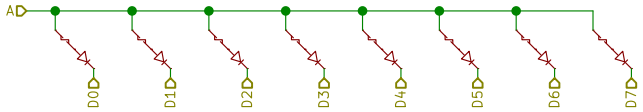
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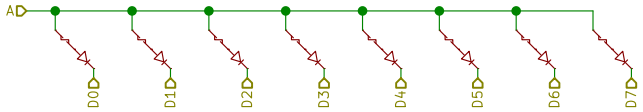
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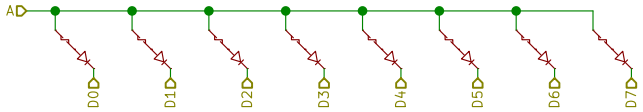
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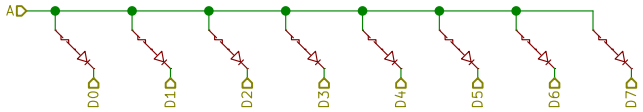
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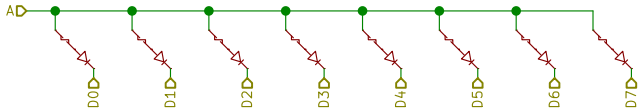
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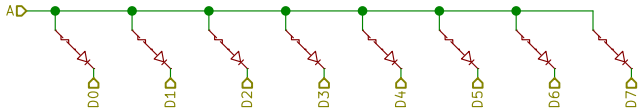
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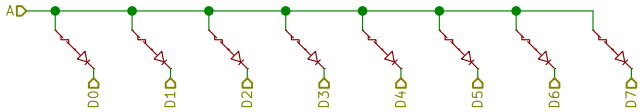
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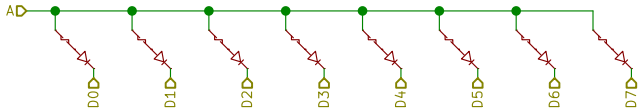
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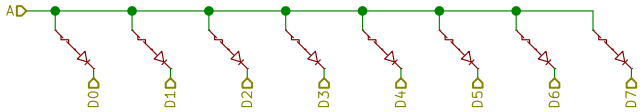
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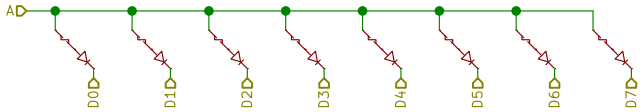
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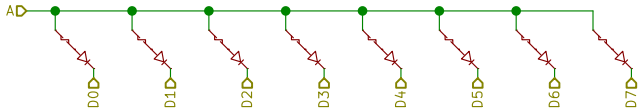
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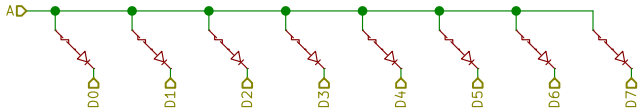
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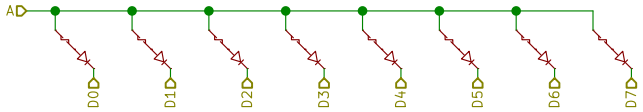
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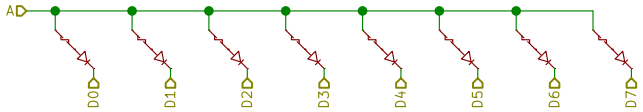
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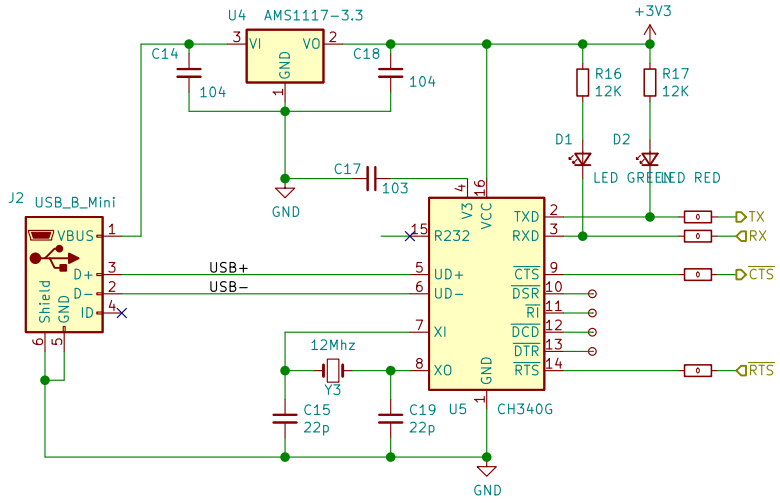

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