Hewlett Packard Enterprise   **HPE Support Center**

HPE UEFI Shell for HPE ProLiant Gen9 10 Servers and HPE Synergy - UEFI Shell devtree Command

devtree

Link to list of available documents

devtree

Syntax

```
devtree[-b][-d][-lxxx][devicehandle]
```

Description

Displays the tree of devices compliant with the UEFI Driver Model.

```
-b
```

Displays one screen at a time.

```
-d
```

Displays the device tree using device paths.

```
-lxxx
```

Displays the device tree in a specific language. For a list of possible code options, see the UEFIspecification.

```
devicehandle
```

Displays the device tree below a specified handle.

Usage

By default, device names that are retrieved from the Component Name Protocol. If the option –d is specified,the device paths are printed instead.

Example

To display the tree of all devices compliant with the UEFI Driver Model one screen at a time:

```
Shell> devtree -b
devtree -b fs0:\
Ctrl[04]  Fv(770BF9B6-8AFE-4F4C-85E5-893FC3D2606C)
Ctrl[05]  Fv(27A72E80-3118-4C0C-8673-AA5B4EFA9613)-directories in the current
Ctrl[06]  MemoryMapped(0xB,0xFFD40000,0xFFD6FFFF)
Ctrl[07]  Fv(5A515240-D1F1-4C58-9590-27B1F0E86827)
Ctrl[08]  Fv(5E2363B4-3E9E-4203-B873-BB40DF46C8E6)
Ctrl[09]  Fv(CDBB7B35-6833-4ED6-9AB2-57D2ACDDF6F0)extension '.efi':
Ctrl[5F]  PciRoot(0x0).efi
Ctrl[195]  PciRoot(0x0)/Pci(0x0,0x0)
Ctrl[196]  PciRoot(0x0)/Pci(0x2,0x0)from all files with extension '.inf':
Ctrl[197]  PciRoot(0x0)/Pci(0x2,0x0)/Pci(0x0,0x0)
Ctrl[198]  PciRoot(0x0)/Pci(0x2,0x1)
Ctrl[199]  PciRoot(0x0)/Pci(0x2,0x2)
Ctrl[19A]  PciRoot(0x0)/Pci(0x2,0x3)
Ctrl[19B]  PciRoot(0x0)/Pci(0x3,0x0)
Ctrl[19C]  PciRoot(0x0)/Pci(0x3,0x1)
Ctrl[19D]  PciRoot(0x0)/Pci(0x3,0x2)
Ctrl[19E]  PciRoot(0x0)/Pci(0x3,0x3)
Ctrl[19F]  PciRoot(0x0)/Pci(0x4,0x0)
Ctrl[1A0]  PciRoot(0x0)/Pci(0x4,0x1)
Ctrl[1A1]  PciRoot(0x0)/Pci(0x4,0x2)
Ctrl[1A2]  PciRoot(0x0)/Pci(0x4,0x3)
Ctrl[1A3]  PciRoot(0x0)/Pci(0x4,0x4)
Ctrl[1A4]  PciRoot(0x0)/Pci(0x4,0x5)
Ctrl[1A5]  PciRoot(0x0)/Pci(0x4,0x6)
Ctrl[1A6]  PciRoot(0x0)/Pci(0x4,0x7)
Ctrl[1A7]  PciRoot(0x0)/Pci(0x5,0x0)
Ctrl[1A8]  PciRoot(0x0)/Pci(0x5,0x1)
Ctrl[1A9]  PciRoot(0x0)/Pci(0x5,0x2)
Ctrl[1AA]  PciRoot(0x0)/Pci(0x5,0x4)
```

```
Ctrl[1AB] PciRoot(0x0)/Pci(0x6,0x7)
Press ENTER to continue or 'Q' break:
```

top

Link to list of available documents

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top

```
Ctrl[1AB] PciRoot(0x0)/Pci(0x6,0x7)
Press ENTER to continue or 'Q' break:
```