1. Description

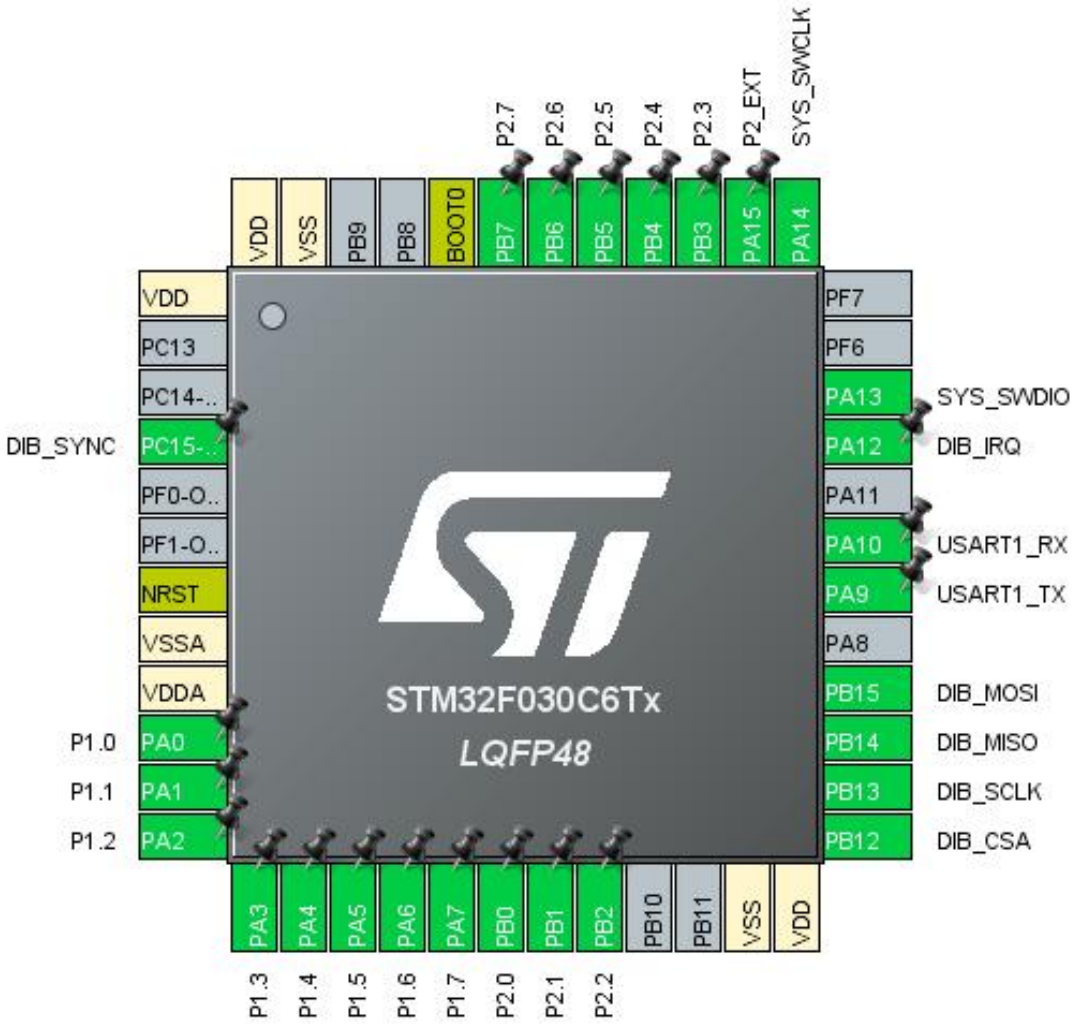
1.1. Project

Project Name	MUX14D
Board Name	custom
Generated with:	STM32CubeMX 5.6.1
Date	12/07/2020

1.2. MCU

MCU Series	STM32F0
MCU Line	STM32F0x0 Value Line
MCU name	STM32F030C6Tx
MCU Package	LQFP48
MCU Pin number	48

2. Pinout Configuration

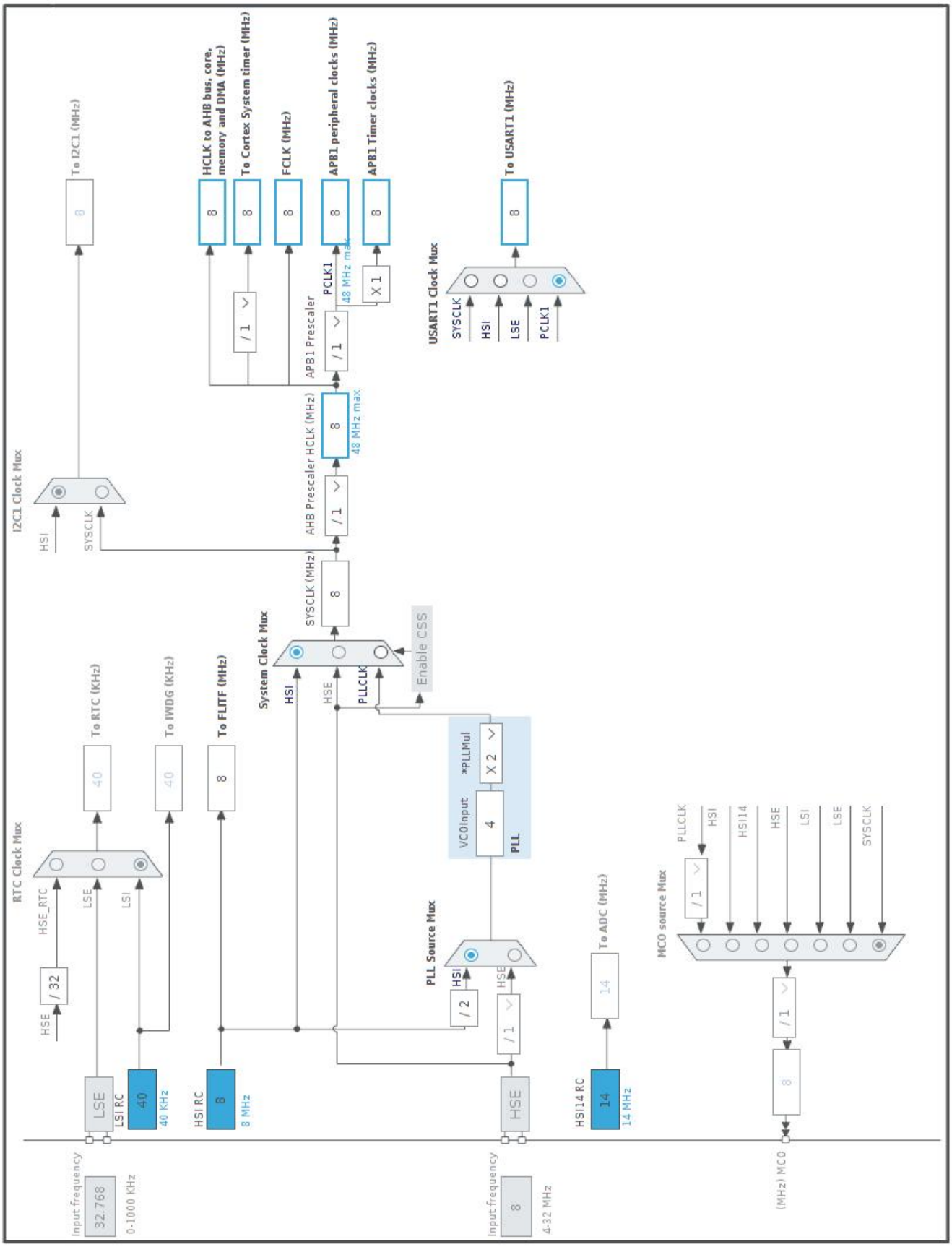


3. Pins Configuration

Pin Number LQFP48	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
1	VDD	Power		
4	PC15-OSC32_OUT *	I/O	GPIO_Input	DIB_SYNC
7	NRST	Reset		
8	VSSA	Power		
9	VDDA	Power		
10	PA0 *	I/O	GPIO_Output	P1.0
11	PA1 *	I/O	GPIO_Output	P1.1
12	PA2 *	I/O	GPIO_Output	P1.2
13	PA3 *	I/O	GPIO_Output	P1.3
14	PA4 *	I/O	GPIO_Output	P1.4
15	PA5 *	I/O	GPIO_Output	P1.5
16	PA6 *	I/O	GPIO_Output	P1.6
17	PA7 *	I/O	GPIO_Output	P1.7
18	PB0 *	I/O	GPIO_Output	P2.0
19	PB1 *	I/O	GPIO_Output	P2.1
20	PB2 *	I/O	GPIO_Output	P2.2
23	VSS	Power		
24	VDD	Power		
25	PB12	I/O	SPI1_NSS	DIB_CSA
26	PB13	I/O	SPI1_SCK	DIB_SCLK
27	PB14	I/O	SPI1_MISO	DIB_MISO
28	PB15	I/O	SPI1_MOSI	DIB_MOSI
30	PA9	I/O	USART1_TX	
31	PA10	I/O	USART1_RX	
33	PA12 *	I/O	GPIO_Output	DIB_IRQ
34	PA13	I/O	SYS_SWDIO	
37	PA14	I/O	SYS_SWCLK	
38	PA15 *	I/O	GPIO_Output	P2_EXT
39	PB3 *	I/O	GPIO_Output	P2.3
40	PB4 *	I/O	GPIO_Output	P2.4
41	PB5 *	I/O	GPIO_Output	P2.5
42	PB6 *	I/O	GPIO_Output	P2.6
43	PB7 *	I/O	GPIO_Output	P2.7
44	BOOT0	Boot		
47	VSS	Power		
48	VDD	Power		

* The pin is affected with an I/O function

4. Clock Tree Configuration



5. Software Project

5.1. Project Settings

Name	Value
Project Name	MUX14D
Project Folder	/home/denis/BACKUP/EEZ/Digital control/MCU/STM32/Projects/MUX14D
Toolchain / IDE	EWARM V8.32
Firmware Package Name and Version	STM32Cube FW_F0 V1.11.0

5.2. Code Generation Settings

Name	Value
STM32Cube MCU packages and embedded software	Copy all used libraries into the project folder
Generate peripheral initialization as a pair of '.c/.h' files	No
Backup previously generated files when re-generating	No
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power consumption)	No

6. Power Consumption Calculator report

6.1. Microcontroller Selection

Series	STM32F0
Line	STM32F0x0 Value Line
MCU	STM32F030C6Tx
Datasheet	024849_Rev2

6.2. Parameter Selection

Temperature	25
Vdd	3.6

6.3. Battery Selection

Battery	Li-SOCL2(A3400)
Capacity	3400.0 mAh
Self Discharge	0.08 %/month
Nominal Voltage	3.6 V
Max Cont Current	100.0 mA
Max Pulse Current	200.0 mA
Cells in series	1
Cells in parallel	1

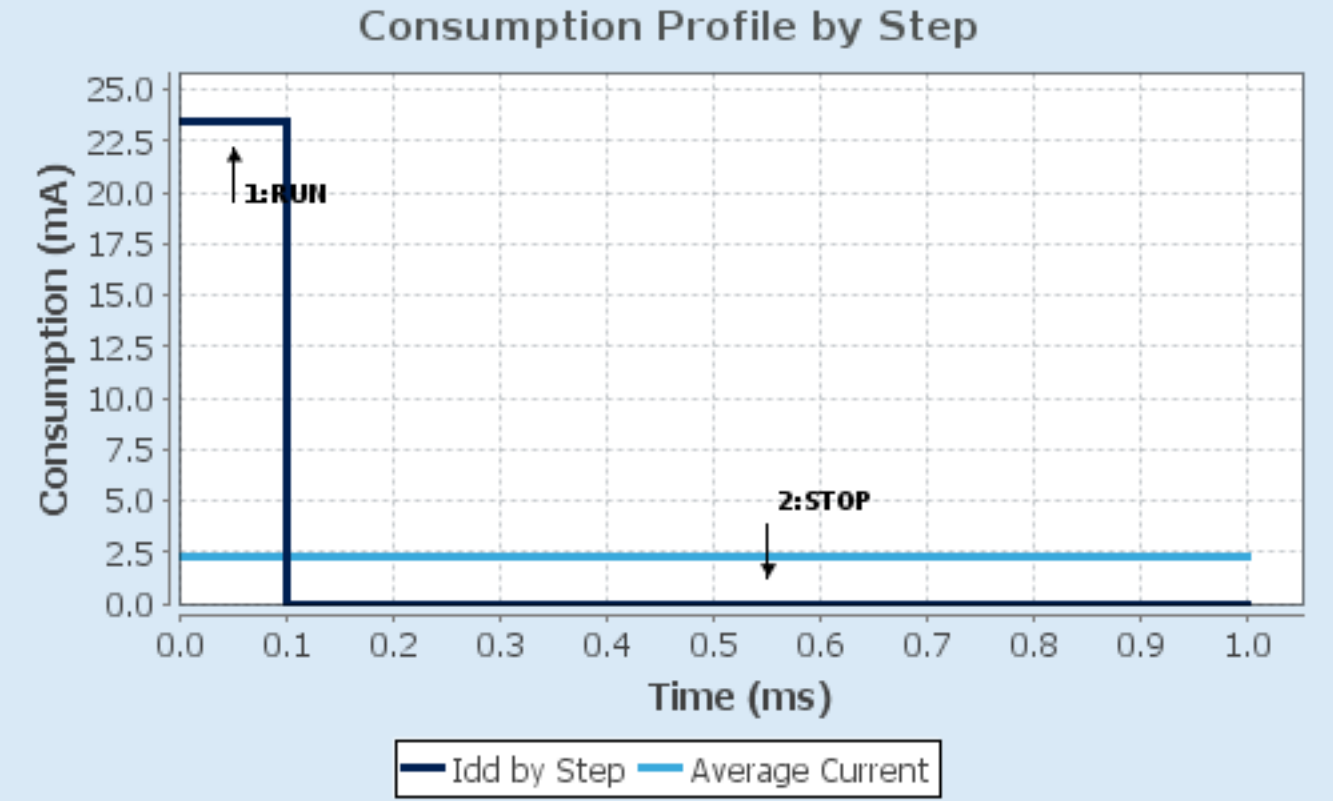
6.4. Sequence

Step	Step1	Step2
Mode	RUN	STOP
Vdd	3.6	3.6
Voltage Source	Battery	Battery
Range	No Scale	No Scale
Fetch Type	FLASH	n/a
CPU Frequency	48 MHz	0 Hz
Clock Configuration	HSE PLL All IPs ON	Regulator_LP
Clock Source Frequency	8 MHz	0 Hz
Peripherals		
Additional Cons.	0 mA	0 mA
Average Current	23.46 mA	7.9 µA
Duration	0.1 ms	0.9 ms
DMIPS	0.0	0.0
Ta Max	100.36	105
Category	In DS Table	In DS Table

6.5. RESULTS

Sequence Time	1 ms	Average Current	2.35 mA
Battery Life	1 month, 29 days, 16 hours	Average DMIPS	0.0 DMIPS

6.6. Chart


Consumption Profile by Step
Consumption (mA)
25.0
22.5
20.0
17.5
15.0
12.5
10.0
7.5
5.0
2.5
0.0
1:RUN
2:STOP
0.0
0.1
0.2
0.3
0.4
0.5
0.6
0.7
0.8
0.9
1.0
Time (ms)
Idd by Step
Average Current

7. IPs and Middleware Configuration

7.1. CRC

mode: Activated

7.1.1. Parameter Settings:

Basic Parameters:

Default Polynomial State	Enable
Default Init Value State	Enable

Advanced Parameters:

Input Data Inversion Mode	None
Output Data Inversion Mode	Disable
Input Data Format	Bytes

7.2. GPIO

7.3. SPI1

Mode: Full-Duplex Slave

Hardware NSS Signal: Hardware NSS Input Signal

7.3.1. Parameter Settings:

Basic Parameters:

Frame Format	Motorola
Data Size	4 Bits
First Bit	MSB First

Clock Parameters:

Clock Polarity (CPOL)	Low
Clock Phase (CPHA)	1 Edge

Advanced Parameters:

CRC Calculation	Disabled
NSS Signal Type	Input Hardware

7.4. SYS

mode: Debug Serial Wire

Timebase Source: SysTick

7.5. USART1

Mode: Asynchronous

7.5.1. Parameter Settings:

Basic Parameters:

Baud Rate	38400
Word Length	8 Bits (including Parity)
Parity	None
Stop Bits	1

Advanced Parameters:

Data Direction	Receive and Transmit
Over Sampling	16 Samples
Single Sample	Disable

Advanced Features:

Auto Baudrate	Disable
TX Pin Active Level Inversion	Disable
RX Pin Active Level Inversion	Disable
Data Inversion	Disable
TX and RX Pins Swapping	Disable
Overrun	Enable
DMA on RX Error	Enable
MSB First	Disable

*** User modified value**

8. System Configuration

8.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
SPI1	PB12	SPI1_NSS	Alternate Function Push Pull	No pull-up and no pull-down	**High ***	DIB_CSA
	PB13	SPI1_SCK	Alternate Function Push Pull	No pull-up and no pull-down	**High ***	DIB_SCLK
	PB14	SPI1_MISO	Alternate Function Push Pull	No pull-up and no pull-down	**High ***	DIB_MISO
	PB15	SPI1_MOSI	Alternate Function Push Pull	No pull-up and no pull-down	**High ***	DIB_MOSI
SYS	PA13	SYS_SWDIO	n/a	n/a	n/a	
	PA14	SYS_SWCLK	n/a	n/a	n/a	
USART1	PA9	USART1_TX	Alternate Function Push Pull	No pull-up and no pull-down	**High ***	
	PA10	USART1_RX	Alternate Function Push Pull	No pull-up and no pull-down	**High ***	
GPIO	PC15-OSC32_OUT	GPIO_Input	Input mode	No pull-up and no pull-down	n/a	DIB_SYNC
	PA0	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	P1.0
	PA1	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	P1.1
	PA2	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	P1.2
	PA3	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	P1.3
	PA4	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	P1.4
	PA5	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	P1.5
	PA6	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	P1.6
	PA7	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	P1.7
	PB0	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	P2.0
	PB1	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	P2.1
	PB2	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	P2.2
	PA12	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	DIB_IRQ
	PA15	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	P2_EXT
	PB3	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	P2.3
	PB4	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	P2.4
	PB5	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	P2.5
	PB6	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	P2.6
	PB7	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	P2.7

8.2. DMA configuration

DMA request	Stream	Direction	Priority
SPI1_RX	DMA1_Channel2	Peripheral To Memory	Low
SPI1_TX	DMA1_Channel3	Memory To Peripheral	Low

SPI1_RX: DMA1_Channel2 DMA request Settings:

Mode:	Normal
Peripheral Increment:	Disable
Memory Increment:	**Enable ***
Peripheral Data Width:	Byte
Memory Data Width:	Byte

SPI1_TX: DMA1_Channel3 DMA request Settings:

Mode:	Normal
Peripheral Increment:	Disable
Memory Increment:	**Enable ***
Peripheral Data Width:	Byte
Memory Data Width:	Byte

8.3. NVIC configuration

Interrupt Table	Enable	Preenmption Priority	SubPriority
Non maskable interrupt	true	0	0
Hard fault interrupt	true	0	0
System service call via SWI instruction	true	0	0
Pendable request for system service	true	0	0
System tick timer	true	0	0
DMA1 channel 2 and 3 interrupts	true	0	0
Flash global interrupt	unused		
RCC global interrupt	unused		
SPI1 global interrupt	unused		
USART1 global interrupt	unused		

*** User modified value**

9. Predefined Views - Category view : Current

10. Software Pack Report