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Envox d.o.o.

Sheet: /CM5 socket/
File: cm5_socket.kicad_sch

Title: BB3+ test mainboard

Size: A4 Date: 2025-10-12

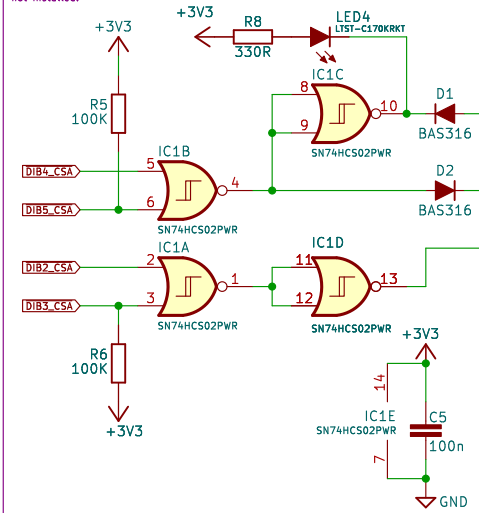
KiCad E.D.A. 9.0.5

Rev: r1B1

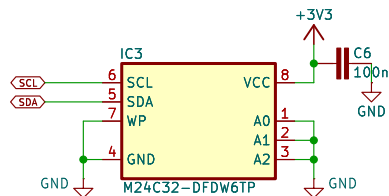
Id: 2/7

System 32-bit MCU

The System MCU is programmed so that the Master MCU first simultaneously activates DIB4_CSA and DIB5_CSA and then simultaneously activates DIB2_CSA and DIB3_CSA which will reset the System MCU and put it into bootloader mode. Simultaneous activation of two CSA (Chip Select) signals on the same SPI channel should never occur in normal operation. 100K pullup resistors ensure that RESET and BOOT0 are inactive if the Master MCU is not installed.

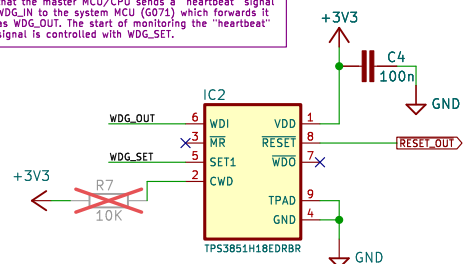


EEPROM

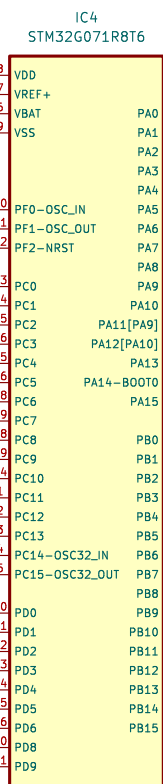
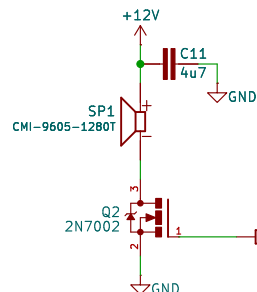


Watchdog/supervisor

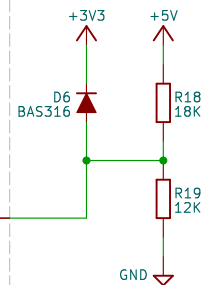
The watchdog signal is daisy chained in such a way that the master MCU/CPU sends a "heartbeat" signal WDG_IN to the system MCU (G071) which forwards it as WDG_OUT. The start of monitoring the "heartbeat" signal is controlled with WDG_SET.



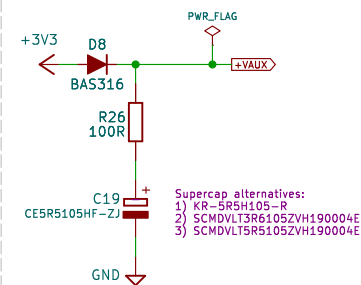
Audio out



5V monitoring

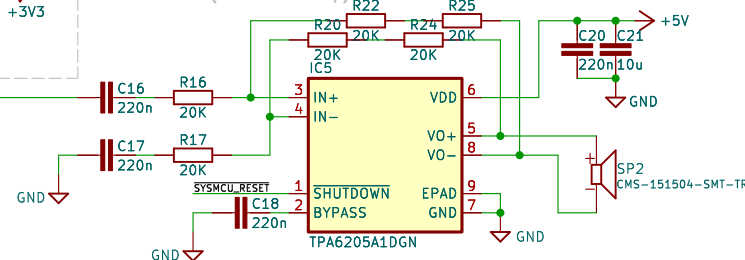


Supercap for RTC



Supercap alternatives:
1) KR-5R5H105-R
2) SCMDVLT3R6105ZVH190004E
3) SCMDVLT5R5105ZVH190004E

Audio out (AB-class amp)



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Sheet: /System MCU, Watchdog, EEPROM/

File: system_mcu.kicad_sch

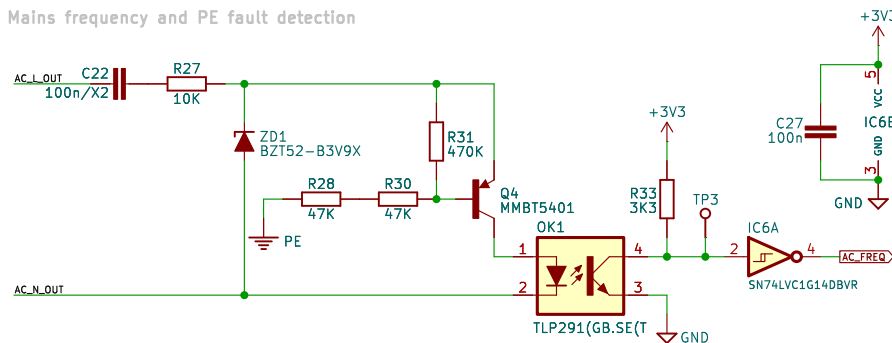
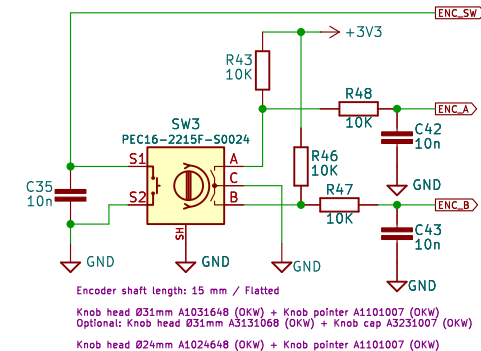
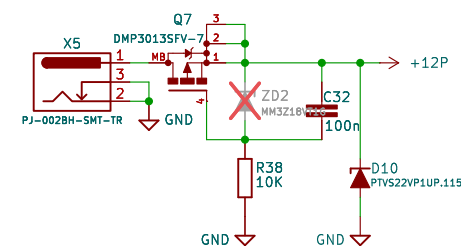
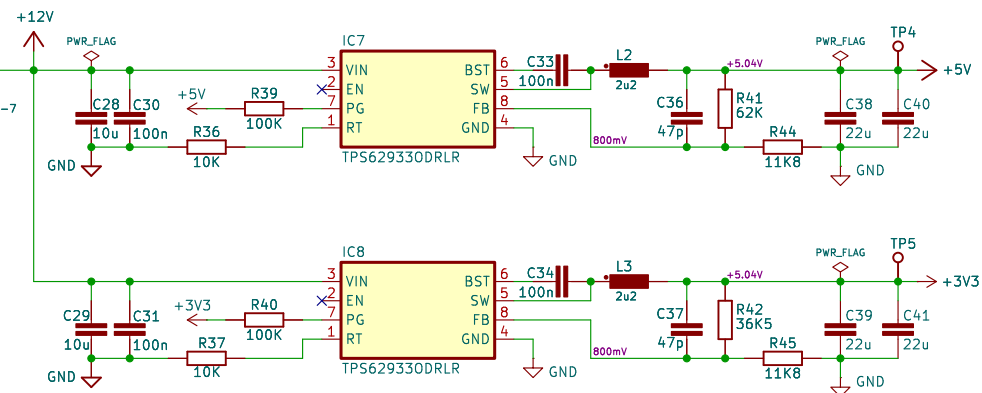
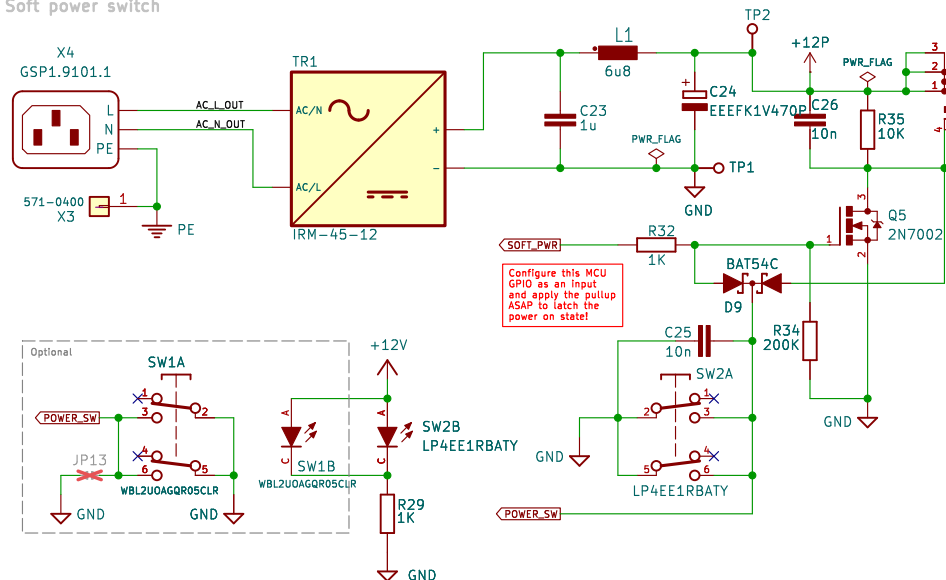
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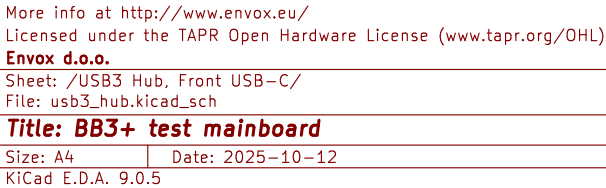
KiCad E.D.A. 9.0.5

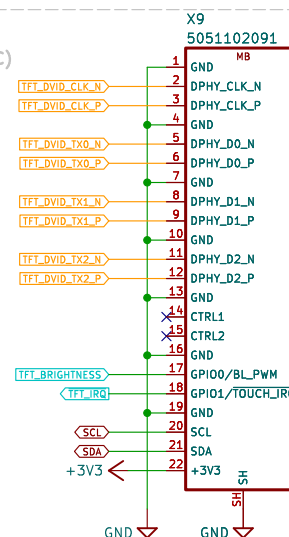
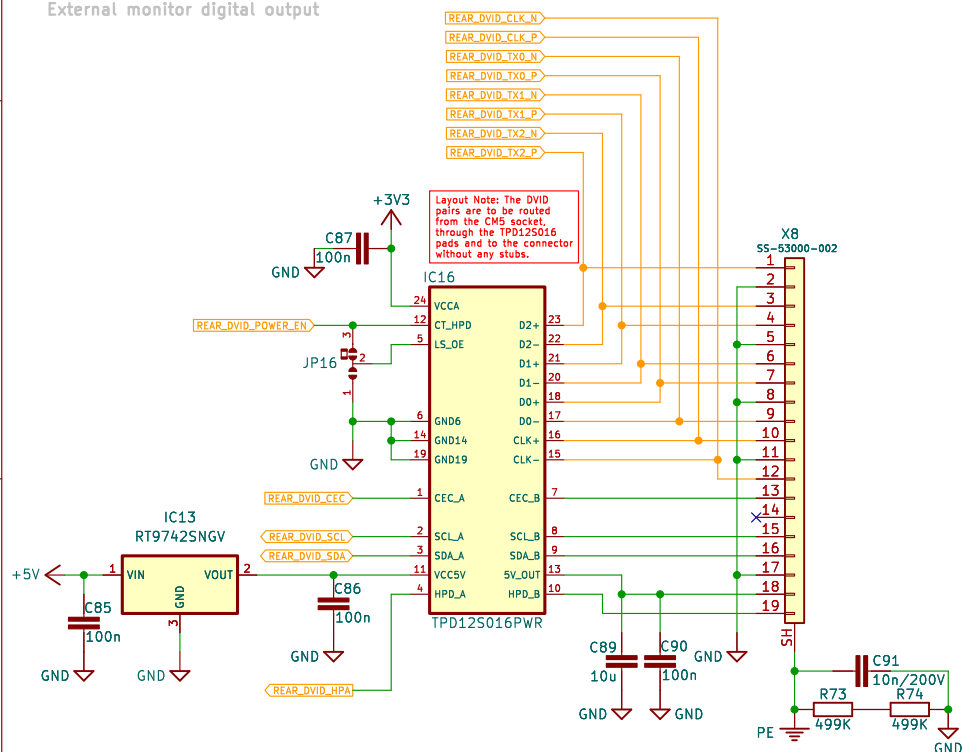
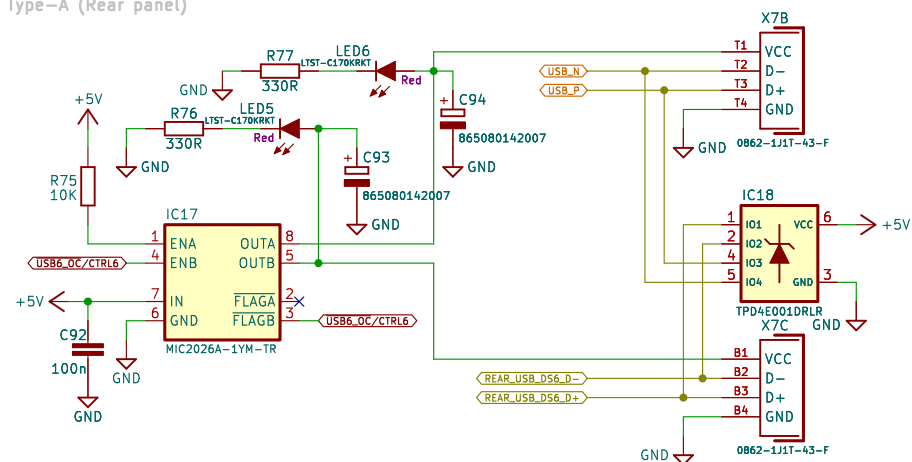
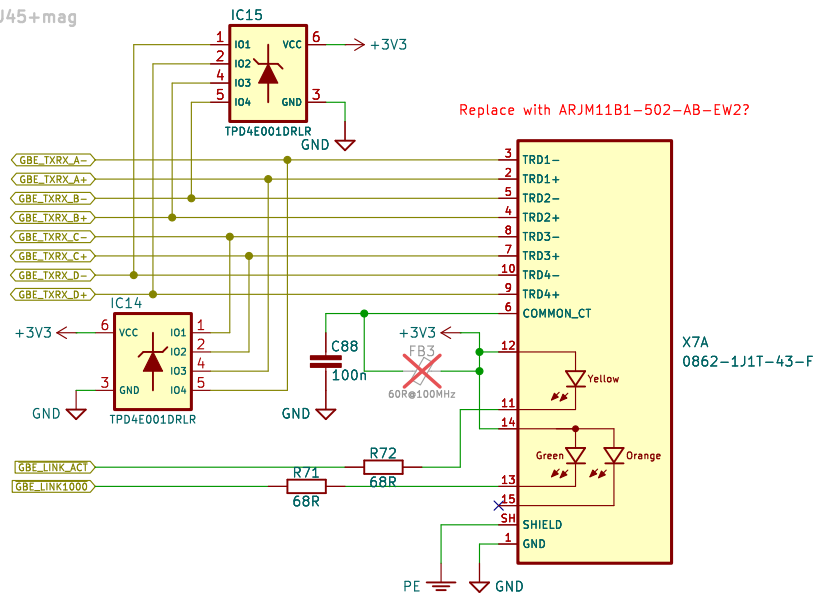
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