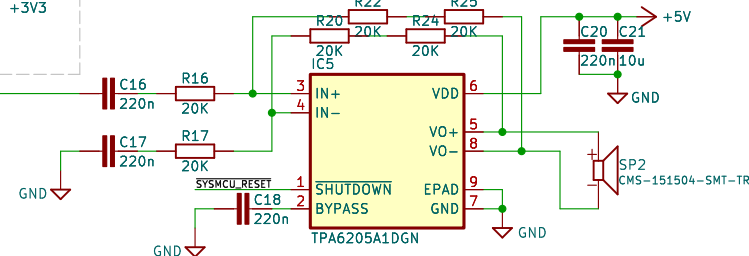


The System MCU is programmed so that the Master MCU first simultaneously activates DIB4_CSA and DIB5_CSA and then simultaneously activates DIB2_CSA and DIB3_CSA which will reset the System MCU and put it into bootloader mode. Simultaneous activation of two CSA (Chip Select) signals on the same SPI channel should never occur in normal operation. 100K pullup resistors ensure that RESET and BOOT0 are inactive if the Master MCU is not installed.

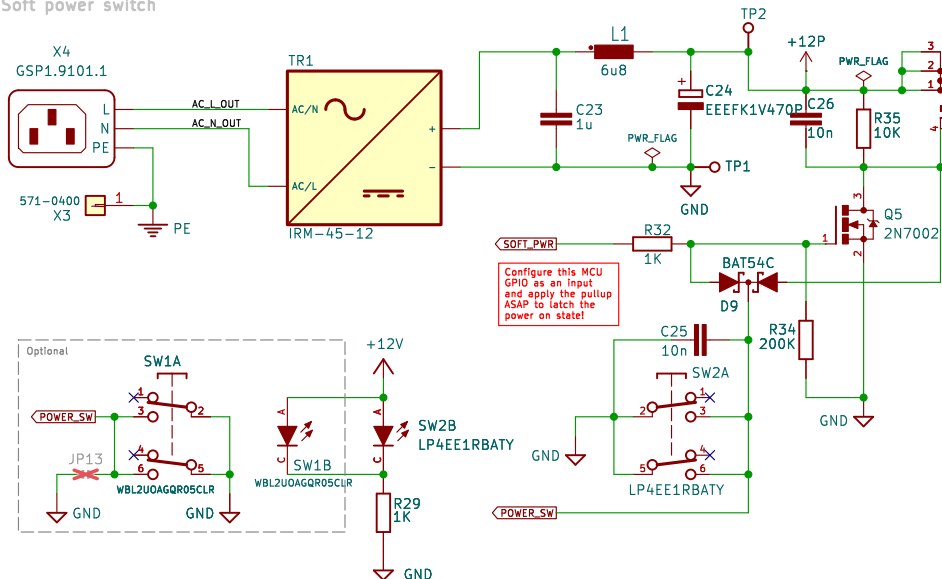


The watchdog signal is daisy chained in such a way that the master MCU/CPU sends a "heartbeat" signal WDG_IN to the system MCU (G071) which forwards it as WDG_OUT. The start of monitoring the "heartbeat" signal is controlled with WDG_SET.

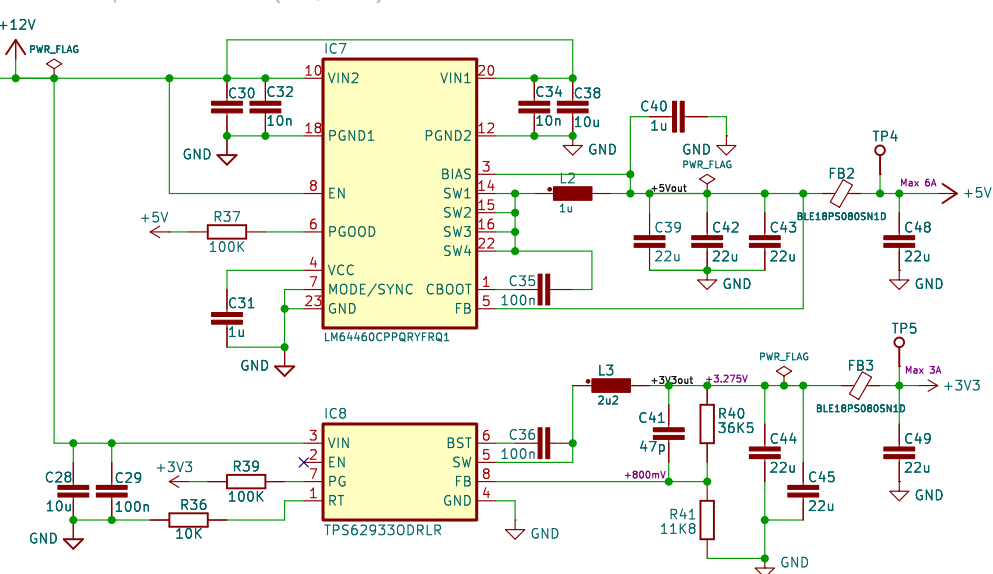


Id: 3/7

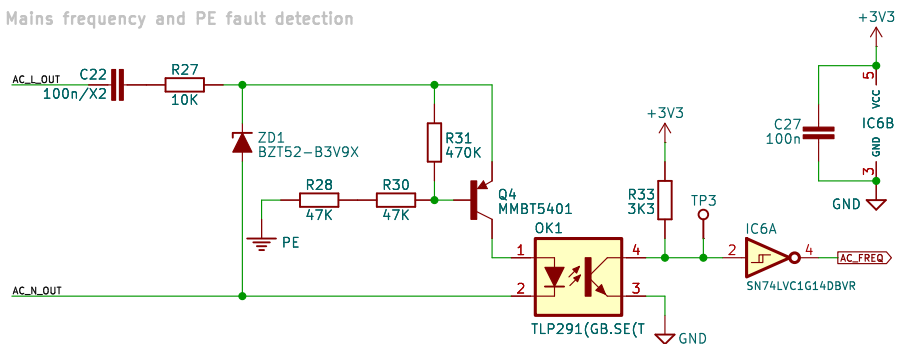
Soft power switch



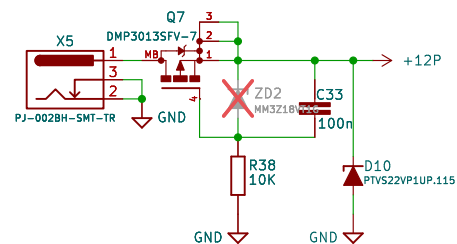
DC-DC stepdown converters (+5V, +3V3)



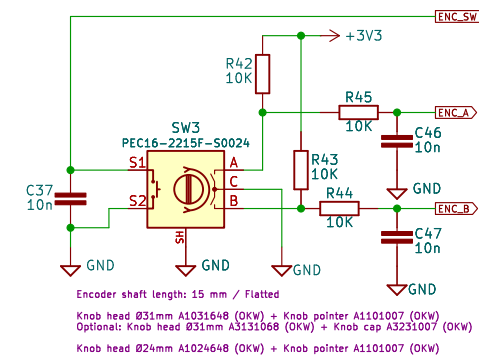
AC Mains frequency and PE fault detection



12Vdc aux



Encoder with switch



More info at <http://www.envox.eu/>
 Licensed under the TAPR Open Hardware License (www.tapr.org/OHL)

Envox d.o.o.

Sheet: /Miscellaneous/

File: misc.kicad_sch

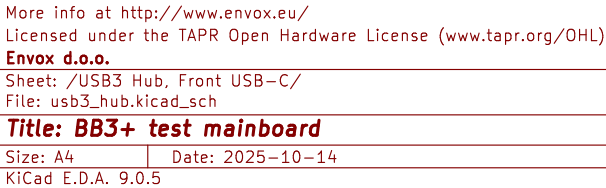
Title: BB3+ test mainboard

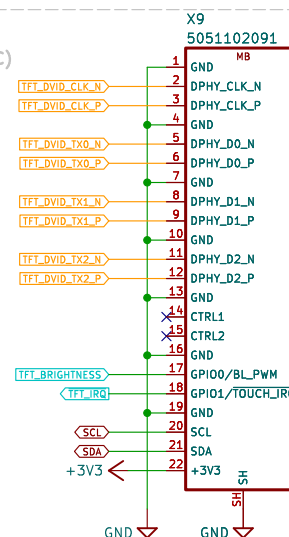
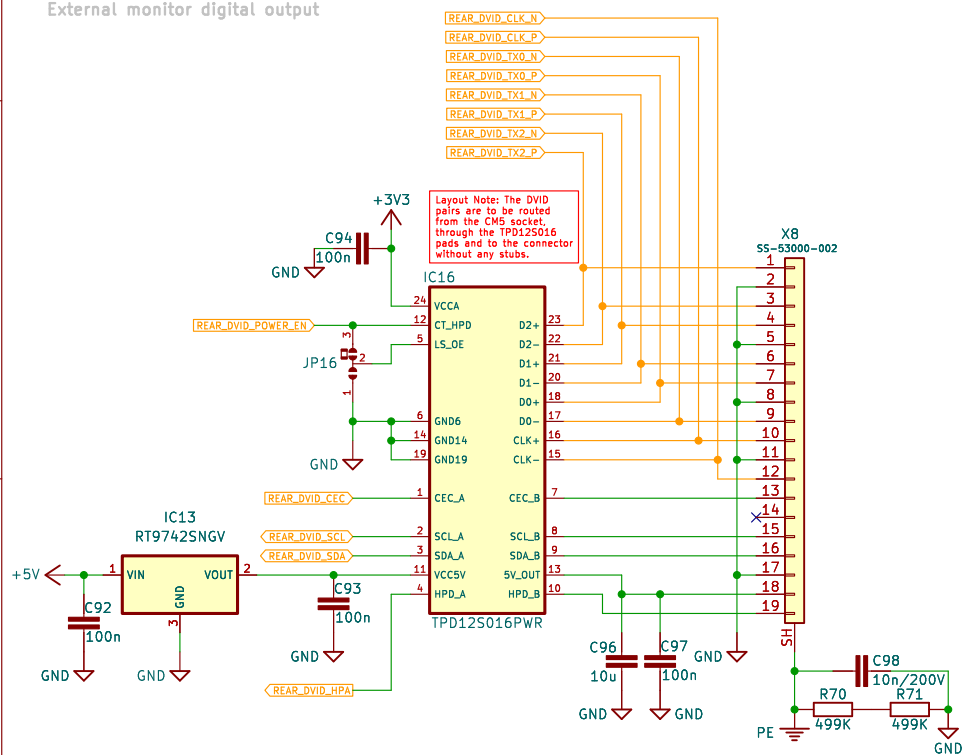
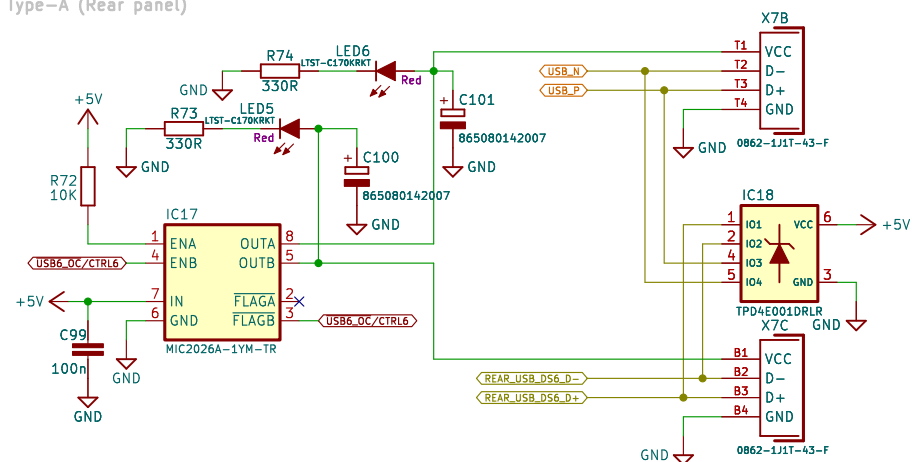
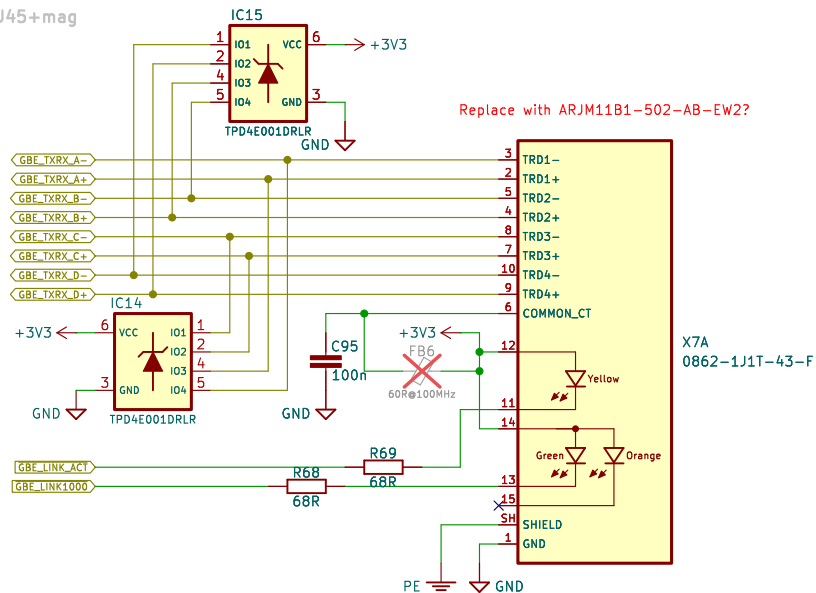
Size: A4 Date: 2025-10-14

KiCad E.D.A. 9.0.5

Rev: r1B1

Id: 4/7





KiCad E.D.A. 9.0.5	Id: 6/7
--------------------	---------

[illegible]

Id: 7/7