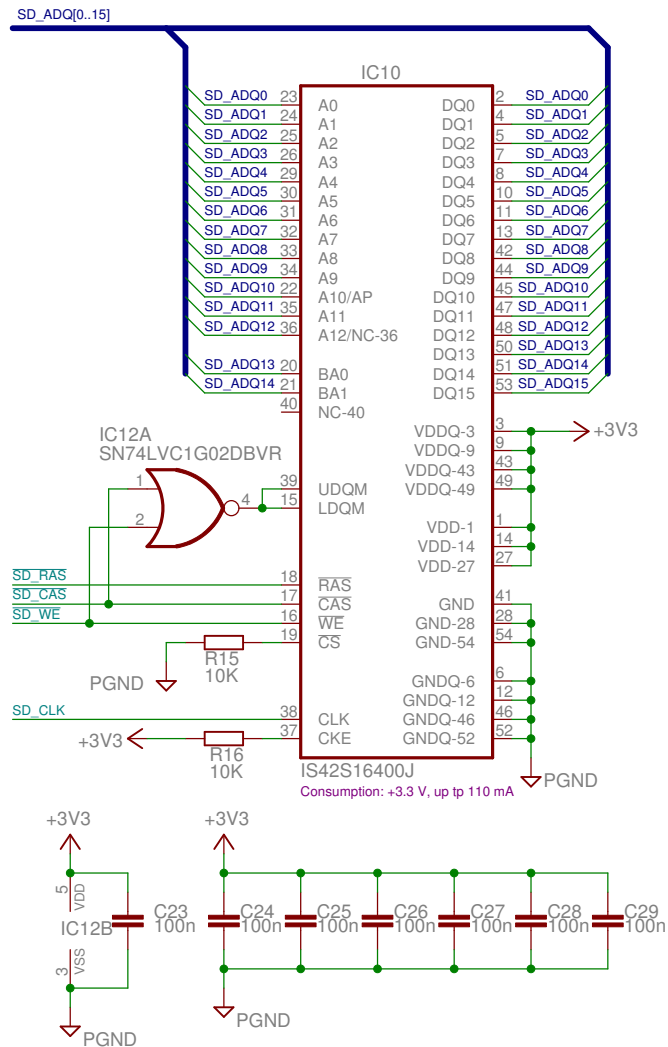
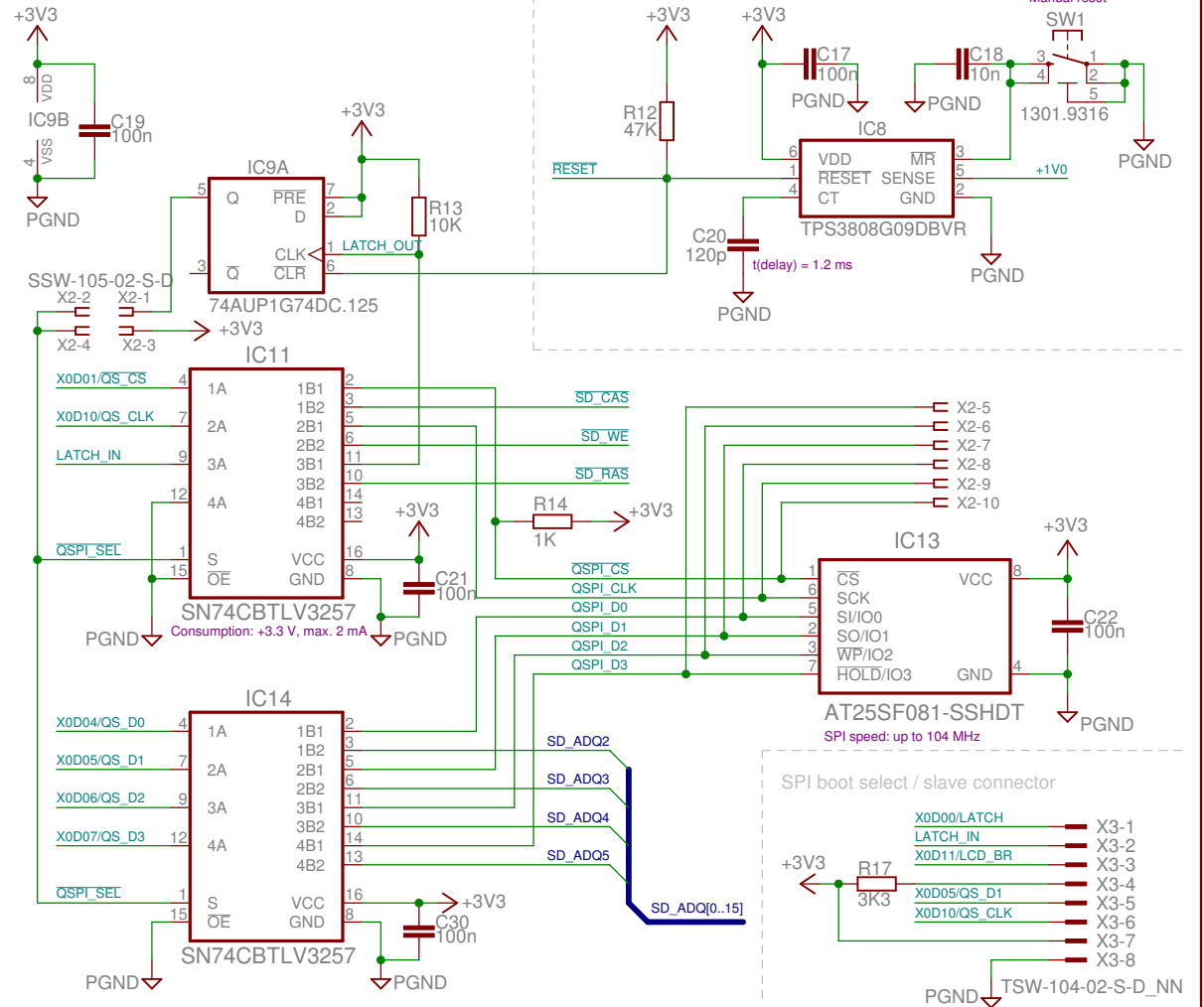




## SDRAM



## QSPI Flash memory



Pin	S=0 A -> B1	S=1 A -> B2
X0D00	LATCH	SD_RAS
X0D01	QSPI_CS	SD_CAS
X0D04	QSPI_D0	SD_ADQ2
X0D05	QSPI_D1	SD_ADQ3
X0D06	QSPI_D2	SD_ADQ4
X0D07	QSPI_D3	SD_ADQ5
X0D10	QSPI_CLK	SD_WE

Ctrl.	X2	Boot mode	X3
Int.	1 - 2 (Def.)	QSPI	1 - 2 (Def.)
Ext.	3 - 4	Slave SPI	3 - 4

SDRAM, QSPI lines muxing and flash, Reset

TITLE: EEZ XM0S eval r1B5

Document Number:

REV:

Date: 07. 03. 2018. 17:43

Sheet: 2/5

