



Pin	S=0 A -> B1	S=1 A -> B2
X0D00	SD_RAS	LATCH
X0D01	SD_CAS	QSPI_CS
X0D04	SD_ADQ2	QSPI_D0
X0D05	SD_ADQ3	QSPI_D1
X0D06	SD_ADQ4	QSPI_D2
X0D07	SD_ADQ5	QSPI_D3
X0D10	SD_WE	QSPI_CLK

	X2	Boot mode	X3
Int.	1 - 2	QSPI	1 - 2
Ext.	3 - 4	Slave SPI	3 - 4

SDRAM, QSPI lines muxing and flash, Reset

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